

# Kaiming Huang

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## RESEARCH STATEMENT

My primary area of expertise lies in advancing the field of software security, program hardening, static/dynamic program analysis, automatic vulnerability detection, exploit generation, and reverse engineering. My research is driven by the goal of contributing to the development of robust, effective, and efficient defenses against memory-related vulnerabilities. To be more specific, my research aims to ensure the security of systems and software while maintaining cost-effectiveness. I'm dedicated to addressing the evolving challenges in software security that arise from emerging features and the continuous development of programs. My ultimate objective is to strengthen systems against the ever-present cyber threats.

## EDUCATION

<b>The Pennsylvania State University</b> <i>Doctor of Philosophy, Computer Science and Engineering</i> <ul style="list-style-type: none"><li>• <b>Advisor:</b> Dr. Trent Jaeger</li></ul>	Aug. 2020 - Aug. 2024 State College, PA, USA
<b>The Pennsylvania State University</b> <i>Master of Science in Computer Science and Engineering</i> <ul style="list-style-type: none"><li>• <b>Advisor:</b> Dr. Trent Jaeger</li></ul>	Aug. 2018 – Jul. 2020 State College, PA, USA
<b>Northeastern University</b> <i>Bachelor of Engineering in Information Security</i>	Oct. 2014 – Jun. 2018 Shenyang, China

## PUBLICATION

<b>OPTISAN: Using Multiple Spatial Error Defenses to Optimize Stack Memory Protection within a Budget</b> <i>Rahul George, Mingming Chen, <b>Kaiming Huang</b>, Zhiyun Qian, Thomas La Porta, Trent Jaeger.</i>	USENIX 2024
<b>Comprehensive Memory Safety Validation: An Alternative Approach to Memory Safety</b> <i><b>Kaiming Huang</b>, Mathias Payer, Zhiyun Qian, Jack Sampson, Gang Tan, Trent Jaeger.</i>	IEEE S&P
<b>Assessing the Impact of Efficiently Protecting Ten Million Stack Objects from Memory Errors Comprehensively</b> <i><b>Kaiming Huang</b>, Jack Sampson, Trent Jaeger.</i>	SecDev 2023
<b>Evolving Operating System Kernels Towards Secure Kernel-Driver Interfaces</b> <i>Anton Burtsev, Vikram Narayanan, Yongzhe Huang, <b>Kaiming Huang</b>, Gang Tan, Trent Jaeger.</i>	HotOS 2023
<b>KSplit: Automating Device Driver Isolation</b> <i>Yongzhe Huang, Vikram Narayanan, David Detweiler, <b>Kaiming Huang</b>, Gang Tan, Trent Jaeger, Anton Burtsev.</i>	OSDI 2022
<b>The Taming of the Stack: Isolating Stack Data from Memory Errors</b> <i><b>Kaiming Huang</b>, Yongzhe Huang, Mathias Payer, Zhiyun Qian, Jack Sampson, Gang Tan, Trent Jaeger.</i>	NDSS 2022
<b>DataGuard: Guarded Pages for Augmenting Stack Object Protections</b> <i><b>Kaiming Huang</b>.</i>	Master Thesis
<b>Employing attack graphs for intrusion detection</b> <i>Frank Capobianco, Rahul George, <b>Kaiming Huang</b>, Trent Jaeger, Srikanth Krishnamurthy, Zhiyun Qian, Mathias Payer, Paul Yu.</i>	NSPW 2019

## TALK

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### **The Taming of the Stack: Isolating Stack Data from Memory Errors**

*GLSD 2021 and CRA Seminar in July 2021, NDSS 2022 in April 2022*

## EXPERIENCES & PROJECTS

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### **Samsung Research America**

Security Research Internship, May. 2022 – Aug. 2022

- Deploying Intel CETs to Samsung BIOS packages.
- Analyzing TOCTTOU issue in Samsung BIOS SMM handler.
- Emulating Samsung BIOS SMM for booting in QEMU to launch fuzz testing.

### **Isolating Memory Objects from Memory Errors**

Research Assistant, Jan. 2020 – Present

- Examined shortcomings of existing protection schemes on stack and heap data
- Leveraged static analysis and guided symbolic execution for verifying the safety of stack and heap memory objects against spatial, type, and temporal memory errors.
- Applied runtime isolation for safe stack and heap objects and remove unnecessary runtime checks.

### **Combining Memory Safety Validation and Taint Analysis**

Research Assistant, Jan. 2024 – Present

- Identified the shortcoming of existing taint analyses are not aware of memory errors.
- Leveraged memory safety validation for augmenting the taint analysis.

### **Bridging Bugs to Exploit through Automatic Exploit Generation**

Research Assistant, Feb. 2021 – Present

- Designed the intermediate representation for synthesizing exploits in compiler backend.
- Investigated and Designed the methods for extracting primitives in given vulnerabilities.

### **Adding Security Plug-ins into IDEs**

Research Assistant, Feb. 2021 – May. 2022

- Mentored 2 undergraduate students on adding security checks at source code level through plug-ins of CLion.
- Plug-ins designed mainly focused on spatial memory errors.

### **Detecting and Preventing DFI violations in BOPC Attack**

Research Assistant, Feb. 2019 – Jun. 2019

- Identified possible Data-Flow Integrity violations in Block-Oriented Programming attack (angr, IDA).
- Designed a lightweight DFI checking for preventing BOPC exploits.
- Augmented BOPC to generate exploits with DFI deployed.

### **Identifying Potential Step-stone Gadgets in Memory Attack**

Research Assistant, Jul. 2019 – Dec. 2019

- Used fuzzing and symbolic execution to identify reachable/exploitable objects through memory errors.
- Designed an approach for chaining potentially exploitable memory objects (gadgets) for synthesizing more powerful attacks through initial memory error.

## Teaching

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**Teaching Assistant** Software Security (Spring 2022), Python (Spring 2024)

**Student Mentor** Mentored 3 Undergraduate Students for thesis

## Awards & Services

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**First Prize Scholarship** Software College, Northeastern University

**Second Prize** Chinese Mathematics Modeling Contest for College Students

**First Prize** Mathematical Modeling Contest of Northeastern University

**Reviewer** IEEE Transaction of Computers, IEEE Transaction on Industrial Informatics, IET Electronic Letters **External**

**Reviewer** USENIX 2019, IEEE S&P 2020, NDSS 2020, USENIX 2020, CCS 2020, NDSS 2021, NDSS 2022, CCS 2023, USENIX 2023

## SKILLS

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**Languages** : C, C++, Python, Scala, Java, JavaScript, HTML/CSS, SQL, Go, Rust

**Tools** : LLVM, angr, KLEE, IDA, AFL, Sanitizers, GDB, Burp Suite, Metasploit, Wireshark, Microsoft Office, Latex