

Most processes store interrupt handling in low order memory

Memory reference registers are use to prevent process from impeding each other in memory

Address binding is symbolic address

Logical address - generated by the cpu

Physical address - address seen by the memory unit

(MMU) Memory management unit

Translate between logical and physical addresses

Dynamic linking

Static linking - system libraries and program code combined by loader into the binary program image

Dynamic linking - linking postponed until execution time

Swapping

Take program out of memory and put into a backing store, a fast disl large enough to accomodate  
copies of all memory images for all users