

1



Pin configuration diagram for the ADXL345 in 8-pin DIP package. The diagram shows a central chip with pins numbered 1 to 8. Pin 1 is VBUS, Pin 2 is VBAT, Pin 3 is STAT, Pin 4 is SISO, Pin 5 is SCK, Pin 6 is CS, Pin 7 is V50, Pin 8 is V33A. A ground symbol is shown connected to pin 8.

---



---

---