

LimiFrog Extension Connector

Refer to the *STM32L476 datasheet, Table 15*

for exhaustive list of alternate functions available on each STM32 GPIO routed to the extension port and for voltage tolerance (5V max or 3V6 max, depending on I/Os)

LimiFrog « Prototype version »

	Signal Available	Notes
Position 1	GPIO_PA5	
Position 2	GPIO_PA6	
Position 3	GPIO_PA7	
Position 4	MICOUT	Analog output from microphone
Position 5	GPIO_PC10	
Position 6	GPIO_PC11	
Position 7	GPIO_PC12	shared with PIO from BTLE module (BTLE not driving by default)
Position 8	VCC_NUM	3V (buck-regulated) power supply (same as used on board)
Position 9	GPIO_PB8	
Position 10	GPIO_PB9	
Position 11	GND	Ground power supply

LimiFrog v0.1

	Signal Available	Notes
Position 1	GPIO_PA6	shared with PIO from BTLE module (BTLE not driving by default)
Position 2	MICOUT	Analog output from microphone, DC-biased at $VCC/2 = 1.5V$
Position 3	GPIO_PA4	
Position 4	GPIO_PA3	
Position 5	GPIO_PA2	
Position 6	VCC_LDO	Independent voltage from LDO, 3V3 nominal
Position 7	GPIO_PA1	
Position 8	GPIO_PA0	
Position 9	GPIO_PB8	
Position 10	GPIO_PB9	
Position 11	GND	Ground power supply

Interfaces and signals that can be made available over pins GPIO_xx of the extension connector include : I2C, 2xUART, USART/SPI, CAN bus, PWM, ADC, GPIO, IRQ, ADC, DAC, WakeUp, etc.