# Modular Analysis

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February 19, 2024

# 1 Syntax and Semantics

### 1.1 Abstract Syntax

Figure 1: Abstract syntax of the language.

## 1.2 Operational Semantics

Figure 2: Definition of the semantic domains.

 $(e,\sigma) \Downarrow v$ 

$$\frac{\text{ID}}{\sigma(x) = v} \quad \frac{\text{RECID}}{\sigma(x) = v} \quad \frac{\sigma(x) = \mu \ell.v}{(x, \sigma) \Downarrow v [\mu \ell.v / \ell]} \quad \frac{\text{Fn}}{(\lambda x.e, \sigma) \Downarrow \langle \lambda x.e, \sigma \rangle} \quad \frac{\text{App}}{(e_1, \sigma) \Downarrow \langle \lambda x.e, \sigma_1 \rangle} \quad \frac{(e_2, \sigma) \Downarrow v_2 \quad (e, (x, v_2) :: \sigma_1) \Downarrow v_2}{(e_1 e_2, \sigma) \Downarrow v}$$

$$\frac{\text{Link}}{(e_1,\sigma) \Downarrow \sigma_1 \quad (e_2,\sigma_1) \Downarrow v} \\ \frac{(e_1 \rtimes e_2,\sigma) \Downarrow v}{(\varepsilon,\sigma) \Downarrow v} \qquad \frac{\text{Empty}}{(\varepsilon,\sigma) \Downarrow \bullet} \qquad \frac{\text{Bind}}{\ell \notin \text{FLoc}(\sigma) \quad (e_1,(x,\ell) :: \sigma) \Downarrow v_1 \quad (e_2,(x,\mu\ell.v_1) :: \sigma) \Downarrow \sigma_2}{(x=e_1;e_2,\sigma) \Downarrow (x,\mu\ell.v_1) :: \sigma_2}$$

Figure 3: The big-step operational semantics.

We use the locally nameless representation, and enforce that all values be *locally closed*. As a consequence, the big-step operational semantics will be *deterministic*, no matter what  $\ell$  is chosen in the Bind rule.

#### 1.3 Adding Memory

The first step towards abstraction is reformulating the semantics into a version with memory.

Figure 4: The equivalent small-step operational semantics.

Figure 5: Definition of the semantic domains with memory.

Figure 6: The small-step operational semantics with memory.

## 1.4 Reconciling the Two Semantics

We need to prove that the two semantics simulate each other. Thus, we need to define a notion of equivalence between the two semantic domains.

Some useful lemmas.

 $w \sim_f v, m$ 

$$\underbrace{\frac{\text{Eq-ConsFree}}{\ell \notin \text{dom}(f) \quad \ell \notin \text{dom}(m) \quad \sigma \sim_f \sigma'}_{(x,\ell) :: \sigma \sim_f (x,\ell) :: \sigma'} \quad \underbrace{\frac{\text{Eq-ConsBound}}{f(\ell) = \ell' \quad \ell' \in \text{dom}(m) \quad \sigma \sim_f \sigma'}_{(x,\ell) :: \sigma \sim_f (x,\ell') :: \sigma'}$$

$$\frac{\text{Eq-ConsWVal}}{m(\ell') = v' \quad w \sim_f v' \quad \sigma \sim_f \sigma'} \quad \frac{\text{Eq-Clos}}{\sigma \sim_f \sigma'} \quad \frac{\text{Eq-Rec}}{\sigma \sim_f \sigma'} \quad \frac{m(\ell') = v' \quad v \sim_{f[\ell \mapsto \ell']} v'}{\langle \lambda x. e, \sigma \rangle \sim_f \langle \lambda x. e, \sigma' \rangle}$$

Figure 7: The equivalence relation between weak values in the original semantics and values in the semantics with memory.  $f \in \text{Loc} \xrightarrow{\text{fin}} \text{Loc}$  tells what the free locations in w that were *opened* should be mapped to in memory. The memory component in the right-hand side is omitted.

**Lemma 1.1** (Free locations not in f are free in memory).

$$w \sim_f v', m \Rightarrow m|_{\mathrm{FLoc}(w)-\mathrm{dom}(f)} = \bot$$

Lemma 1.2 (Equivalence is preserved by extension of memory).

$$w \sim_f v', m \text{ and } m \sqsubseteq m' \text{ and } m'|_{\mathrm{FLoc}(w)-\mathrm{dom}(f)} = \bot \Rightarrow w \sim_f v', m$$

**Lemma 1.3** (Equivalence only cares about f on free locations).

$$w \sim_f v', m \text{ and } f|_{\mathrm{FLoc}(w)} = f|_{\mathrm{FLoc}(w)} \Rightarrow w \sim_{f'} v', m$$

Lemma 1.4 (Extending equivalence on free locations).

$$w \sim_f v', m \text{ and } \ell \notin \text{dom}(f) \text{ and } \ell \notin \text{dom}(m) \Rightarrow \forall u', w \sim_{f[\ell \mapsto \ell]} v', m[\ell \mapsto u']$$

Lemma 1.5 (Substitution of values).

$$w \sim_f v', m \text{ and } f(\ell) = \ell' \text{ and } m(\ell') = u' \text{ and } u \sim_{f-\ell} u', m \Rightarrow w[u/\ell] \sim_{f-\ell} v', m$$

Lemma 1.6 (Substitution of locations).

$$w \sim_f v', m \text{ and } \ell \in \text{dom}(f) \text{ and } \nu \notin \text{FLoc}(w) \Rightarrow w[\nu/\ell] \sim_{f \circ (\nu \leftrightarrow \ell)} v', m$$

It turns out, we only need to reason about what free locations are in the continuation of the semantics of Figure 6. Thus, we can formulate a big-step semantics as in Figure 8 and reason about the equivalence between the big-step semantics directly.

Then we can prove the following.

**Definition 1.1** (Equivalence of configurations).

$$\sigma \sim \sigma', m, L \triangleq \sigma \sim_{\perp} \sigma', m \text{ and } FLoc(\sigma) \subseteq L$$

Theorem 1.1 (Equivalence of semantics).

$$\sigma \sim \sigma', m, L \text{ and } (e, \sigma) \Downarrow v \Rightarrow \exists v', m', L' : v \sim v', m', L' \text{ and } (e, \sigma', m, L) \Downarrow (v', m', L')$$
$$\sigma \sim \sigma', m, L \text{ and } (e, \sigma', m, L) \Downarrow (v', m', L') \Rightarrow \exists v : v \sim v', m', L' \text{ and } (e, \sigma) \Downarrow v$$

# 2 Generating and Resolving Events

#### 2.1 Without Memory

Now we formulate the semantics for generating events. The point of this semantics is that we are expressing a function that interacts with a stream of answers to external events as a data structure.

$$(e, \sigma, m, L) \downarrow (v, m, L)$$

$$\begin{array}{l} \text{ID} \\ \ell = \sigma(x) \quad v = m(\ell) \\ \hline (x, \sigma, m, L) \Downarrow (v, m, L) \end{array} \quad \begin{array}{l} \text{Fn} \\ \hline (\lambda x.e, \sigma, m, L) \Downarrow (\langle \lambda x.e, \sigma \rangle, m, L) \end{array}$$

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$$(e_1,\sigma,m,L) \Downarrow (\langle \lambda x.e,\sigma_1 \rangle,m_1,L_1) \\ (e_2,\sigma,m_1,L_1) \Downarrow (v_2,m_2,L_2) \\ \hline (e_1,\sigma,m,L) \Downarrow (\langle \lambda x.e,\sigma_1 \rangle,m_1,L_1) \\ \hline (e_1,\sigma,m,L) \Downarrow (e_1,\sigma,m,L) \\ \hline (e_1,\sigma,m,L) \\ (e_1,\sigma,m,L)$$

$$\frac{\text{Link}}{(e_1,\sigma,m,L) \Downarrow (\sigma_1,m_1,L_1)} \quad (e_2,\sigma_1,m_1,L_1) \Downarrow (v,m',L')}{(e_1 \rtimes e_2,\sigma) \Downarrow (v,m',L')} \qquad \frac{\text{Empty}}{(\varepsilon,\sigma,m,L) \Downarrow (\bullet,m,L)}$$

$$\frac{\text{BIND}}{\ell \notin \text{dom}(m) \cup L \quad \left(e_1, (x, \ell) :: \sigma, m, L \cup \{\ell\}\right) \Downarrow \left(v_1, m_1, L_1\right) \quad \left(e_2, (x, \ell) :: \sigma, m_1[\ell \mapsto v_1], L_1 - \{\ell\}\right) \Downarrow \left(\sigma_2, m', L'\right)}{\left(x = e_1; e_2, \sigma, m, L\right) \Downarrow \left((x, \ell) :: \sigma_2, m', L'\right)}$$

Figure 8: The big-step operational semantics with memory.

Figure 9: Definition of the semantic domains with events. All other semantic domains are equal to Figure 2.

We redefine how to read weak values given an environment.

Then we need to add only one rule to the semantics in Figure 3 for the semantics to incorporate events.

$$\frac{\text{AppEvent}}{(e_1,\sigma) \Downarrow E} \quad (e_2,\sigma) \Downarrow v \\ \hline (e_1 \, e_2,\sigma) \Downarrow \text{Call}(E,v)$$

Now we need to formulate the *event resolution* rules. The event resolution rule  $w\downarrow_{\sigma_0} W$ , given an answer  $\sigma_0$  to the Init event, resolves all events within w to obtain W. Note that  $\mathrm{FLoc}(\sigma_0)\cap\mathrm{FLoc}(w)=\emptyset$  is required for the resolution to work.

For the event resolution to be sound, we expect the following theorem to hold.

#### Theorem 2.1 (Soundness of resolution).

$$\sigma \downarrow_{\sigma_0} \Sigma$$
 and  $(e, \sigma) \Downarrow v$  and  $(e, \Sigma) \Downarrow V \Rightarrow v \downarrow_{\sigma_0} V$ 

Now we present the definition for resolution.

Note that substitution of free locations that happens when unfolding a recursive value may generate multiple copies of the same event. In our simple module language, resolution is not impacted by neither the order nor the redundancy of resolution. However, in the future, to support stateful events which depend on both the

$$w\downarrow_{\sigma_0} W$$

$$\frac{\text{R-Init}}{\mathsf{Init} \downarrow \sigma_0} \quad \frac{E \cdot \mathsf{READ}}{\mathsf{Read}(E,x) \downarrow V} \quad \frac{E \cdot \mathsf{DREC}}{\mathsf{Read}(E,x) \downarrow V} \quad \frac{E \cdot \mathsf{DREC}}{\mathsf{Read}(E,x) \downarrow V[\mu\ell.V/\ell]} \quad \frac{E \cdot \mathsf{DREC}}{\mathsf{Read}(E,x) \downarrow V[\mu\ell.V/\ell]} \quad \frac{E \cdot \mathsf{CALLV}}{\mathsf{Call}(E,v) \downarrow V'} \quad \frac{E \cdot \mathsf{DRED}}{\mathsf{Call}(E,v) \downarrow V'} \quad \frac{E \cdot \mathsf{DRED}}{\mathsf{Call$$

$$\frac{\text{R-CallE}}{E \downarrow E'} \underbrace{v \downarrow V}_{\text{Call}(E,v) \downarrow \text{Call}(E',V)} \quad \frac{\text{R-Nil}}{\bullet \downarrow \bullet} \quad \frac{\text{R-ConsLoc}}{(x,\ell) :: \sigma \downarrow (x,\ell) :: \Sigma} \quad \frac{\text{R-ConsWVal}}{w \downarrow W} \underbrace{\sigma \downarrow \Sigma}_{(x,w) :: \sigma \downarrow (x,W) :: \Sigma}$$

Figure 10: The resolution relation. The subscript  $\sigma_0$  in  $\downarrow_{\sigma_0}$  is omitted for brevity.

order and redundancy, we must assign *unique numbers* to the events. Furthermore, for abstraction of the data structure, it is much easier if everything is threaded through the memory through memory locations. This motivates us to:

- 1. Lift  $\downarrow$  in the case for memory in a way that is *compatible* with the equivalence relation  $\sim$ .
- 2. Formulate a semantics where events are assigned a unique number, and their dependencies are tracked by a *memory for events*.

We expect by doing so, we may obtain a sound approximation for the semantics by simply abstracting the evaluation relation and the resolution relation.

### 2.2 With Memory

# 2.3 With Event Memory