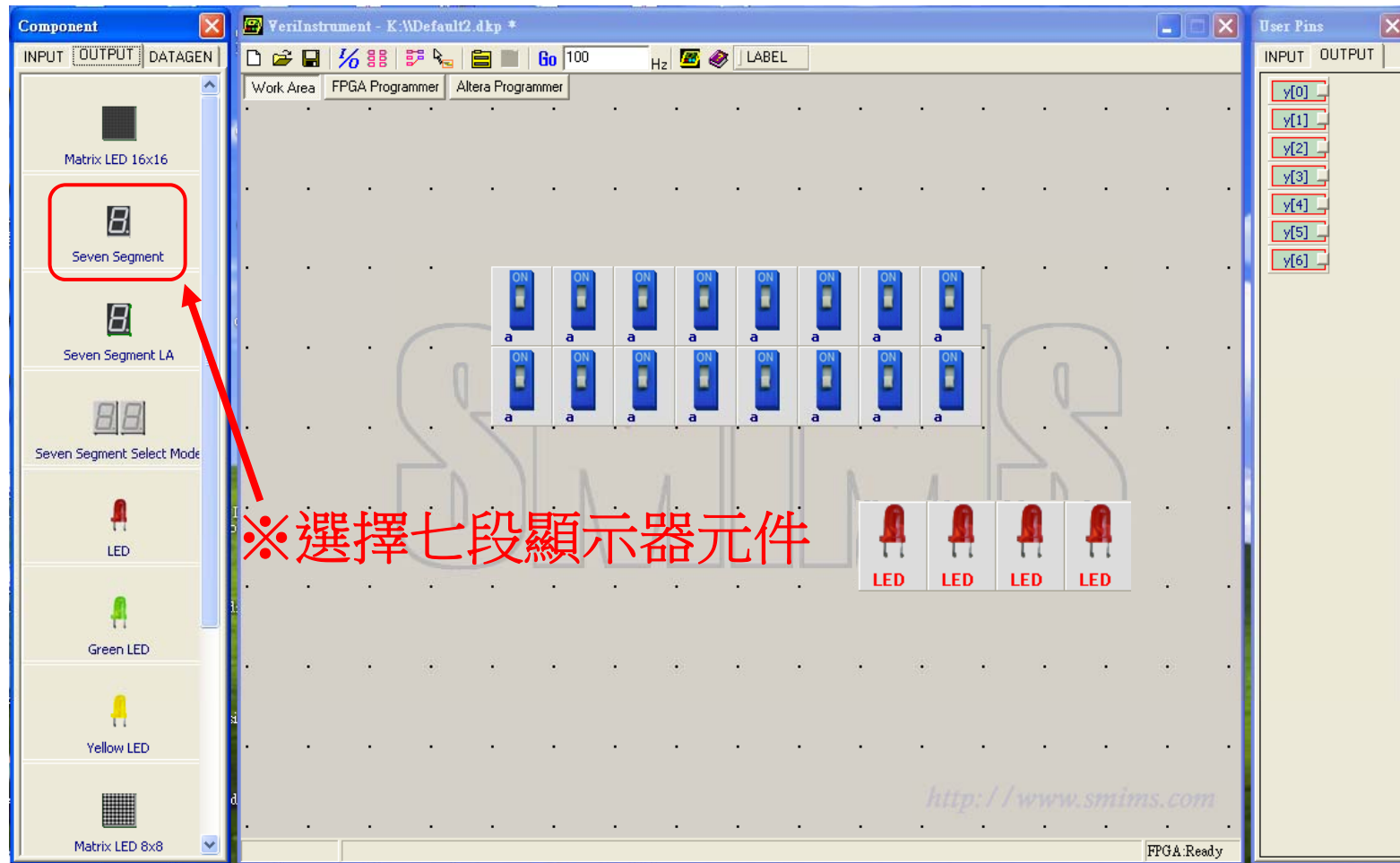


# 七段顯示器元件

CVLSI

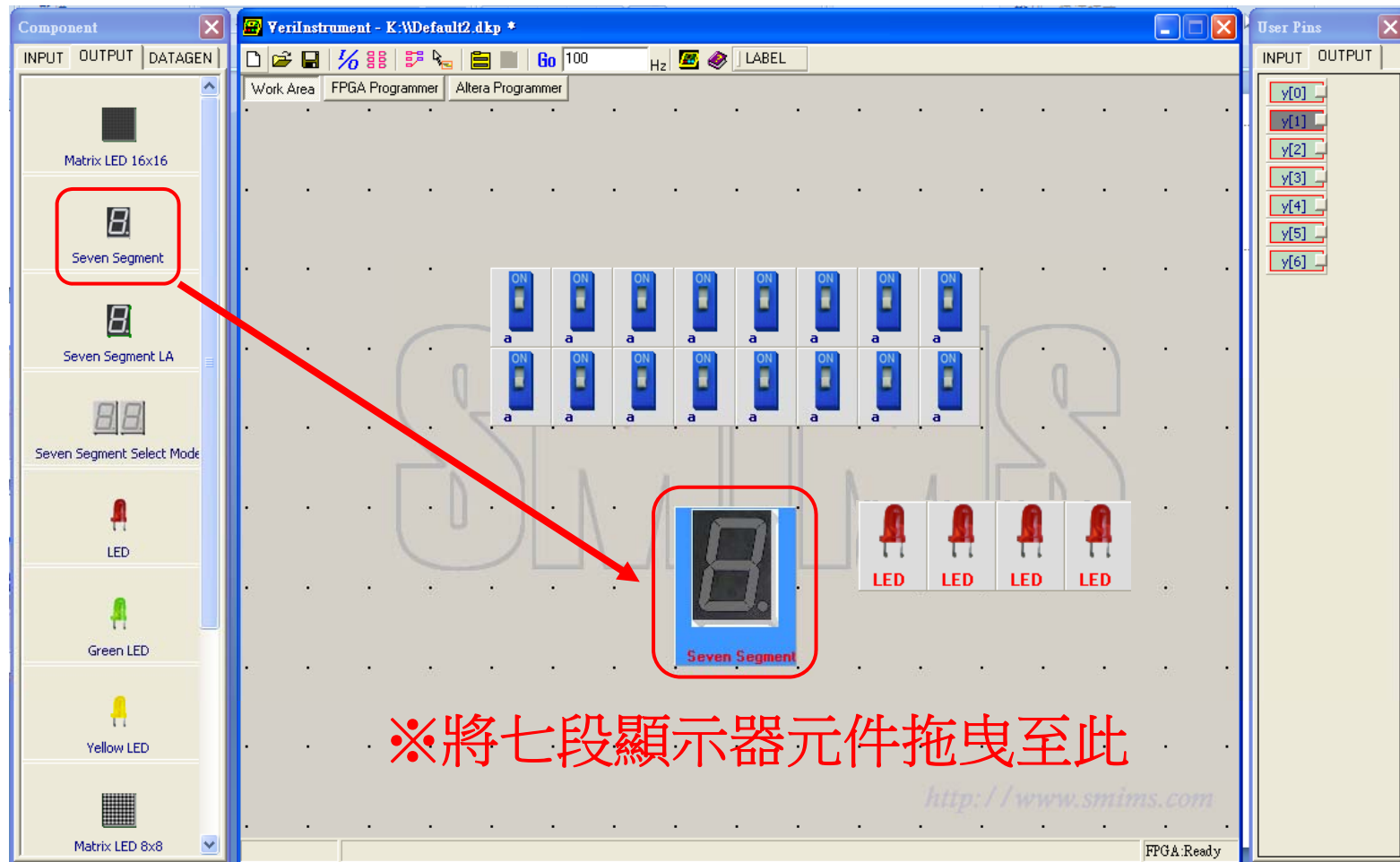
601系統晶片實驗室

# 七段顯示器

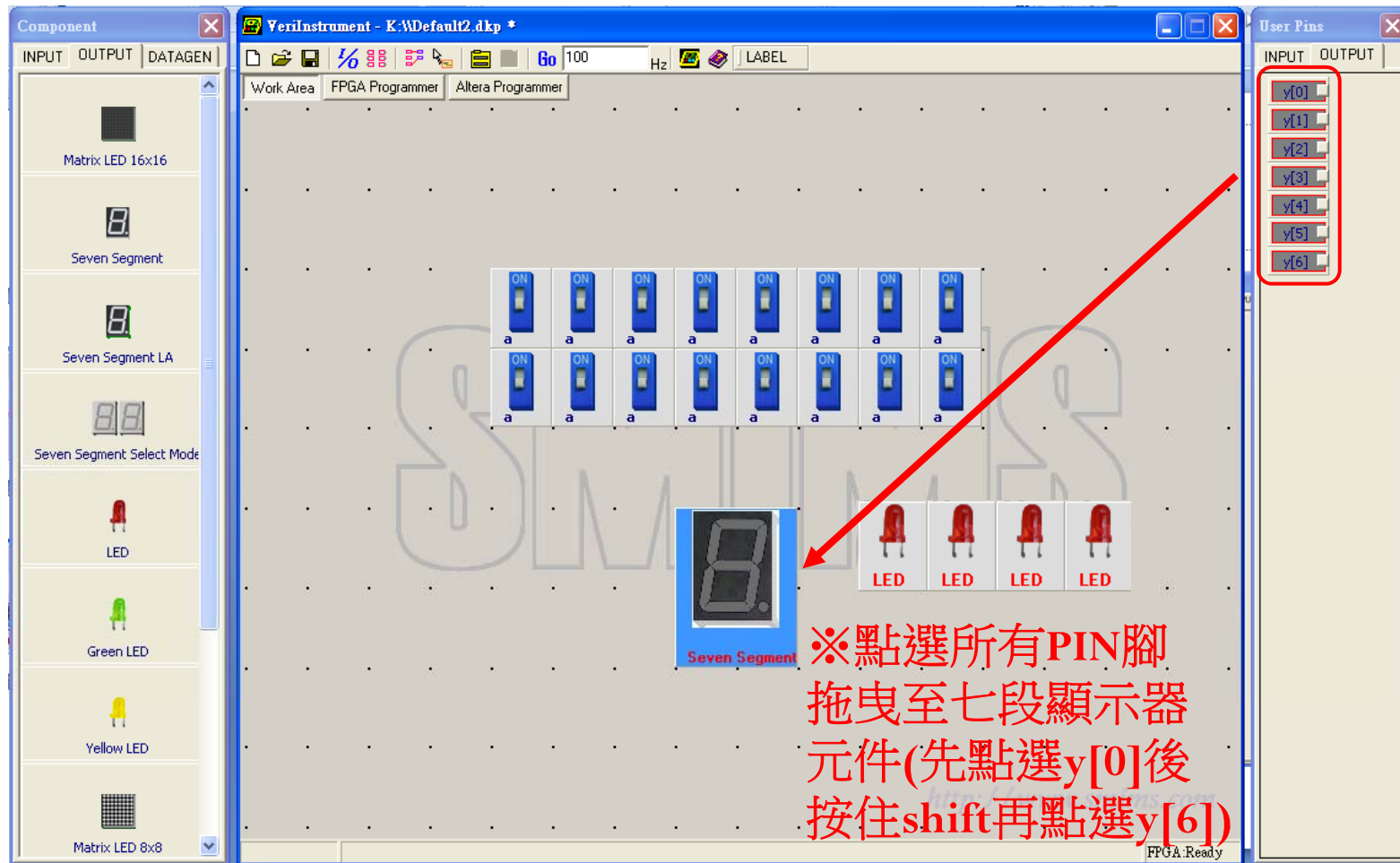


※選擇七段顯示器元件

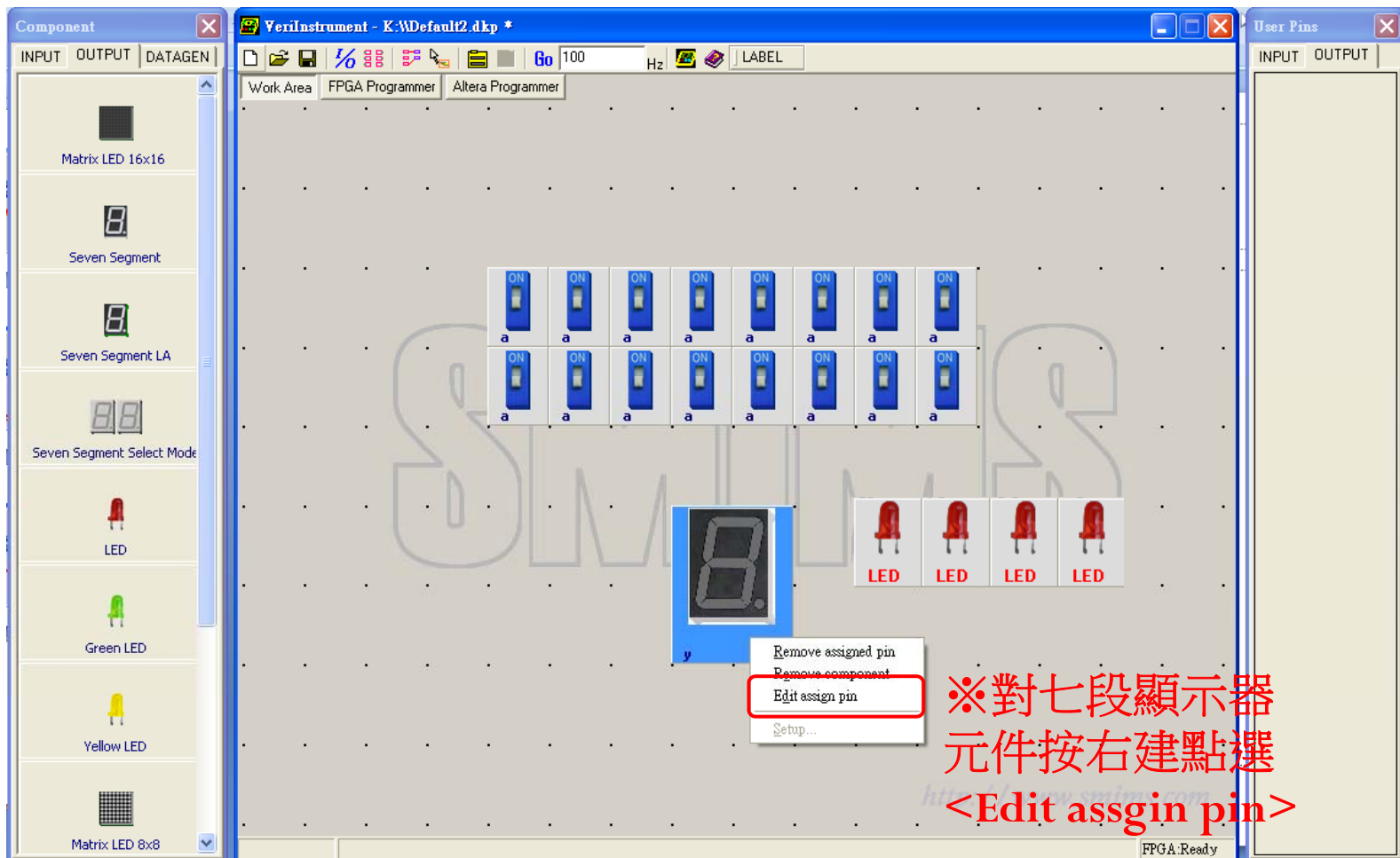
# 七段顯示器



# 七段顯示器



# 七段顯示器



# 七段顯示器

The screenshot shows the VeriInstrument software interface. On the left is a 'Component' panel with various electronic components. The main workspace shows a breadboard circuit with a 7-segment display (labeled 'y') and several push buttons (labeled 'a'). A red box highlights a 'Seven Segment' configuration window, which lists the pin connections for the display segments.

Seven Segment	
7SEG_A	y[0]
7SEG_B	y[1]
7SEG_C	y[2]
7SEG_D	y[3]
7SEG_E	y[4]
7SEG_F	y[5]
7SEG_G	y[6]
7SEG_DP	

※檢查七段顯示器元件的pin腳對應資訊

# 模擬結果

