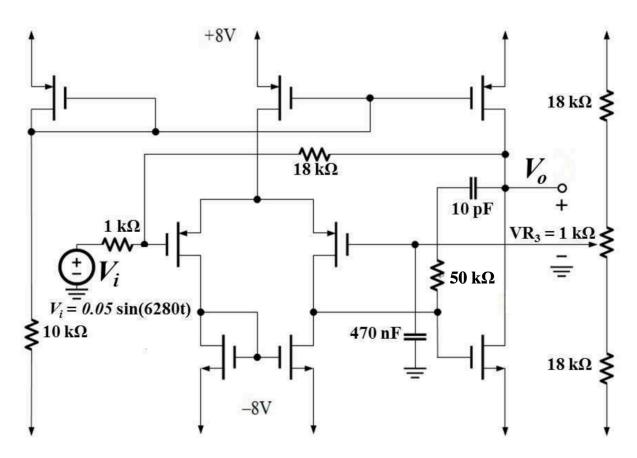
## 電子學實驗#4-預報

## **CMOS Operational Amplifier-Small-Signal Analysis**

- I. Please use 3 CD4007 ICs to construct the circuit.
- II. After the experiment, please return the ICs.



- Please record the input (Vi) and output (Vo) waveforms in Y-t mode (f = 1 kHz).
- Please plot the magnitude bode diagram of the voltage gain (Vo/Vi).  $f = 500 \ Hz \sim 1 \ MHz$ .
- Please indicate the 3-dB frequency and estimate the bandwidth.

• Please supply voltage VSS = -8V in pin 7 and VDD = +8V in pin 14 for Each CD4007 IC.

