

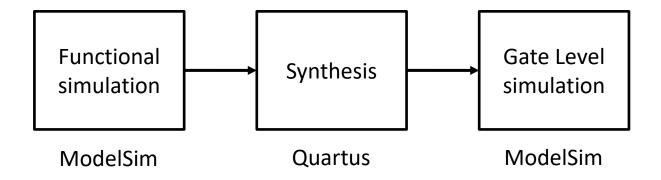
#### **NCKU CSIE DICLAB**

# The simulation process

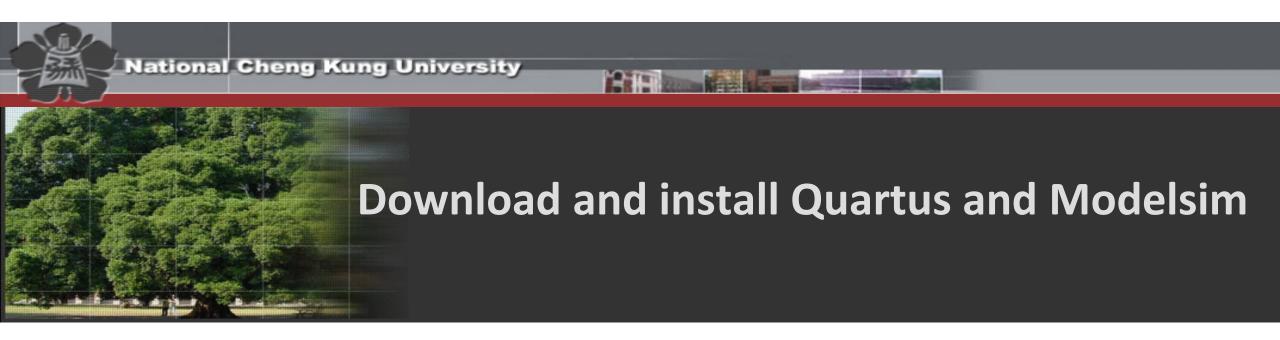




► The simulation process and the necessary tools

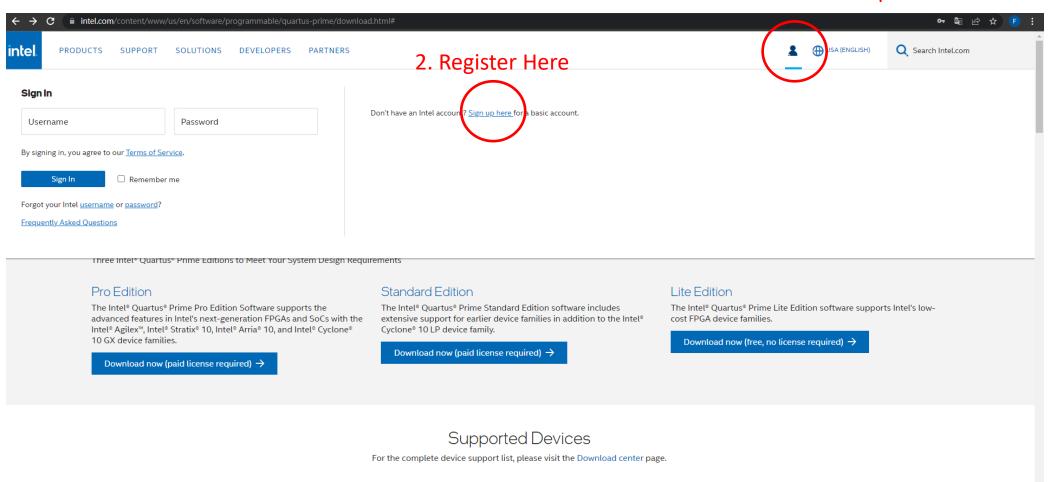


- ▶ The tools and tutorial for setup can be downloaded from :
  - https://www.intel.com/content/www/us/en/software/programmable/quartus-prime/download.html#

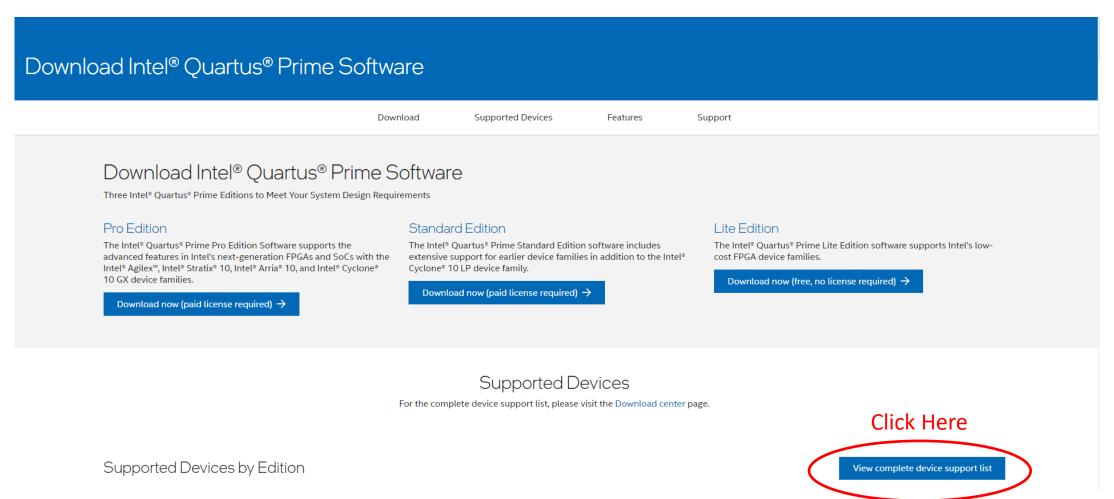




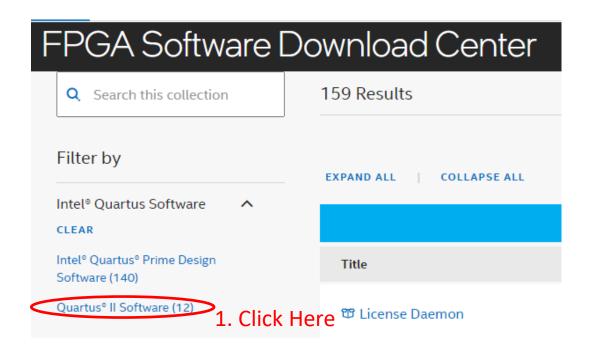
1. Click here to expand the window below

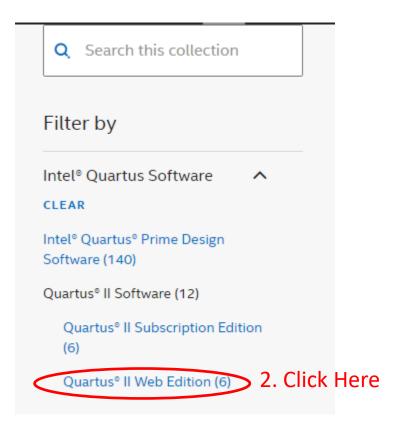




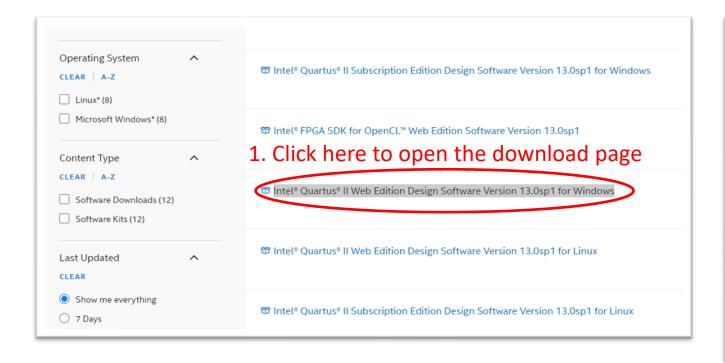


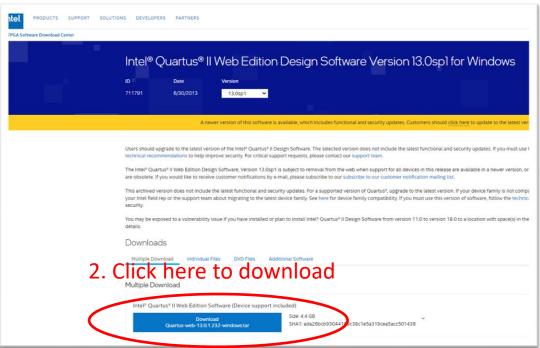
Step 3: Click Quartus II Software -> Quartus II web Edition



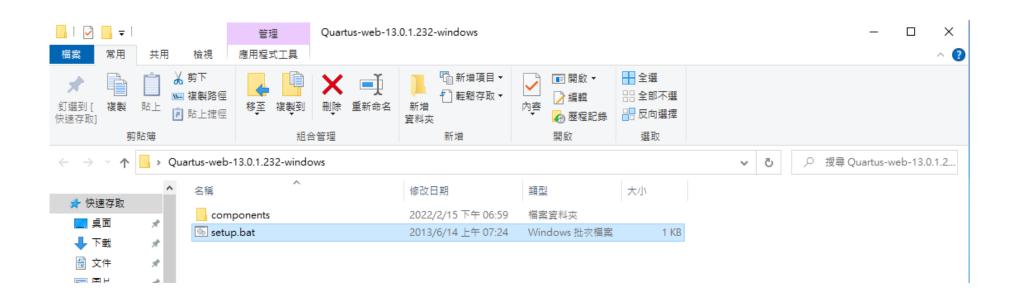


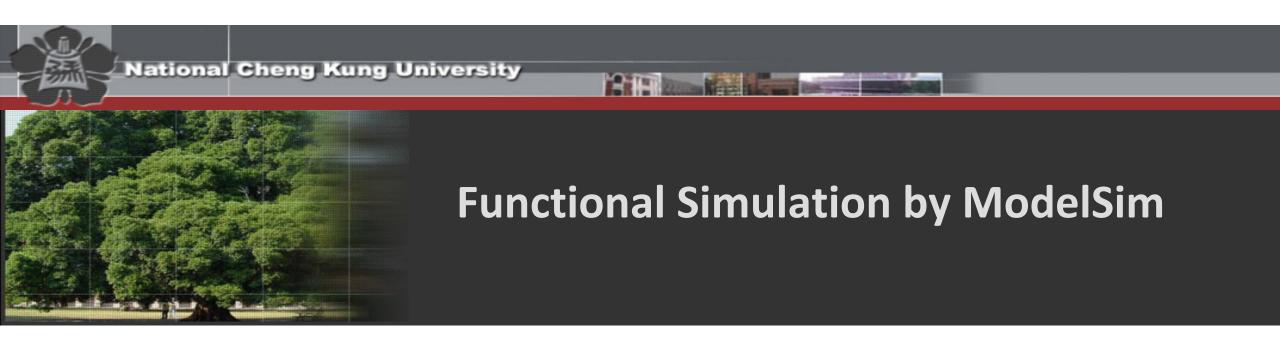
► Step 4: Download Intel® Quartus® II Web Edition Design Software Version 13.0sp1 for Windows





Step 5: Open setup.bat to install the Quartus and Modelsim





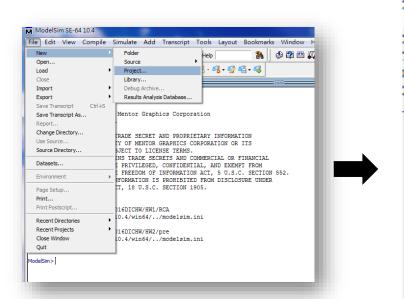
Ei: Russia Land

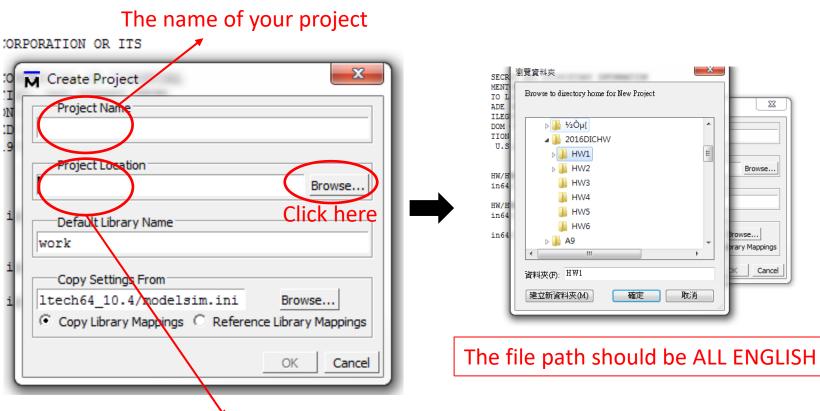
- Step1: Run modelsim.exe
  - > Your quartus installation path
    - Ex: C:/altera/13.0sp1/modelsim\_ase/win32aloem/modelsim.exe

本機磁碟 (C:) → altera → 13.0sp1 → modelsim_ase → win32aloem				
* ^	名稱	修改日期	類型	大小
*	mgc.pkginfo	2012/11/3 上午 12:26	PKGINFO 檔案	1,315 KB
*	<b>■</b> mgcld	2012/11/3 上午 12:26	應用程式	2,106 KB
70		2012/11/3 上午 12:26	應用程式擴充	1,770 KB
niv_cell_basec	<b>™</b> modelsim	2012/11/3 上午 12:26	應用程式	6,233 KB
	THE CONTRACTOR OF THE CONTRACT	2042/44/2   6-42/20	#± co ≤o →÷	2.040.1/0

- Step2: Create a new project
  - ▶ File -> New -> Project
  - Click "Browse..."

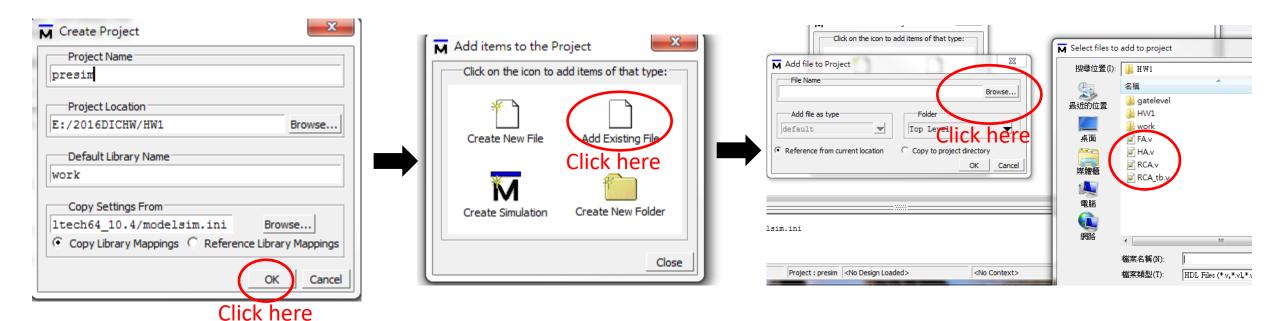
而成功大學





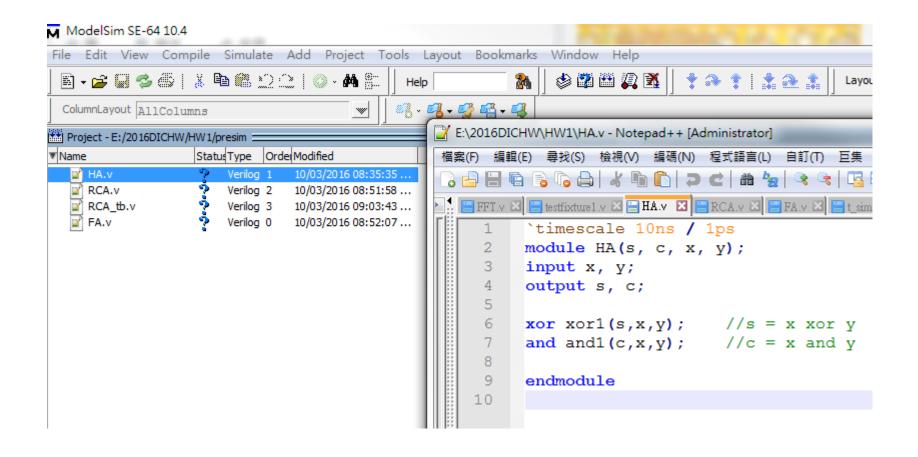
The location of your project

- Step 2: Add the files
  - Select all the files including the testbench
    - Verilog code could be stored in any location, "add existing file" would refer to the file location automatically.
    - Remember to put the test data and golden data at the same directory of the testbench file.



Step3 : Start coding...

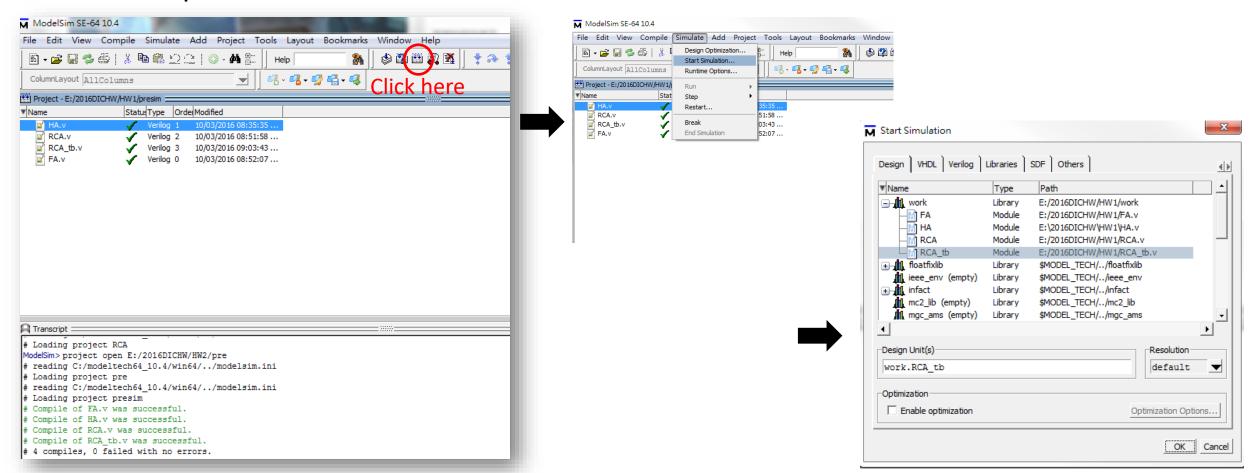
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Step4: Compile & Start simulation

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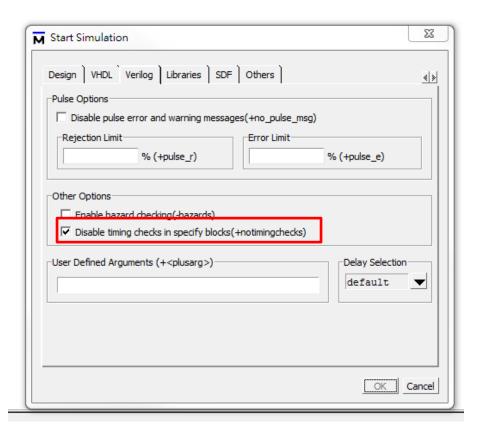
Compile -> Simulate -> Start Simulation->work->testbench file



Step4: Start simulation

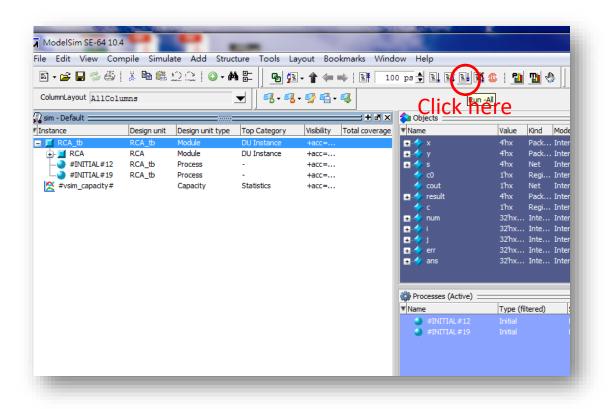
**添成功大學** 

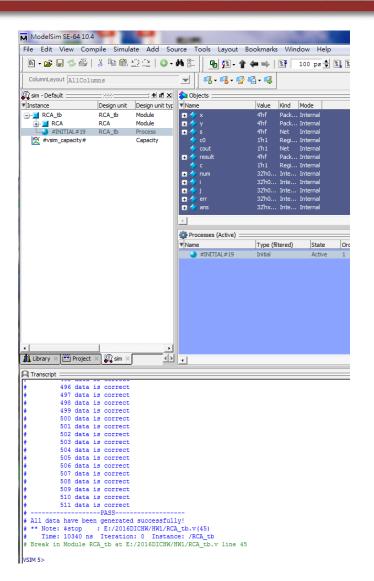
Check +no timingchecks



► Step5 : Simulation

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Step6 : Show waveform

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