

1) Read the assembly code below; add comments to explain what each line of code is doing; in one sentence, explain what this procedure is trying to accomplish. (15 points)

```
new-proc:  
    sll $a0, $a0, 24  
    srl $a0, $a0, 24  
    add $v0, $a0, $zero  
    jr $ra
```

Answer:

```
new-proc:  
    sll $a0, $a0, 24 # Shift the contents of register $a0 left by 24 bits.  
    # Importantly it inserts zeros.  
    srl $a0, $a0, 24 # Shifts the result from the  
    # previous operation back to the right by 24 bits.  
    add $v0, $a0, $zero # Adds the content of  
    # register $a0 to $zero (which is always 0) and stores the result in $v0.  
    jr $ra # Returns to the address stored  
    # in the return address register ($ra).
```

This procedure extracts out the lowest 8 bits of the \$a0 register and stores it in \$v0. It then returns to the caller.

2) Read the assembly code below; add comments to explain what each line of code is doing; provide a simple equation to express the return value v0 as a function of input arguments a0 and a1. (30 points)

```
new-proc:
    blt $a1, $zero, loop2
loop1:
    beq $a1, $zero, proc-end
    sll $a0, $a0, 1
    addi $a1, $a1, -1
    j loop1
loop2:
    beq $a1, $zero, proc-end
    srl $a0, $a0, 1
    addi $a1, $a1, 1
    j loop2
proc-end:
    add $v0, $a0, $zero
    jr $ra
```

Answer:

```
new-proc:
    blt $a1, $zero, loop2 # Branch to loop2 if $a1 is less than 0.
loop1:
    beq $a1, $zero, proc-end # If $a1 is 0, go to proc-end.
    sll $a0, $a0, 1 # Multiply $a0 by 2.
    addi $a1, $a1, -1 # Decrease $a1 by 1.
    j loop1 # Jump back to the start of loop1.
loop2:
    beq $a1, $zero, proc-end # If $a1 is 0, go to proc-end.
    srl $a0, $a0, 1 # Divide $a0 by 2.
    addi $a1, $a1, 1 # Increase $a1 by 1.
    j loop2 # Jump back to the start of loop2.
proc-end:
    add $v0, $a0, $zero # Move the content of $a0 to $v0.
    jr $ra # Returns to the address stored
    # in the return address register ($ra).
```

Equation:

If $a1 \geq 0$, $v0 = a0 \cdot 2^{a1}$

If $a1 < 0$, $v0 = a0 / 2^{(-a1)}$

3) For the (pseudo) assembly code below, replace X, Y, P, and Q with the smallest set of instructions to save/restore values on the stack and update the stack pointer. Assume that procA and procB were written independently by two different programmers who are following the MIPS guidelines for caller-saved and callee-saved registers. In other words, the two programmers agree on the registers handling input arguments and return value of procB, but they can't see the code written by the other person. Assume that \$fp isn't being used by either procA or procB. Be sure to read the class notes first so you understand the MIPS guidelines for caller-saved and callee-saved registers. (40 points)

```

procA:
    $s0 = ...
    $s1 = ...
    $s2 = ...
    $s3 = ...
    $t0 = ...
    $t1 = ...
    $t2 = ...
    $t3 = ...
    X
    $a0 = ...
    $a1 = ...
    jal procB
    Y
    ... = $s3
    ... = $t1
    ... = $t2
    ... = $a0 (this is the value that was originally passed to procA as an
argument)
    jr $ra
procB:
    P
    ... = $a0
    ... = $a1
    $s1 = ...
    $s2 = ...
    $s3 = ...
    $t0 = ...
    $t1 = ...
    Q
    jr $ra
Answer:
For X:
    No caller-saved registers to preserve since procA does not expect them to
be preserved.
For Y:

```

No need to restore any caller-saved registers as procA does not expect them to be preserved.

For P:

```
subu $sp, $sp, 12 # Allocate space on the stack for 3 registers.
sw $s1, 0($sp)    # Save $s1 on the stack.
sw $s2, 4($sp)    # Save $s2 on the stack.
sw $s3, 8($sp)    # Save $s3 on the stack.
```

For Q:

```
lw $s1, 0($sp)    # Restore $s1 from the stack.
lw $s2, 4($sp)    # Restore $s2 from the stack.
lw $s3, 8($sp)    # Restore $s3 from the stack.
addu $sp, $sp, 12 # Deallocate space on the stack.
```

4) Download and install the MARS simulator. Read this Google doc before you start. Create a new file and copy-paste the code below into the Edit window. Execute the program. The program should print "BOO7" at the bottom. Take a screenshot of your MARS screen. Annotate this image to show that one of the registers and one of the memory locations has the integer 7, and three registers and three memory locations have the ASCII codes for the three characters "B", "O", and "O". Your annotation can be as simple as a circle with the corresponding label (7, B, O, O). (15 points)

.data

```
str: .asciiz "BOO"
myint: .word 7
```

.text

```
li $v0, 4 # load immediate; 4 is the code for print_string
la $a0, str # the print_string syscall expects the string
# address as the argument; la is the instruction
# to load the address of the operand (str)
syscall # MARS will now invoke syscall-4.
# This should print the string on the bottom of the screen.
lb $t1, ($a0) # load the byte at address $a0 into register $t1
lb $t2, 1($a0) # load the byte at address $a0+1 into register $t2
lb $t3, 2($a0) # load the byte at address $a0+2 into register $t3
li $v0, 1 # syscall-1 corresponds to print_int
lw $a0, myint # Bring the value at label myint to register $a0.
# print_int expects the integer to be printed in $a0.
syscall # MARS will now invoke syscall-1
```

Answer:

File Edit Run Settings Tools Help

Run speed at max (no interaction)

Text Segment

Bkpt	Address	Code	Basic	Source
	0x00400000	0x24020004	addiu \$2,\$0,0x00000004	10: li \$v0, 4 # load immediate; 4 is the code for print_string
	0x00400004	0x3c011001	lui \$1,0x00001001	11: la \$a0, str # the print_string syscall expects the string
	0x00400008	0x34240000	ori \$4,\$1,0x00000000	
	0x0040000c	0x0000000c	syscall	14: syscall # MARS will now invoke syscall-4.
	0x00400010	0x80890000	lb \$9,0x00000000(\$4)	16: lb \$t1, (\$a0) # load the byte at address \$a0 into register \$t1
	0x00400014	0x80860001	lb \$10,0x00000001(\$4)	17: lb \$t2, 1(\$a0) # load the byte at address \$a0+1 into register \$t2
	0x00400018	0x80880002	lb \$11,0x00000002(\$4)	18: lb \$t3, 2(\$a0) # load the byte at address \$a0+2 into register \$t3
	0x0040001c	0x24020001	addiu \$2,\$0,0x00000001	19: li \$v0, 1 # syscall-1 corresponds to print_int
	0x00400020	0x3c011001	lui \$1,0x00001001	20: la \$a0, aint # Bring the value at label aint to register \$a0.
	0x00400024	0x8c240004	lw \$4,0x00000004(\$1)	

Data Segment

Address	Value (+0)	Value (+4)	Value (+8)	Value (+c)	Value (+10)	Value (+14)	Value (+18)	Value (+1c)
0x10010000	0x004f4f42	0x00000007	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010020	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010040	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010060	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010080	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100a0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100c0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100e0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000

Registers

Name	Number	Value
\$zero	0	0x00000000
\$at	1	0x10010000
\$v0	2	0x00000001
\$v1	3	0x00000000
\$a0	4	0x00000007
\$a1	5	0x00000000
\$a2	6	0x00000000
\$a3	7	0x00000000
\$t0	8	0x00000000
\$t1	9	0x00000042
\$t2	10	0x0000004f
\$t3	11	0x0000004f
\$t4	12	0x00000000
\$t5	13	0x00000000
\$t6	14	0x00000000
\$t7	15	0x00000000
\$t8	16	0x00000000
\$t9	17	0x00000000
\$s0	18	0x00000000
\$s1	19	0x00000000
\$s2	20	0x00000000
\$s3	21	0x00000000
\$s4	22	0x00000000
\$s5	23	0x00000000
\$s6	24	0x00000000
\$s7	25	0x00000000
\$s8	26	0x00000000
\$s9	27	0x00000000
\$fp	28	0x10000000
\$sp	29	0xffffffff
\$tp	30	0x00000000
\$ra	31	0x00000000
pc		0x00400024
hi		0x00000000
lo		0x00000000

Mars Messages Run I/O

-- program is finished running (dropped off bottom) --

Clear

\$a0	4	0x00000007
\$a1	5	0x00000000
\$a2	6	0x00000000
\$a3	7	0x00000000
\$t0	8	0x00000000
\$t1	9	0x00000042
\$t2	10	0x0000004f
\$t3	11	0x0000004f

a0 = 7; t0 = 'B'; t2 = 'O'; t3 = 'O'.