

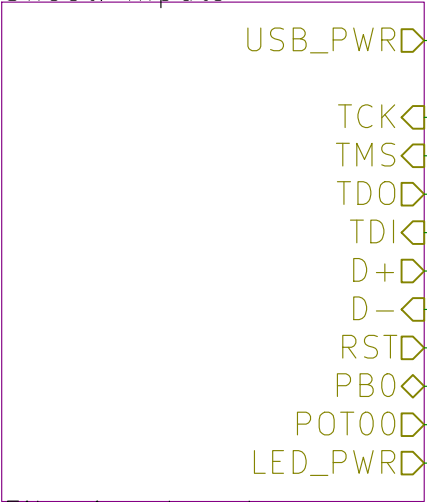
Sheet: Power Management



File: power.sch

USB\_PWR

Sheet: Inputs



File: inputs.sch

USB\_PWR

TCK

TMS

TDO

TDI

D+

D-

RST

PB0

POT00

LED\_PWR

Sheet: Microcontroller



File: mic.sch

TCK

TMS

TDO

TDI

D+

D-

RST

PB0

POT00

LED[0..12]

Sheet: Drawings



File: draw.sch

Kicad "Bug" Report: Eeschema supports homogeneous buses only (e.g. DATA[31:0]), it doesn't currently support heterogeneous buses (e.g. {DQS0, DM0, DQ[7:0]}). Maybe this feature will come along in v6.0.

Sheet: Leds/Stars



File: leds.sch

LED[0..12]

LED\_PWR

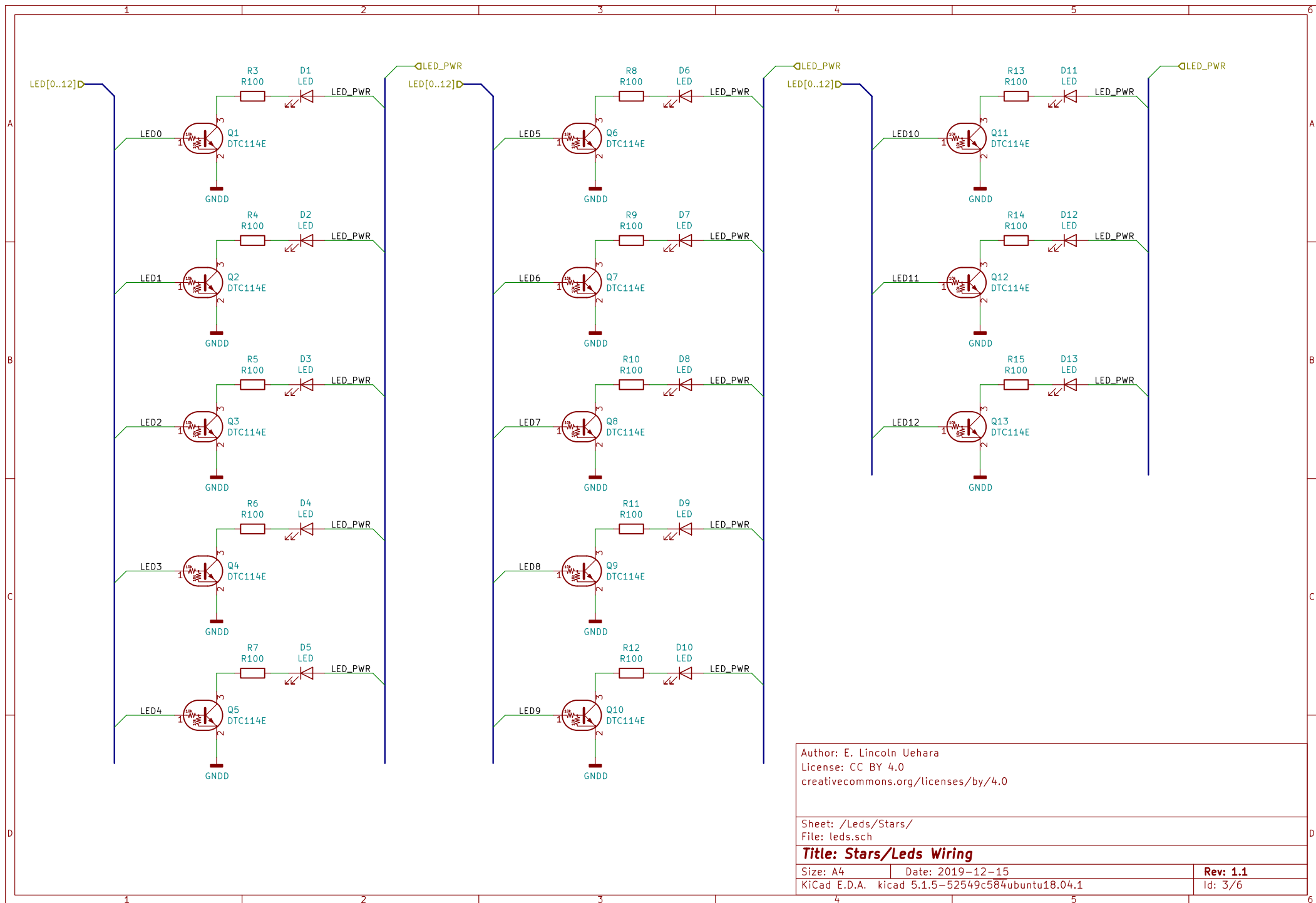
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Sheet: /  
File: aquarius.sch

**Title: Aquarius Project General Overview**

Size: A4	Date: 2019-12-15	Rev: 1.1
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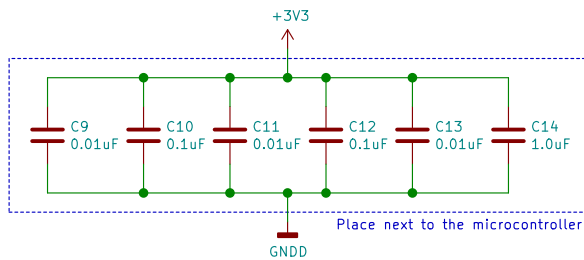
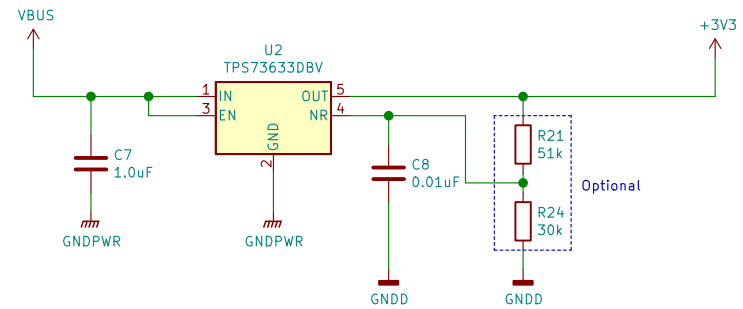
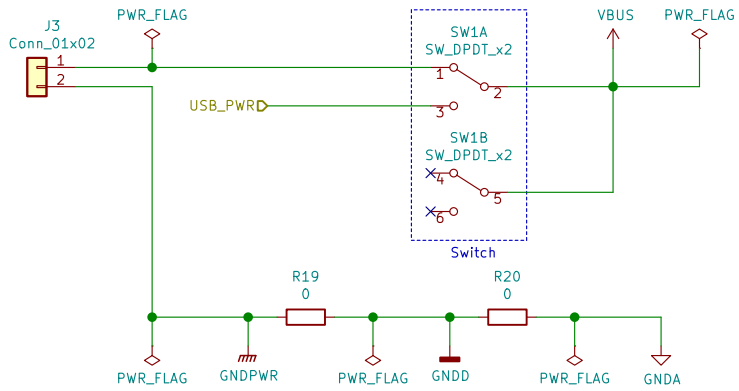
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Sheet: /Leds/Stars/  
File: leds.sch

### Title: Stars/Leds Wiring

Size: A4 Date: 2019-12-15  
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Rev: 1.1  
Id: 3/6



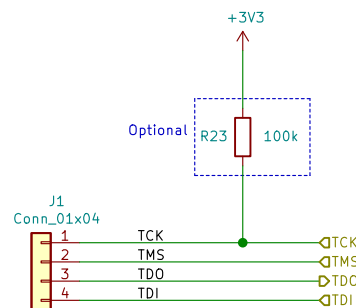
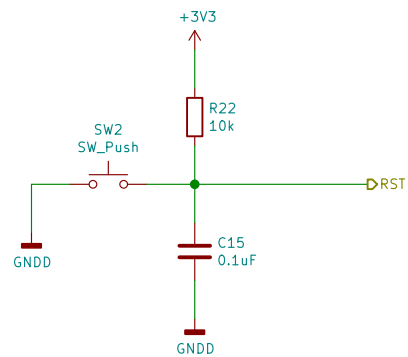
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Sheet: /Power Management/  
File: power.sch

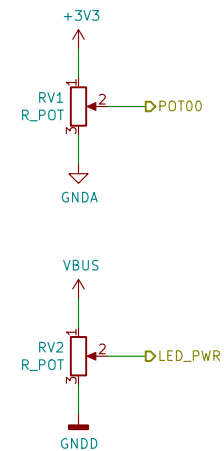
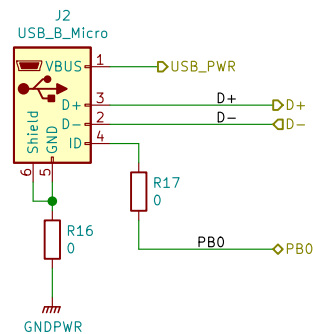
### Title: Power Management

Size: A4  
Date: 2019-12-15  
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Rev: 1.1  
Id: 4/6



"If the JTAG signals are greater than 2 in. (51 mm) or routed near an area where they could pick up noise, TCK should be externally pulled-up with a 10K or stronger resistor or pulled-down with a 1K or stronger resistor to prevent any transitions that could unexpectedly execute a JTAG instruction." (spma059 - page 30)



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USB, trimpots, reset button and debug interface

Sheet: /Inputs/  
File: inputs.sch

### Title: Inputs

Size: A4 Date: 2019-12-15  
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Rev: 1.1  
Id: 5/6

- X1  
theGirl
- X2  
rinhara\_inkan
- X3  
DesignedWithKicad
- X4  
OpenSourceHardware

- X5  
choose\_indication
- X6  
control\_indication

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Sheet: /Drawings/  
File: draw.sch

**Title: PCB Drawings**

Size: A4      Date: 2019-12-15  
KiCad E.D.A.    kicad 5.1.5-52549c584ubuntu18.04.1

**Rev: 1.1**  
Id: 6/6