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1. in cyclic redundancy checking, the divisor is _____ the CRC.

- a. The same size as
- b. 1 bit less than
- c. 1 bit more than
- d. 2 bit more than

2. A receiver receives the code 11001100111. When it uses the hamming encoding algorithm. The result is 0101. Which bit is in error?

- a. 1
- b. 3
- c. 5
- d. None of the above

3. Flow control is needed to prevent _____.

- a. bit errors,
- b. overflow of the sender buffer
- c. Overflow of the receiver buffer.
- d. Collision between sender and receiver.

4. In High-level Data Link Control (HDLC), frame that is used only to transport control information is called

- a. frame
- b. S-frame
- c. V-frame
- d. Piggybacking