



King Saud University

College of Computer and Information Sciences
Computer Science Department

Course Code	CSC 227
Course Title	Operating Systems
Semester	Spring 2019
Type of Examination	Midterm 2 Exam
Student Name:	
Student ID:	
Student Section No.	
Instructor Name:	

	Full Mark	Student's Mark
Question No.1	8	
Question No.2	3	
Question No.3	4	
Question No.4	5	
Total	20	

Instructions:

- This exam has 20 marks.
- This exam has 6 pages
- **Do not use pencil.**
- Write clearly and neatly.
- Copy your answers for questions 1-1 to 1-16 in the table below. **ONLY THAT TABLE WILL BE GRADED**

1.	2.	3.	4.	5.	6.	7.	8.
D	C	B	B	C	D	A	D
9.	10.	11.	12.	13.	14.	15.	16.
C	A	A	B	A	B	A	D

Question 1. [8 marks] Select ONLY ONE ANSWER (the best answer).

1. Kernels generally are	2. The following are shared among threads
a. Single threaded	a. Global variables, local variables, opened files
b. Single threaded by default, but require user intervention to be multithreaded	b. Registers, stack, global variables
c. Multithreaded but limited to available number of cores	c. Global variables, opened files, and code
d. Multithreaded	d. Data, opened files, stack
3. Testing and debugging is (a) _____ for (b)	4. A server for a multiplayer game requires
a. (a) The same. (b) single threaded and multithreaded applications	a. Single threaded application to control requests from users
b. (a) a challenge (b) multithreaded applications	b. Multithreaded application to control requests from users
c. (a) not a challenge (b) multithreaded applications	c. No server required, connection of minds would materialize the multiplayer game
d. (a) not required (b) multithreaded applications	d. Each player will be connected to a process in the server with a different code.
5. Thread switching has _____ overhead in comparison to context switching	6. _____ provides programmer with API for creating and managing threads
a. The same	a. System library
b. Greater	b. User program
c. Lower	c. System calls
d. None of the Above	d. Thread libraries
7. _____ selects which processes should be brought into the ready queue.	8. When the degree of multiprogramming needs to be decreased, the following scheduler is used:
a. Long-term scheduler	a. Job Scheduler
b. Short-term scheduler	b. Long-term scheduler
c. Medium-term scheduler	c. Short-term scheduler
d. Dispatcher	d. Medium-term scheduler
9. _____ is a system call used to replace a process' memory space with a new program.	10. What state would a process be in after an interrupt?

a.	fork()
b.	replace()
c.	exec()
d.	wait()

a.	Ready
b.	Waiting
c.	Interrupted
d.	Running

11.	The _____ section of a process contains dynamically allocated memory during run time.	12.	Context switching time depends heavily on:
a.	Heap	a.	Hardware support via larger RAM
b.	Stack	b.	Hardware support via multiple sets of registers per CPU
c.	Data	c.	OS support via more complex PCB
d.	Text	d.	None of the above.

13.	Which module gives control of the CPU to the process selected by the short-term scheduler?	14.	The interval from the time of submission of a process to the time of completion is termed as:
a.	Dispatcher	a.	Waiting time
b.	Interrupt	b.	Turnaround time
c.	Scheduler	c.	Response time
d.	None of the mentioned	d.	Throughput

15.	In priority scheduling algorithm,	16.	Process are classified into different groups in:
a.	CPU is allocated to the process with highest priority mentioned	a.	Shortest job scheduling algorithm
b.	CPU is allocated to the process with lowest priority	b.	Round robin scheduling algorithm
c.	Equal priority processes cannot be scheduled	c.	Priority scheduling algorithm
d.	Both A and B	d.	Multilevel queue scheduling algorithm

Question 2. [3 marks]

2-a) [1 mark] Explain Data parallelism with an example?

Data parallelism – distributes subsets of the same data across multiple cores, same operation on each

Any example that divide the data and perform the same operation would be acceptable. [1 mark]

.....

.....

.....

2-b) [1 mark] Calculate the performance gain for an application that is 100% serial in case of switching from dual core processor to quad core?

$$speedup \leq \frac{1}{S + \frac{(1-S)}{N}}$$

Since the application is 100% serial , the speedup would be $\leq 1/1$ regardless of the number of cores. So there are NO performance gain by switching from dual core to quad core.

.....

2-c) [1 mark] List four benefits of multithreaded programming.

Each correct answer (0.25)

Responsiveness – may allow continued execution if part of process is blocked, especially important for user interfaces

Resource Sharing – threads share resources of process, easier than shared memory or message passing

Economy – cheaper than process creation, thread switching lower overhead than context switching

Scalability – process can take advantage of multiprocessor architectures

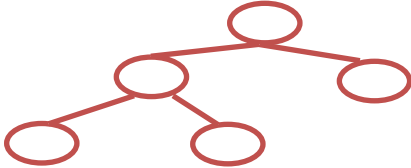
Question 3. [4 marks]

3-a) [2 marks] For the following C code, what's the output of this program? Also, draw the process tree.

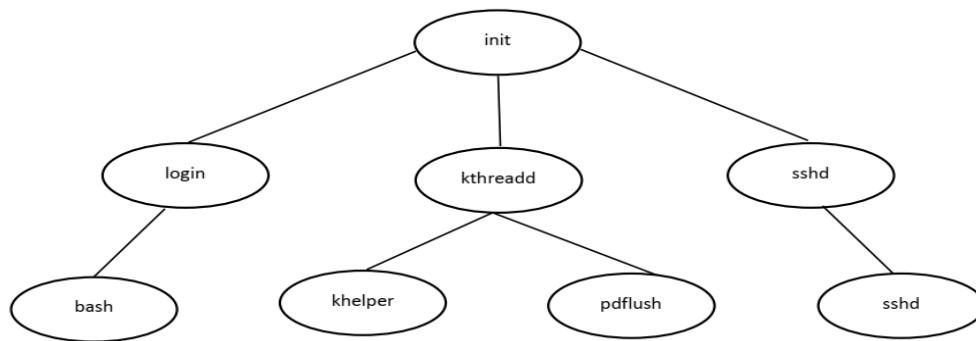
```
#include <stdio.h>
#include <unistd.h>
int main() {
    if (fork()>0 || fork()>0)
        fork();
    printf("1 ");
    return 0;
}
```

Output [1 mark]: **1 1 1 1 1**

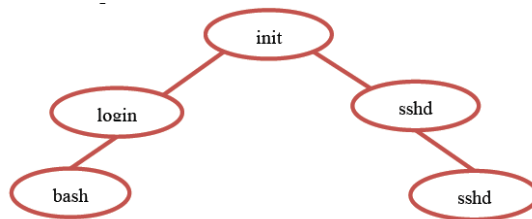
Process Tree [1 mark]:



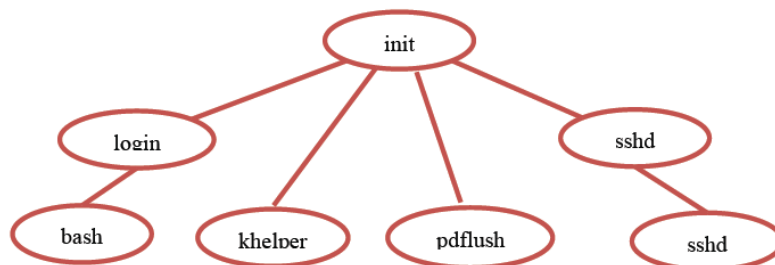
[2 marks] Consider the following process tree:



1- Draw the process tree after **kthreadd** terminates assuming cascading termination is enabled.



2- Draw the process tree after **kthreadd** terminates assuming cascading termination is NOT enabled.



Question 4. [5 marks]

Consider the following set of processes which are assumed to have arrived in the order P1, P2, P3, P4 all at time zero.

Process	Burst Time	Waiting Time
P1	30.....
P2	53.....
P3	118.....
P4	219.....

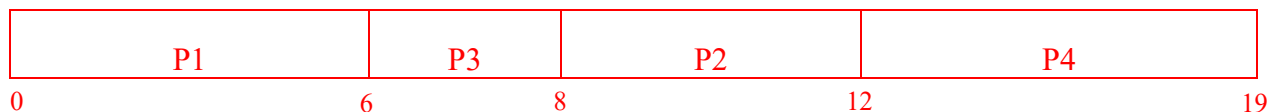
4-a) [2 marks] Calculate waiting time for each process using FCFS scheduling algorithm.

4-b) [1 mark] Calculate the average waiting time for the above scenario.

Average waiting time = $(0+3+8+19) / 4 = 7.5$

[1 marks] Draw the Gantt chart of the following scenario given in table below using the Shortest Remaining Time First (preemptive version of the shortest job first) algorithm:

Process	Arrival Time	Burst Time	Turnaround Time
P1	0	6	6
P2	3	4	9
P3	4	2	4
P4	5	7	14



4-c) [1 marks] Calculate the turnaround time for each process using SRTF scheduling algorithm.