1. One of the purposes of Virtual memory is:
   Decouple memory management from an application

2. Load time binding helps in easily swapping back the processes to different areas of memory since we compute the addresses beforehand.
   False

3. Which of the following provides slowest data access?.
   Secondary Storage

4. The run time of mapping between Virtual Address and Physical Address is done by a hardware device is known as an MMU.
   True

5. Which one of the following is the address generated by CPU?
   Logical Address

6. Limit register keeps track of lower limit of the physical address space.
   False

7. Base register in hardware is used to keep track of:
   Lower limit of the physical address space

8. The page table contains:
   Base address of each page in physical memory

9. Which of the following are valid address bindings?
   Compile time binding, run time binding, load time binding

10. If _____ binding is used, a process can be easily swapped back into a different area of memory.
    Run time binding