

From SAT to SMT

The Next Frontier in Problem Solving and Formal Verification

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November 2024

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From SAT to SMT

The Next Frontier in Problem Solving and Formal Verification.

Abstract: This talk introduces the transition from the classical Boolean Satisfiability Problem (SAT) to the more expressive Satisfiability Modulo Theories (SMT). We explore the motivations behind SMT, and the key theories that extend the capabilities of SAT solvers. The talk also covers the architecture of SMT solvers and their applications in software analysis, in particular, for symbolic execution.

*Mathematics is not about numbers, equations,
computations, or algorithms: it is about **understanding**.*

— William Paul Thurston

1. Introduction to SAT



SAT is the classical **NP-complete** problem of determining if a given **Boolean formula** is satisfiable, that is, if there **exists a model** an assignment of truth values to the variables that makes the formula true, or **proving** that no such assignment exists.

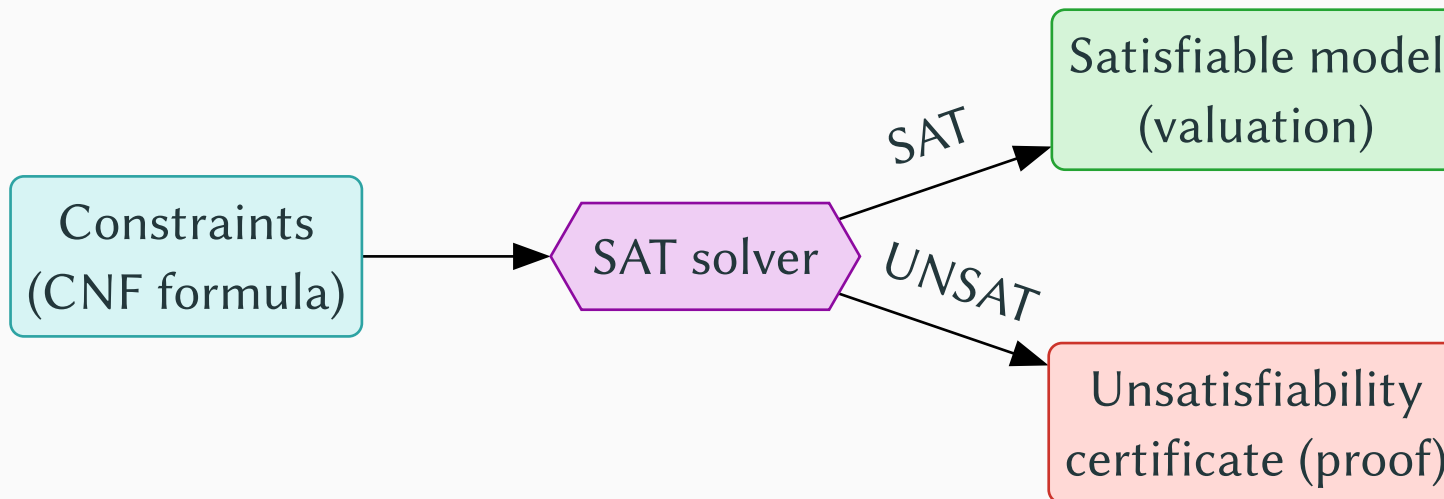
$$\exists X. F(X) = 1$$

Example:

$(\text{TIE} \rightarrow \text{SHIRT}) \wedge (\text{TIE} \vee \text{SHIRT}) \wedge \neg(\text{TIE} \wedge \text{SHIRT}) \wedge \text{TIE}$

Limitations of SAT:

- Restricted to propositional variables.
- Most SAT solvers are limited to CNF formulas.
- Cannot handle arithmetic expressions (e.g., $x + y > 5$) natively.
- Lacks support for data structures like arrays or lists.

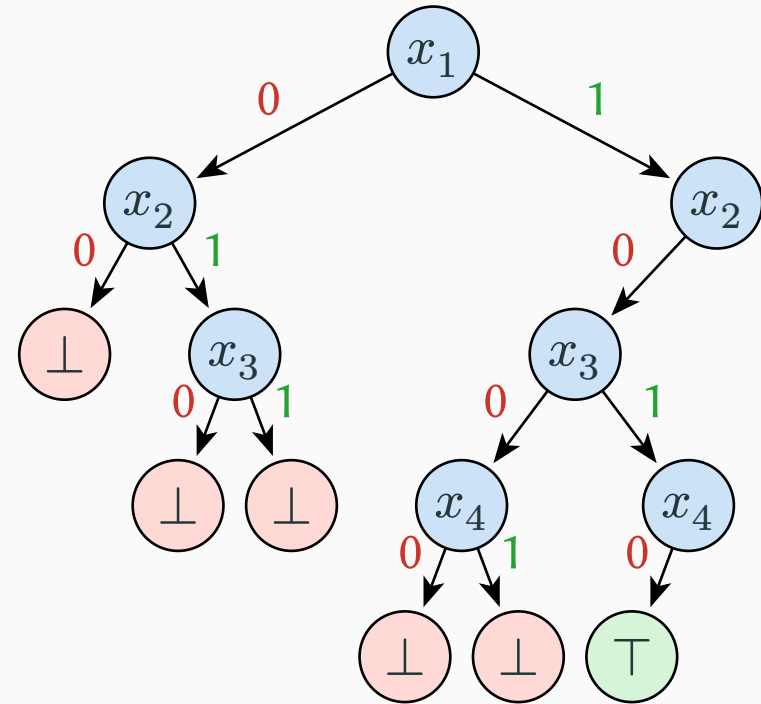


To interact with SAT solvers from Java/Kotlin, use `kotlin-satlib` library.

To interact with SAT solvers from Rust, use `sat-nexus` library.

CDCL ALGORITHM

```
1 while true do  
2   while propagate_gives_conflict() do  
3     if decision_level == 0 then return UNSAT  
4     else analyze_conflict()  
5   end  
6   restart_if_applicable()  
7   remove_lemmas_if_applicable()  
8   if not decide() then return SAT  
9 end
```



2. From SAT to SMT

$SMT = SAT + \text{Theories.}$

A **theory** is a set of logical formulas modeling a particular domain.

Common components of theories:

- **Logic**: Propositional and first-order logic.
- **Arithmetic**: Numbers, math operations, inequalities.
- **Arrays**: Access (read) and update (write) operations.
- **Bit-Vectors**: Bitwise operations on fixed-size (e.g. 32-bit) binary representations.
- **Uninterpreted Functions**: Functions without a fixed interpretation.

Logic

$$\forall x \exists y. (x \rightarrow y)$$

Arithmetic

$$2x + 3 \geq y$$

Arrays

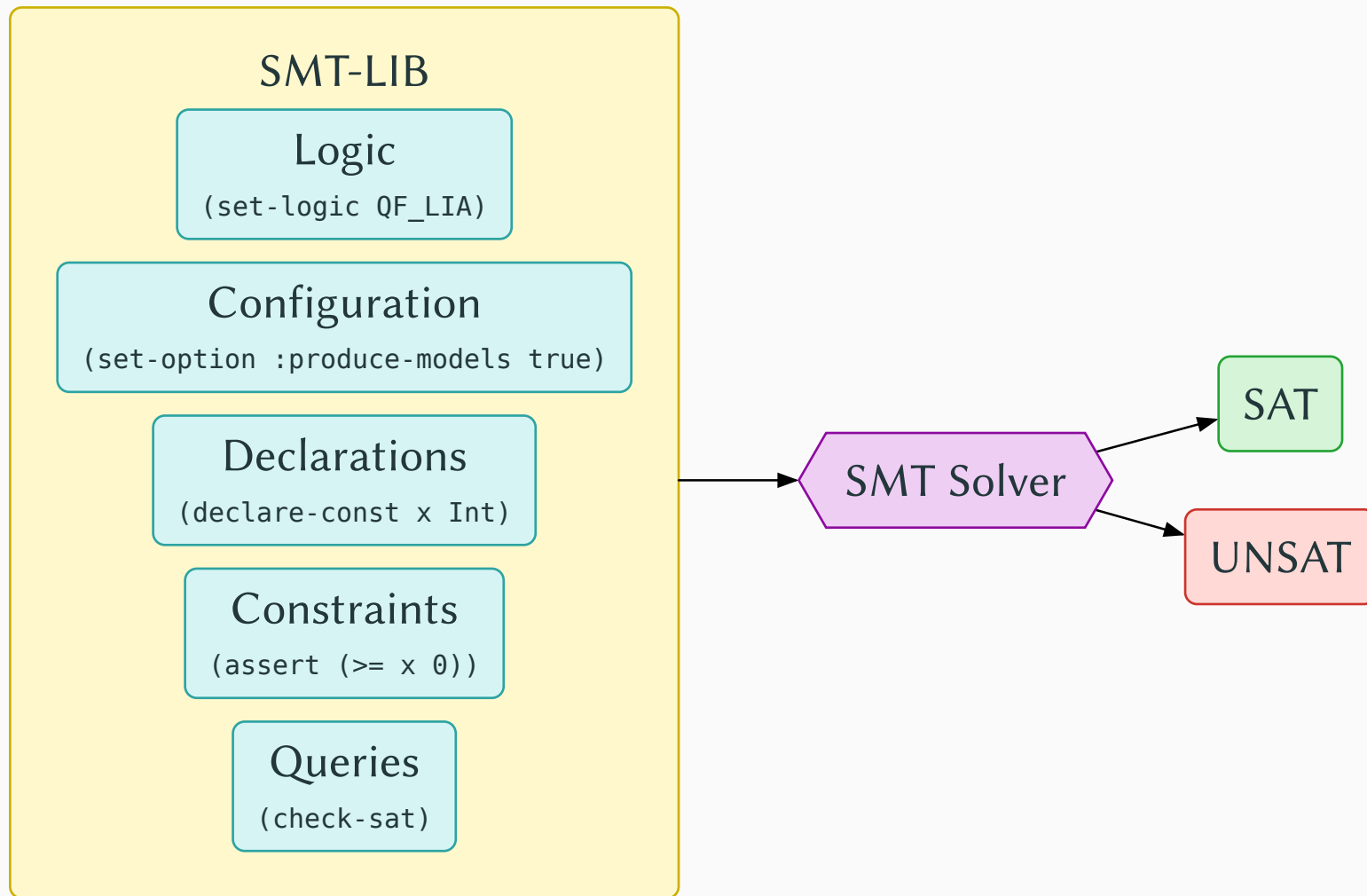
$$A[i] = x$$

Bit-Vectors

$$x_{32} = y_{32} \oplus z_{32}$$

Uninterpreted
Functions

$$f(x) = y$$



The basis of almost all “practical” SMT theories is a classical **first-order logic with equality**.

- **Variables**: Boolean ($x, y, z \in \mathbb{B}$) or from some domain \mathbb{D} (numbers, objects, ...).
- **Logical connectives**: $\wedge, \vee, \neg, \rightarrow, \leftrightarrow$.
- **Quantifiers**: universal (\forall) and existential (\exists).
- **Functions** and **Predicates**: $f : \mathbb{D} \rightarrow \mathbb{D}$ and $P : \mathbb{D} \rightarrow \mathbb{B}$.
- **Equality**: “=” is a binary relation symbol with the following axioms:
 - Reflexivity: $\forall x.(x = x)$.
 - Substitution for functions: $\forall x, y.(x = y) \rightarrow (f(\dots, x, \dots) = f(\dots, y, \dots))$.
 - Substitution for formulas: $\forall x, y.(x = y) \rightarrow (\varphi(x) \rightarrow \varphi(y))$.

Examples: $\forall x \exists y.(x \rightarrow y) \quad \exists x.P(x) \quad \forall x \forall y.P(f(x)) \rightarrow \neg(P(x) \rightarrow Q(f(y), x, z))$

- “=” is **equality**, f is an **uninterpreted function**.
- If the background logic is **FOL with equality**, then EUF is **empty** theory.

‣ **Example** (UNSAT formula):

$$a \cdot (f(b) + f(c)) = d \quad \wedge \quad b \cdot (f(a) + f(c)) \neq d \quad \wedge \quad (a = b)$$

‣ Abstracted formula (still UNSAT):

$$h(a, g(f(b), f(c))) = d \quad \wedge \quad h(b, g(f(a), f(c))) \neq d \quad \wedge \quad (a = b)$$

- Both formulas are **unsatisfiable**, without any arithmetic reasoning.
- EUF is used to abstract “non-supported constructions”, e.g. non-linear multiplication.

Restricted fragments support more efficient methods.

Logic	Example expression
Linear arithmetic (LIA, LRA)	$x + 2y \leq 5$
Non-linear arithmetic (NIA, NRA)	$2xy + 4xz^2 - 5y \leq 10$
Difference logic (DL)	$x - y \bowtie 3$, where $\bowtie \in \{<, >, \leq, \geq, =\}$
UTVPI (Unit Two-Variable Per Inequality)	$ax + by \bowtie d$, where $a, b \in \{-1, 0, 1\}$

Commonly, variables are **Reals** or **Integers**. But there are also:

- Floating-point arithmetic (IEEE 754 standard).
- Rational arithmetic.

Theory of arrays defines two “interpreted” functions: **read** and **write**.

Axioms:

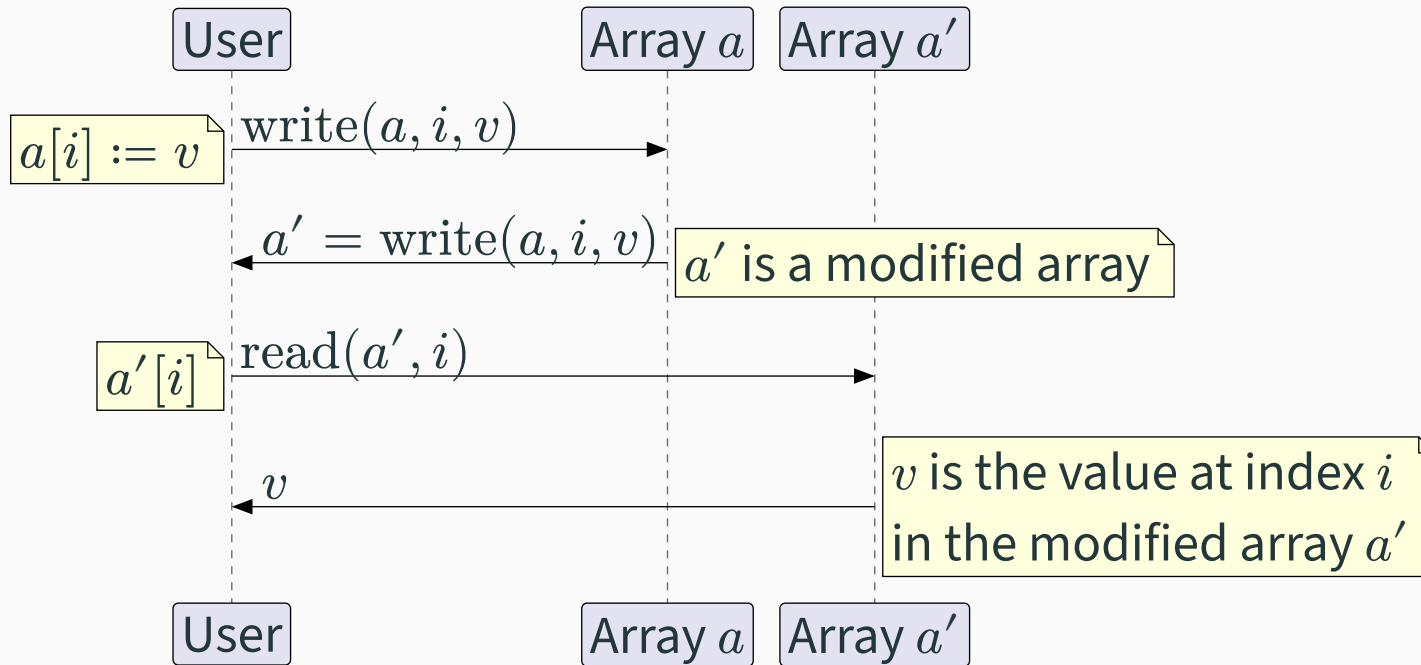
- $\forall a \forall i \forall v. (\text{read}(\text{write}(a, i, v), i) = v)$
- $\forall a \forall i \forall j \forall v. (i \neq j) \rightarrow (\text{read}(\text{write}(a, i, v), j) = \text{read}(a, j))$

Extensionality: $\forall a \forall b (\forall i (\text{read}(a, i) = \text{read}(b, i))) \rightarrow (a = b)$

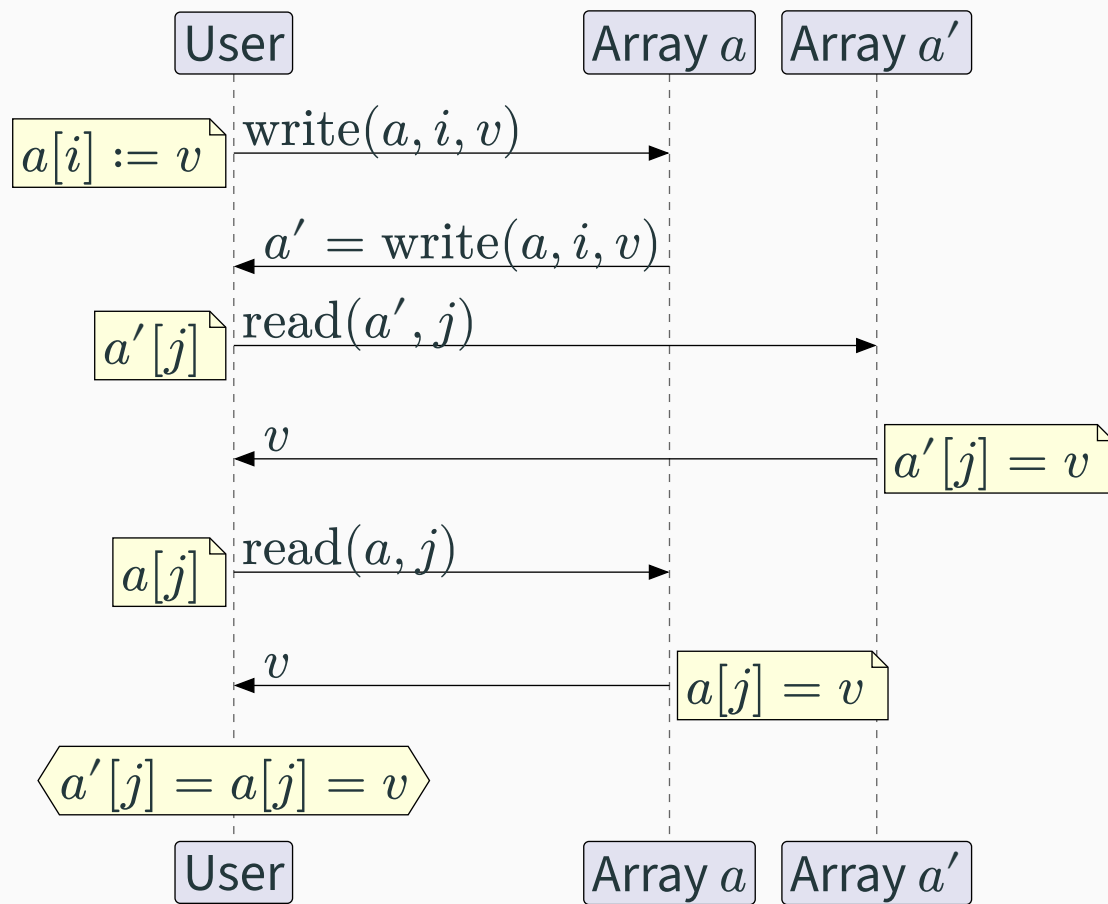
Example:

$$\Gamma = \{\text{write}(a, i, x) \neq b, \text{read}(b, i) = y, \text{read}(\text{write}(b, i, x), j) = y, a = b, i = j\}$$

First axiom: $\forall a \forall i \forall v. (\text{read}(\text{write}(a, i, v), i) = v)$



Second axiom: $\forall a \forall i \forall j \forall v. (i \neq j) \rightarrow (\text{read}(\text{write}(a, i, v), j) = \text{read}(a, j))$



Try it online!

<https://compsys-tools.ens-lyon.fr/z3>

<https://jfmc.github.io/z3-play/>

```
(set-logic QF_AX) ; Arrays theory

(declare-sort Index)
(declare-sort Element)
(declare-fun a () (Array Index Element))
(declare-fun b () (Array Index Element))
(declare-fun i () Index)
(declare-fun j () Index)
(declare-fun x () Element)
(declare-fun y () Element)

(assert (distinct (store a i x) b))      ; write(a, i, x) != b
(assert (= (select b i) y))              ; read(b, i) = y
(assert (= (select (store b i x) j) y))  ; read(write(b, i, x), j) = y
(assert (= a b))                        ; a = b
(assert (= i j))                        ; i = j

(check-sat) ; Check satisfiability
(get-model) ; Get a model if possible
```

$$\Gamma = \{\text{write}(a, i, x) \neq b, \text{read}(b, i) = y, \text{read}(\text{write}(b, i, x), j) = y, a = b, i = j\}$$

Bit-vector is a vector of bits of some fixed size.

“Numbers” (integers) are represented in binary form as bit-vectors.

Operations on bit-vectors:

- String-like: concat and extract.
- Logical: `bvnot`, `bvor`, `bvand`, `bvxor`, ...
- Arithmetic: `bvadd`, `bvsub`, `bvmul`, `bvudiv`, `bvurem`, ...
- Comparisons, shifts, rotations, ...

Example: (assume all bit-vectors are of size 3)

$$a[0..1] \neq b[0..1] \quad \wedge \quad (a \mid b) = c \quad \wedge \quad c[0] = 0 \quad \wedge \quad a[1] + b[1] = 0$$

Solver	Distinctive feature
Z3	Supports many theories
CVC5	Academic cutting-edge
Yices	Ultra fast
Bitwuzla	Top choice for bit-vectors
MathSAT	Combination of theories
Alt-Ergo	Deductive reasoning

To interact with SMT solvers from Java/Kotlin, use KSMT library.

3. Advanced Topics in SMT

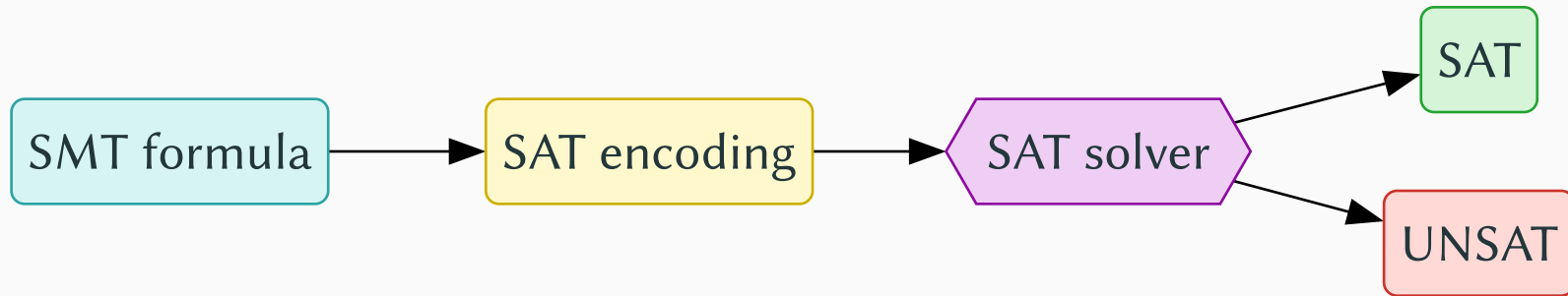
There are **two** main approaches to solving SMT:

- **Eager** approach 🦅

Encode SMT as SAT and solve using a SAT solver.

- **Lazy** approach 🦉

Use SAT solver for Boolean part and theory solver for theory-specific parts.



Pros

- ✓ Can use the best available SAT solver
- ✓ Simple modular architecture
- ✓ Ideal for finite domains and bounded integers (bit-blasting of bit-vectors)

Cons

- ✗ Complex encodings for some theories
- ✗ Scalability issues for large theories
- ✗ Unbounded integers and quantifiers can lead to intractable problems

Step 1: Eliminate function and predicate symbols.

Consider a EUF-formula with functions $f(a)$, $f(b)$ and $f(c)$.

- **Ackermann** reduction:
 - Replace each function/predicate with a fresh variable: A, B, C, \dots
 - Add clauses: $(a = b) \rightarrow (A = B)$, $(a = c) \rightarrow (A = C)$, $(b = c) \rightarrow (B = C)$
- **Bryant** reduction:
 - Replace $f(a)$ with A
 - Replace $f(b)$ with $\text{ITE}(b = a, A, B)$
 - Replace $f(c)$ with $\text{ITE}(c = a, A, \text{ITE}(c = b, B, C))$
 - Here, ITE stands for “If-Then-Else” conditional expression.

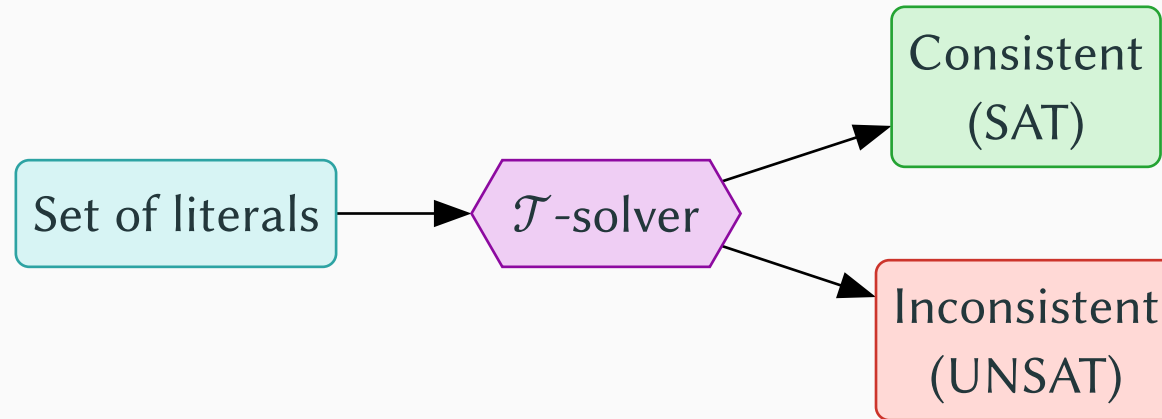
After the first step, atoms in the formula are **equalities** between **constants**.

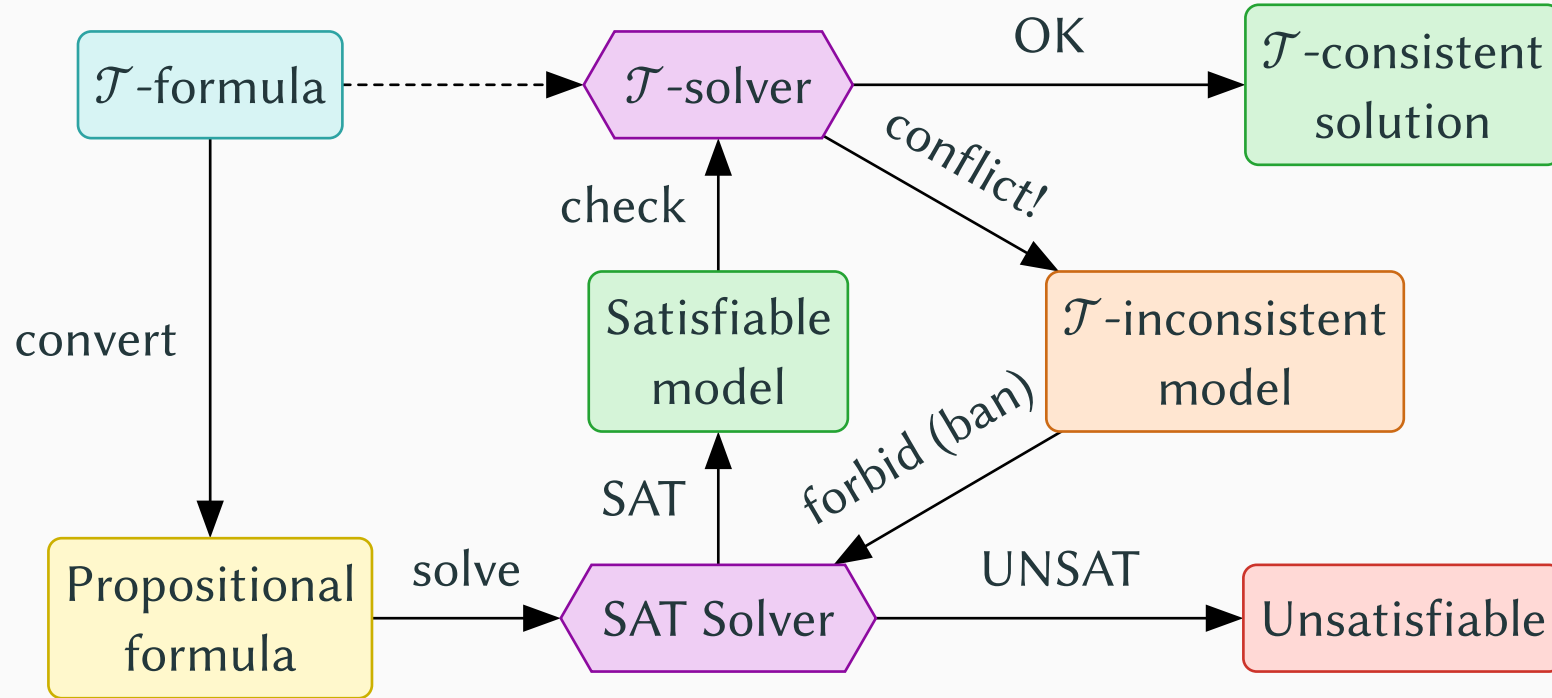
Step 2: Encode into propositional logic.

- **Small-domain** encoding:
 - If there are n different constants, there is a model with size at most n .
 - Given n constants, we need $\log n$ bits to represent each constant.
 - Equalities, such as $(a = b)$, are encoded using the bits of a and b .
- **Per-constraint** encoding:
 - Replace each equality $(a = b)$ with a propositional variable $P_{a,b}$.
 - Add transitivity constraints: $P_{a,b} \wedge P_{b,c} \rightarrow P_{a,c}$.

Done. Throw it into a **SAT solver** and let it do its **magic**!

“**Lazy**” means the theory information is used lazily, on demand, when checking the consistency of propositional models found by the SAT solver.



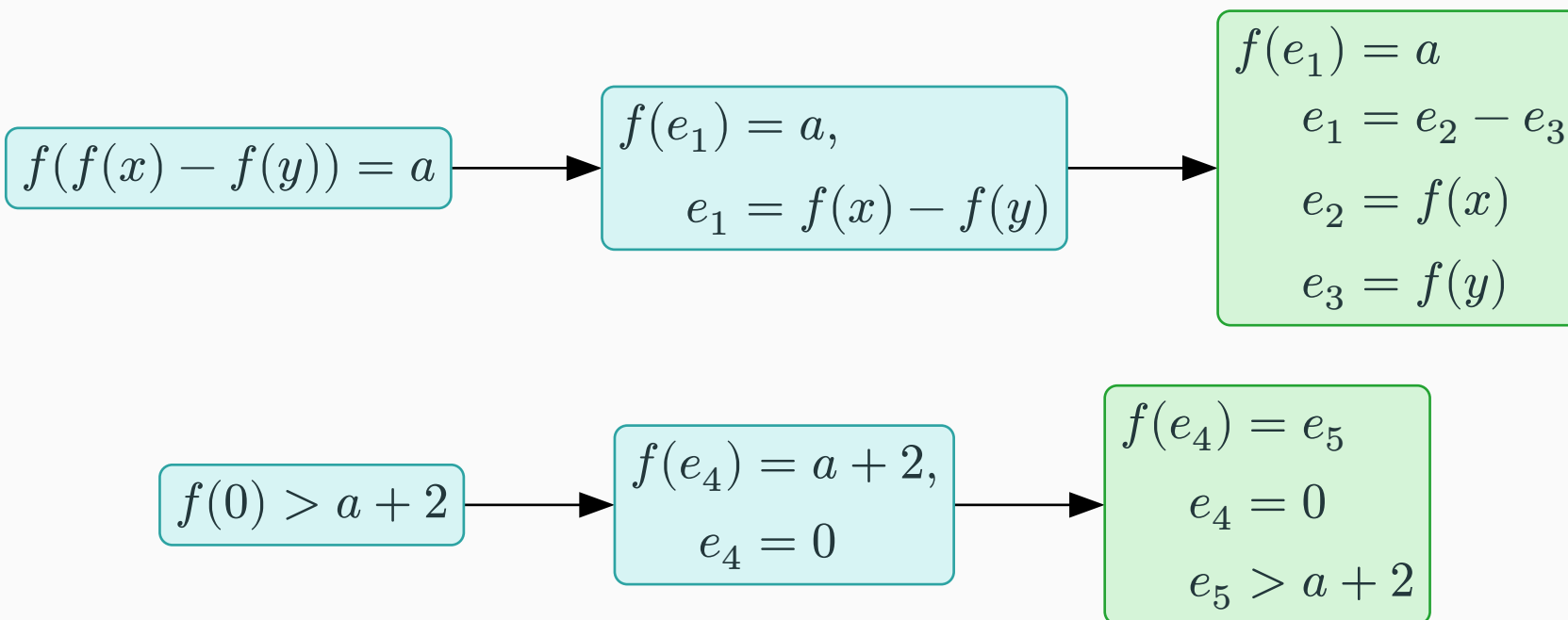


Formulas often involve **multiple** theories, for example:

$$\begin{aligned} &(a = b + 2) \wedge \\ &(A = \text{write}(B, a + 1, 4)) \wedge \\ &((\text{read}(A, b + 3) = 2) \vee (f(a - 1) \neq f(b + 1))) \end{aligned}$$

Here, we have “+” from \mathcal{T}_{LIA} , “read” and “write” from \mathcal{T}_{AX} , and “ $f(\cdot)$ ” from \mathcal{T}_{UF} .

Step 1: Purify literals so that each belongs to a single theory.



Step 2: Exchange entailed **interface equalities** (over shared constants $e_1, e_2, e_3, e_4, e_5, a$).

$$L_1 = \{f(e_1) = a, f(x) = e_2, f(y) = e_3, f(e_4) = e_5, x = y, e_1 = e_4\}$$

$$L_2 = \{e_2 - e_3 = e_1, e_4 = 0, e_5 > a + 2, e_2 = e_3, a = e_5\}$$

$$\frac{\frac{\frac{f(e_1) = a \quad f(e_4) = e_5}{a = e_5} \mathcal{T}_{\text{UF}} \quad \frac{\frac{e_4 = 0 \quad e_2 - e_3 = e_1}{e_2 = e_3} \mathcal{T}_{\text{LRA}} \quad \frac{f(x) = e_2 \quad f(y) = e_3 \quad x = y}{\mathcal{T}_{\text{UF}}}}{\mathcal{T}_{\text{LRA}}}$$

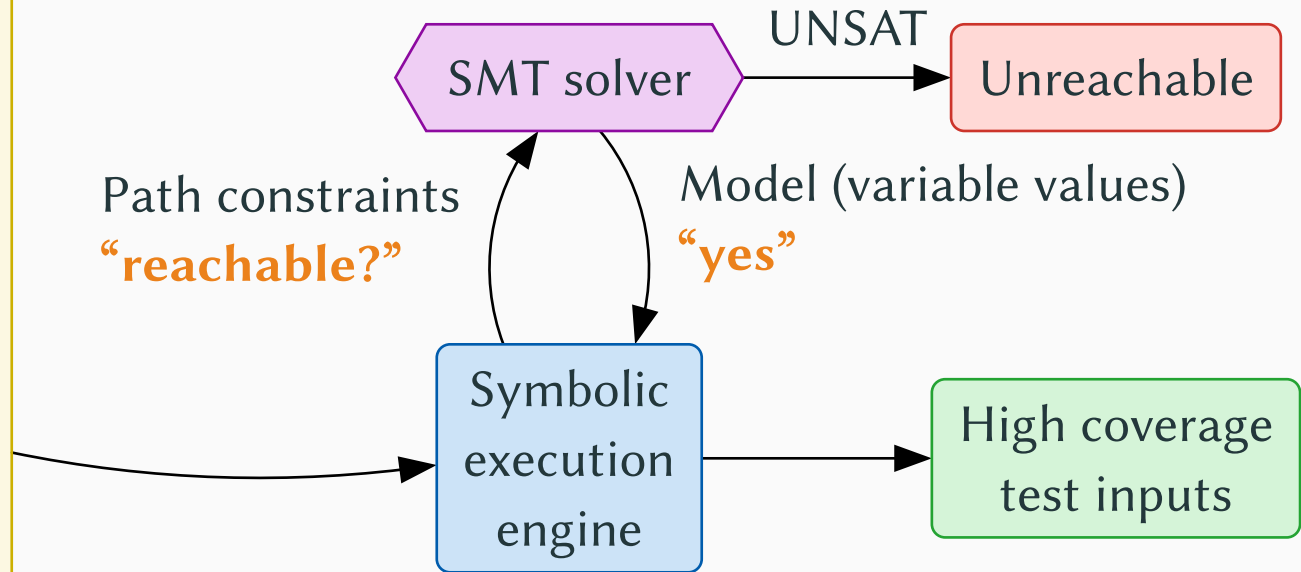
Step 3: Check for satisfiability.

- $L_1 \not\models_{\text{UF}} \perp$
- $L_2 \models_{\text{LRA}} \perp$
- Thus, the whole formula is **unsatisfiable**.

$$\frac{e_5 > a + 2 \quad a = e_5}{\perp} \mathcal{T}_{\text{LRA}}$$

4. Applications of SMT

```
void func(int x, int y) {  
    int z = 2 * y;  
    if (z == x) {  
        if (x > y + 10) {  
            assert(false); // !  
        }  
    }  
}  
  
int main() {  
    int x = sym_input();  
    int y = sym_input();  
    func(x, y);  
    return 0;  
}
```



```
1 def func(x: int, y: int):
```

```
2
```

```
    z = 2 * y
```

 $x \mapsto x_0$ $y \mapsto y_0$ $z \mapsto 2 \cdot y_0$

```
3
```

```
    if x == z:
```

```
        PC:  $(x_0 = 2 \cdot y_0)$ 
```

```
4
```

```
        if x > y + 10:
```

```
            PC:  $(x_0 = 2 \cdot y_0) \wedge (x_0 > y_0 + 10)$ 
```

```
5
```

```
        raise
```

```
        RuntimeError("???)
```

```
        Reachable with  $x_0 = 40, y_0 = 20$ 
```

```
6
```

```
7 def main():
```

```
8
```

```
    x = sym_input()
```

 $x \mapsto x_0$

```
9
```

```
    y = sym_input()
```

 $x \mapsto x_0$ $y \mapsto y_0$

```
10
```

```
    func(x, y)
```

```
    PC:  $\top$ 
```

5. Conclusion

- SMT **extends** the capabilities of SAT by incorporating **rich theories**.
- SMT solvers are **precise** which makes them a critical tool for verification and analysis.
- As SMT **evolves**, its role in modern computing is becoming more prominent.

Thanks.



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