Admin

- Fifth quiz opens on Monday
- Sudoku assignment:
 - Deadline: 1st March 4pm
 - Cut-off date: 15th March 4pm
 - You can submit your work without penalty until
 15th March 4pm I recommend you submit asap so you don't fall behind
- Virtual Contact Hours
 - New (non-assessed) worksheet on Monday
 - We will have a meeting for the new worksheet
 - You may ask for help with Sudoku assignment in VCH but make it clear in your question

The number of **sequential operations** (time-steps) is lower in one algorithm than another

What are these operations?

Operations performed by a Random Access Machine

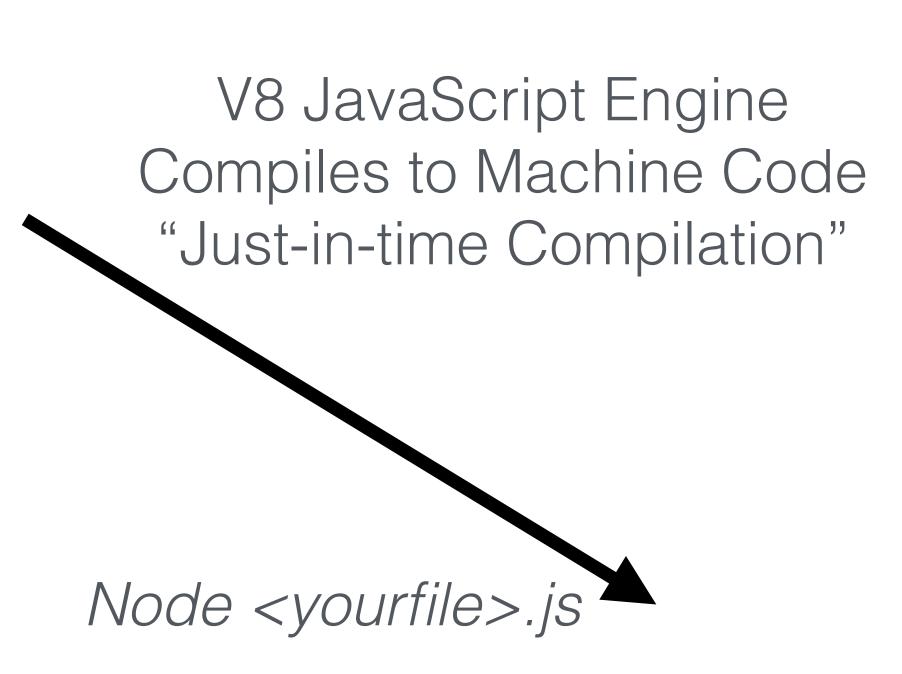
For Review Seminar

```
function lengthQueue(queue) {
   if (queue.isEmpty()) {
      return 0;
   }
   var secondQueue = new Queue();
   var count = 0;
   while (queue.isEmpty() === false) {
      count++;
      secondQueue.enqueue(queue.head());
      queue.dequeue();
   }
   while (secondQueue.isEmpty() === false) {
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      secondQueue.dequeue();
   }
   return count;
}
```

"Text file" stored in memory

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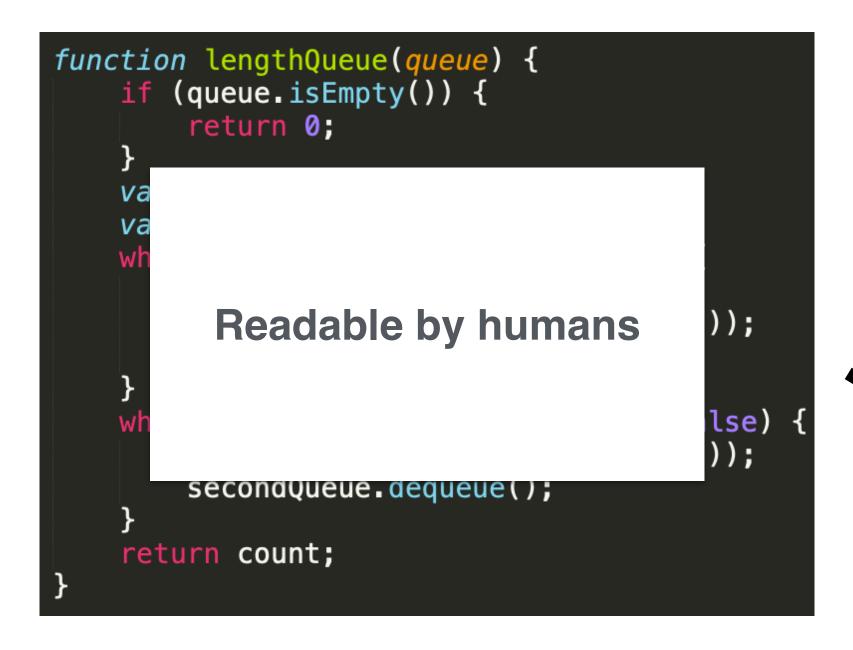


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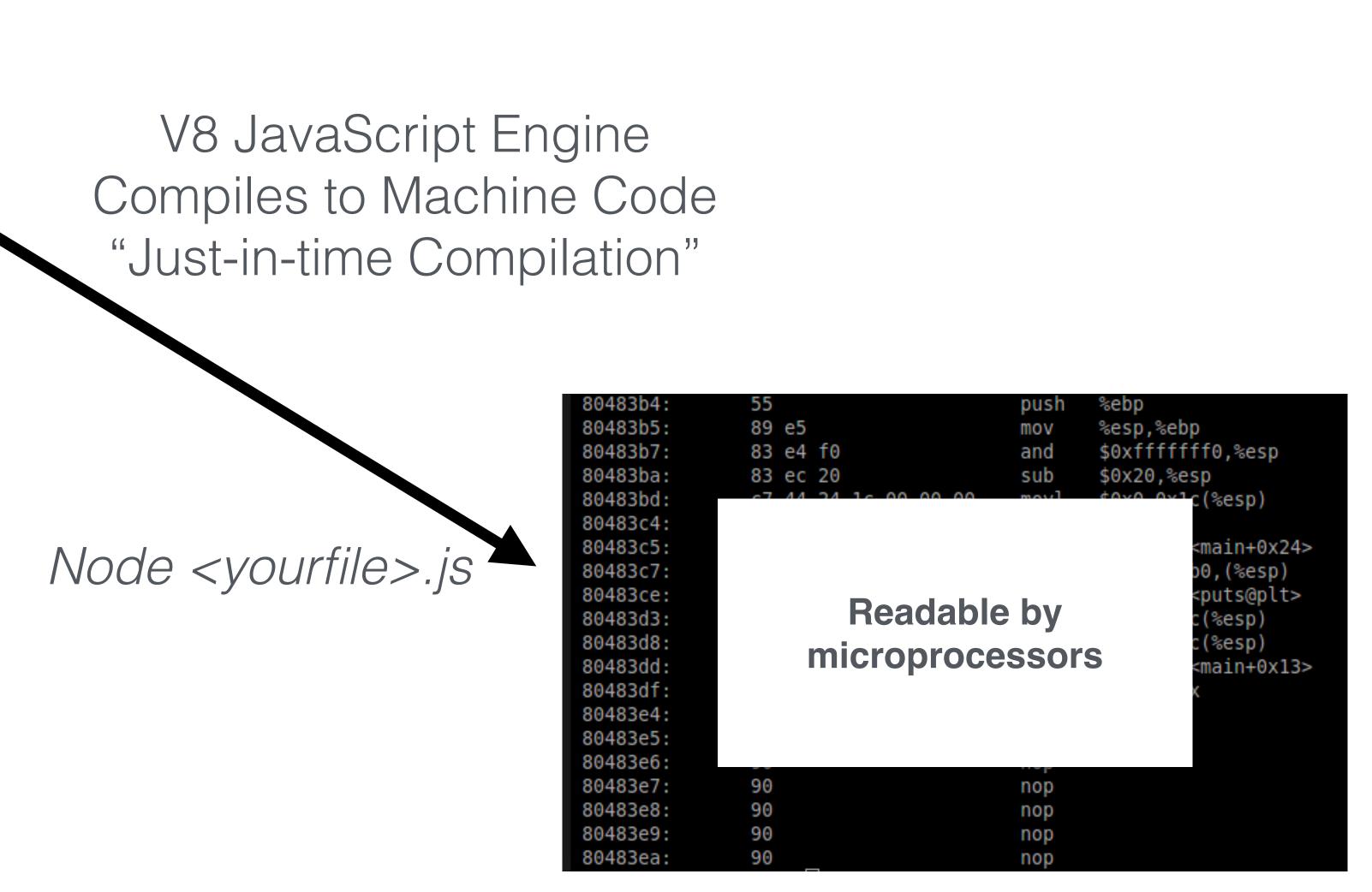
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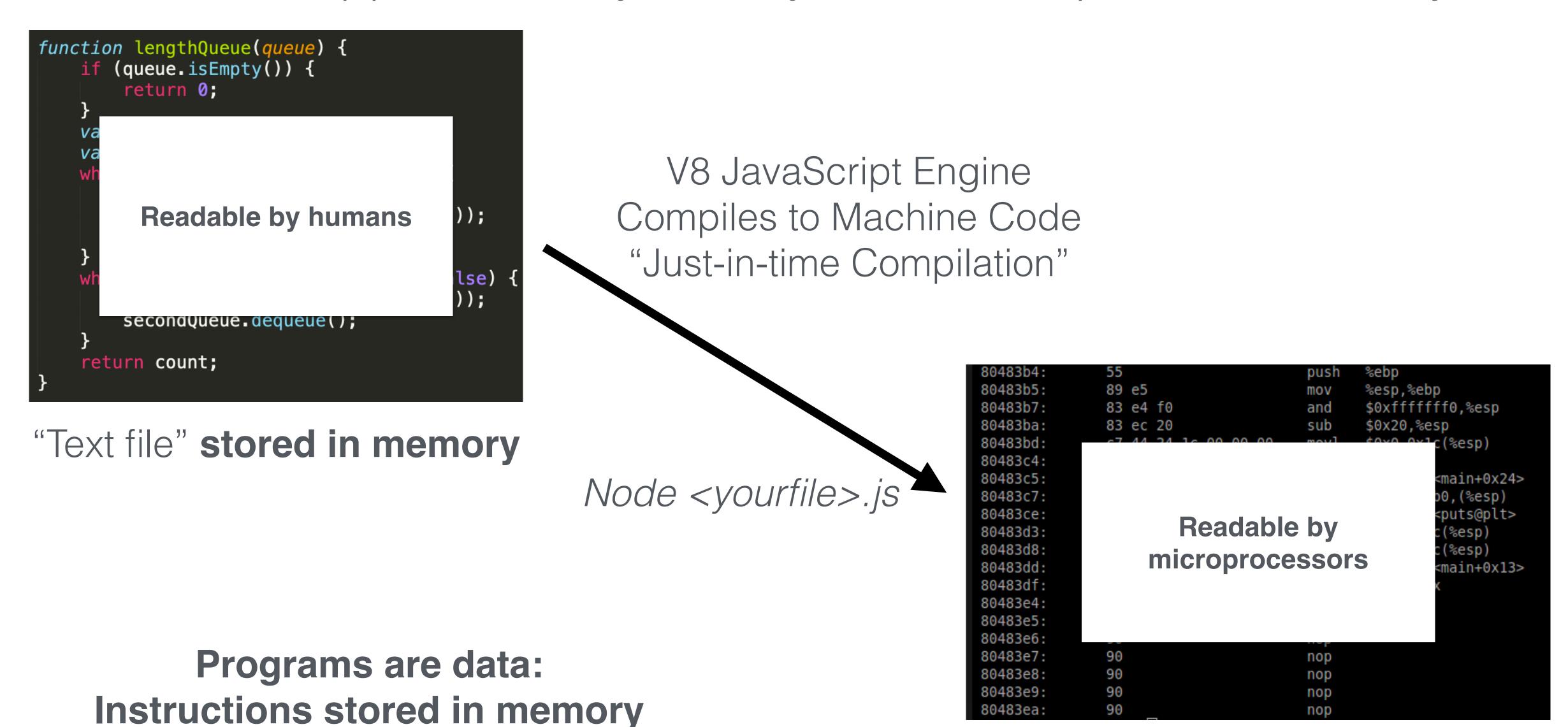
Set of *basic instructions* stored in memory Everything is basically in hexadecimal



"Text file" stored in memory

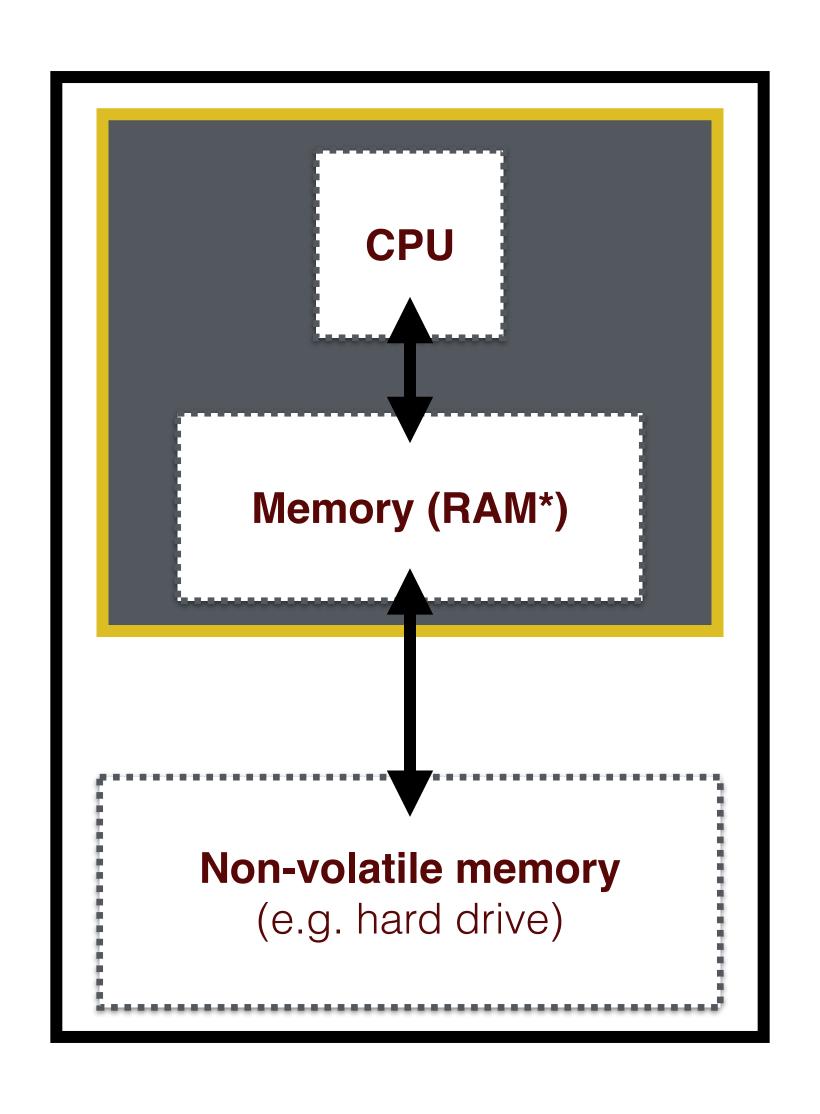


Set of *basic instructions* stored in memory Everything is basically in hexadecimal



Set of basic instructions **stored in memory** Everything is basically in hexadecimal

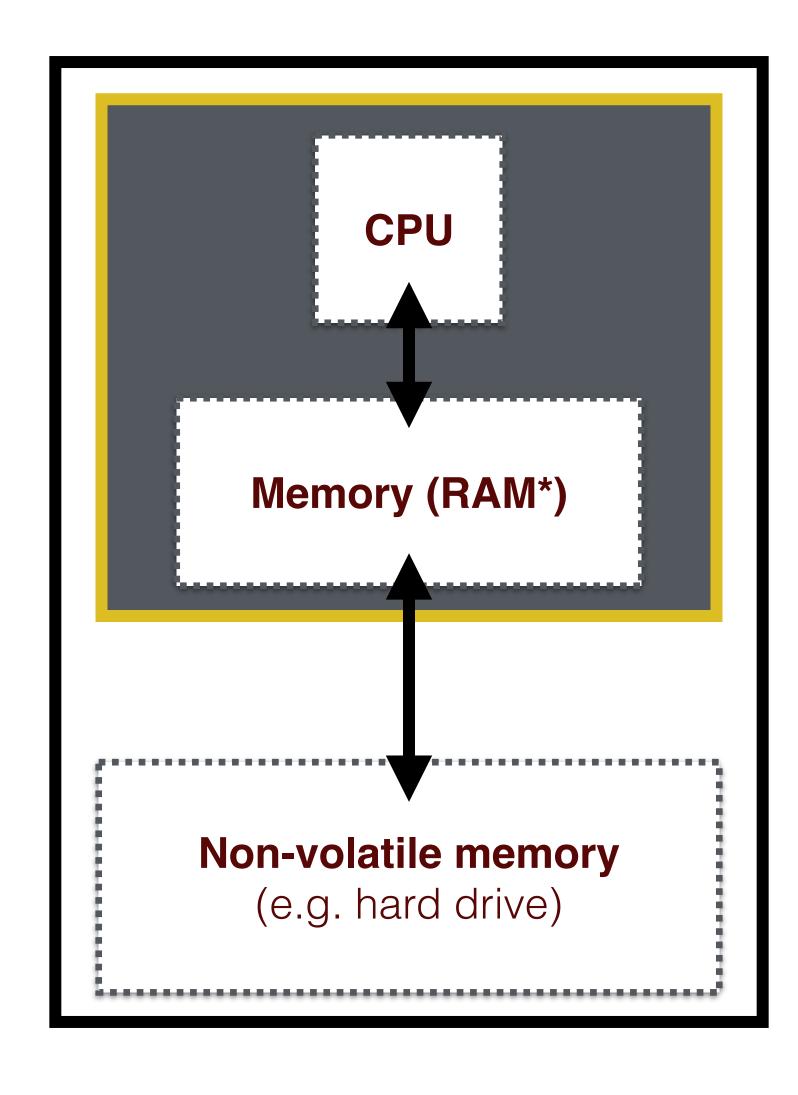
We abstract away details to an architecture



We abstract away details to an architecture

Machine code instructions copied to here when needed

Initial js and machine code stored here



Machine code instructions implemented by this

*Random Access Memory

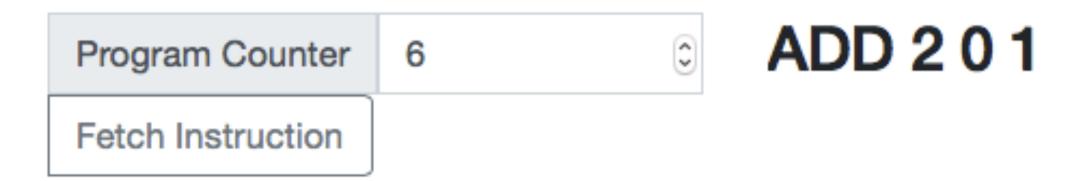
CPU Simulator (How Computers Work)

http://igor.doc.gold.ac.uk/~afior002/cpu_simulator/index.html

CPU Sim

Instructions

Fetch instruction from Memory



Fetch data from Memory



Store data to Memory

Address VAL	Store
----------------	-------

Correct! Now press "Fetch Instruction" to get the next instruction.

Registers

R0	1
R1	5
R2	6
R3	VAL
R4	VAL
R5	VAL
R6	VAL
R7	VAL

What are the main components?

We can distill this architecture to two basic elements: processor and memory

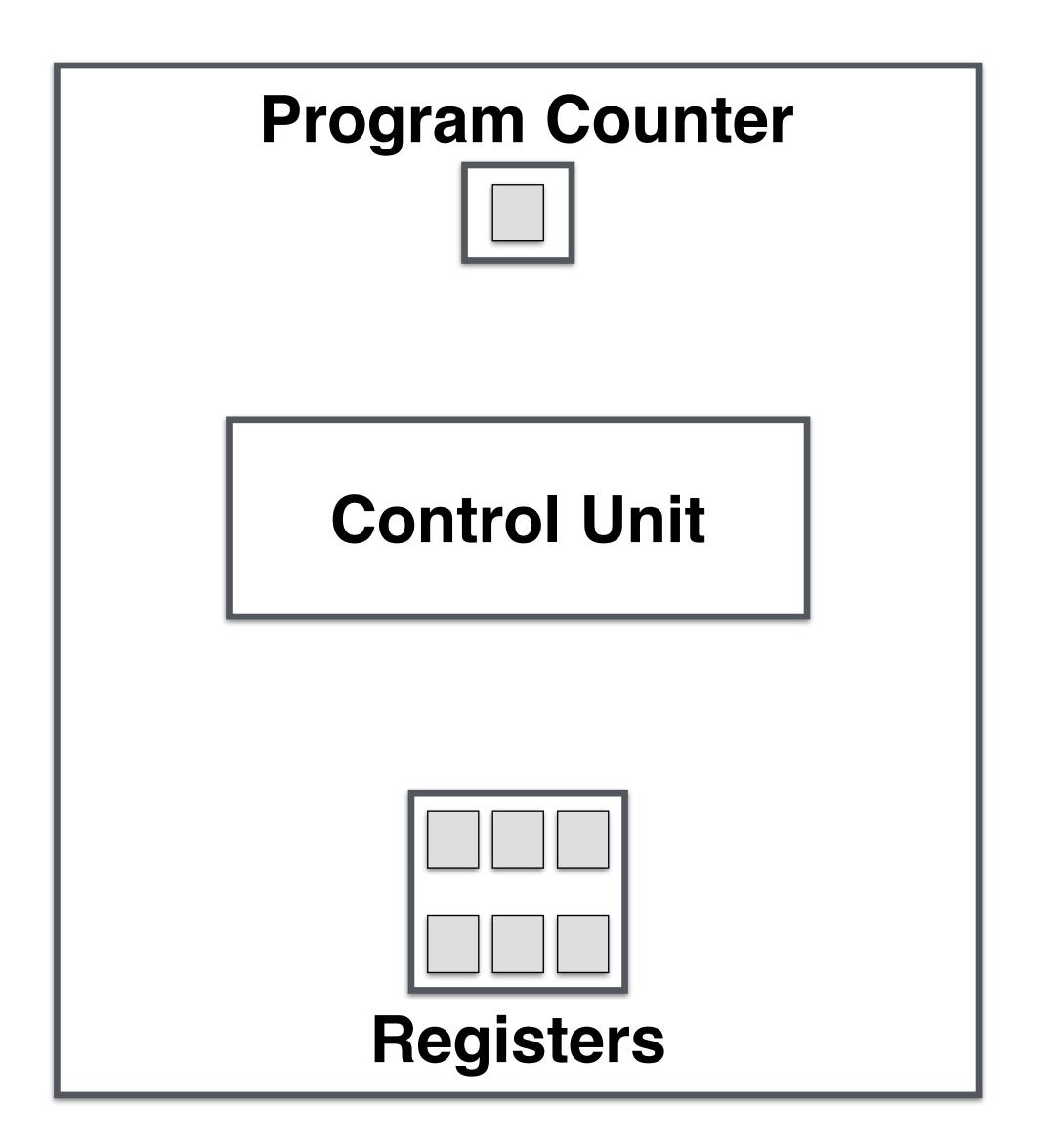
Give an abstract model of computation to study the performance of algorithms

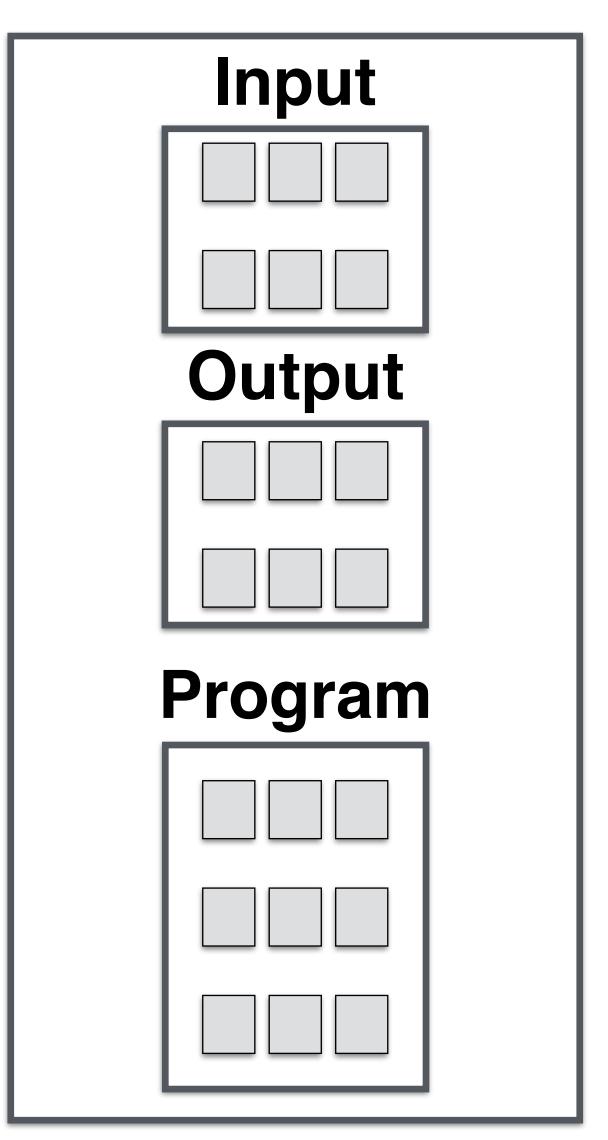
Abstract model is called Random-Access Machine model

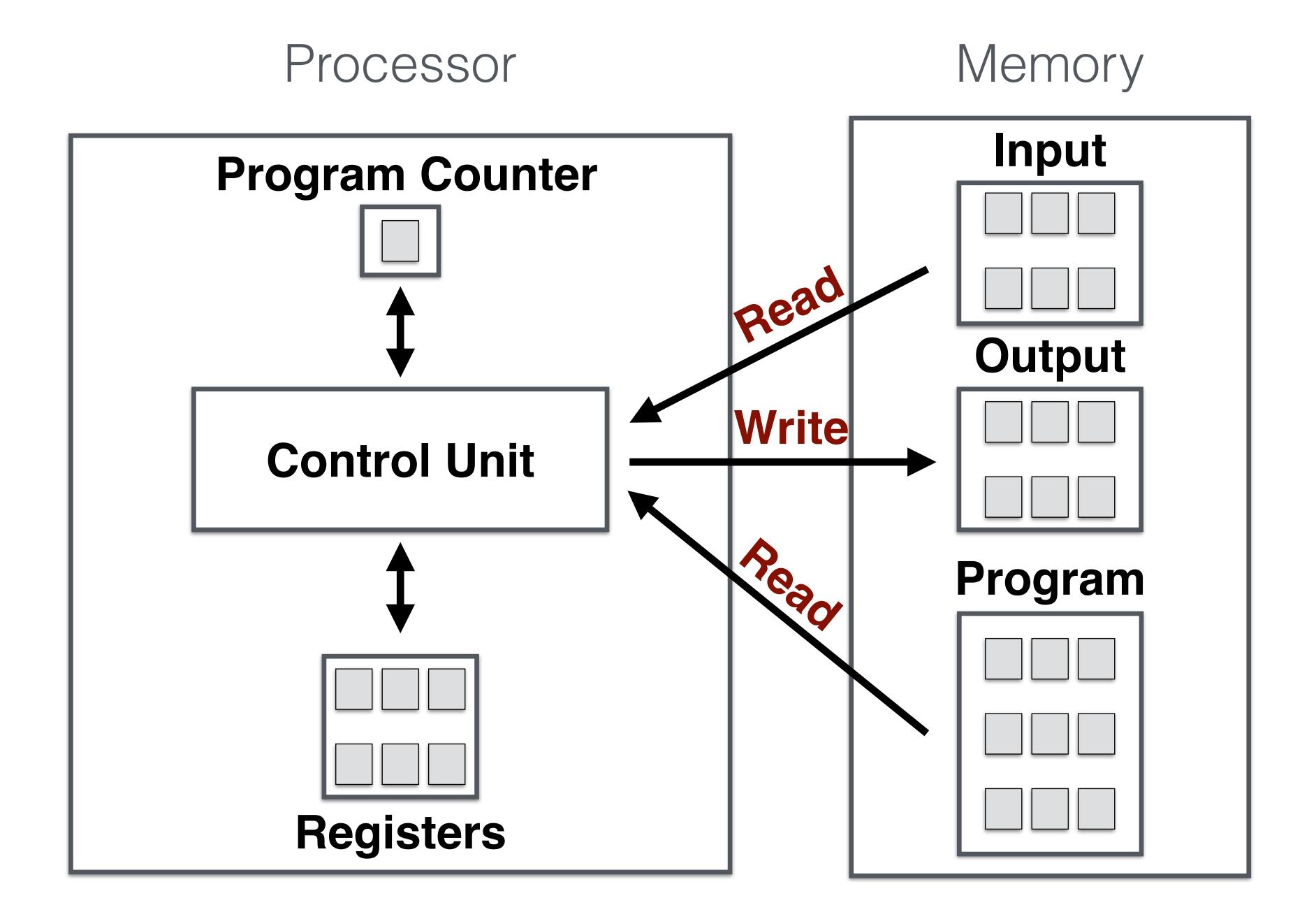
Why do we need an abstract model?

Processor

Memory



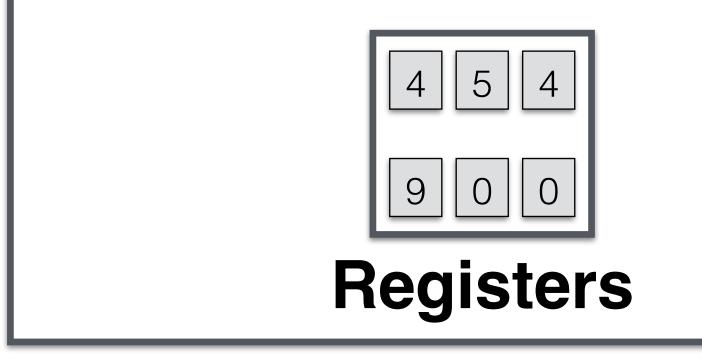


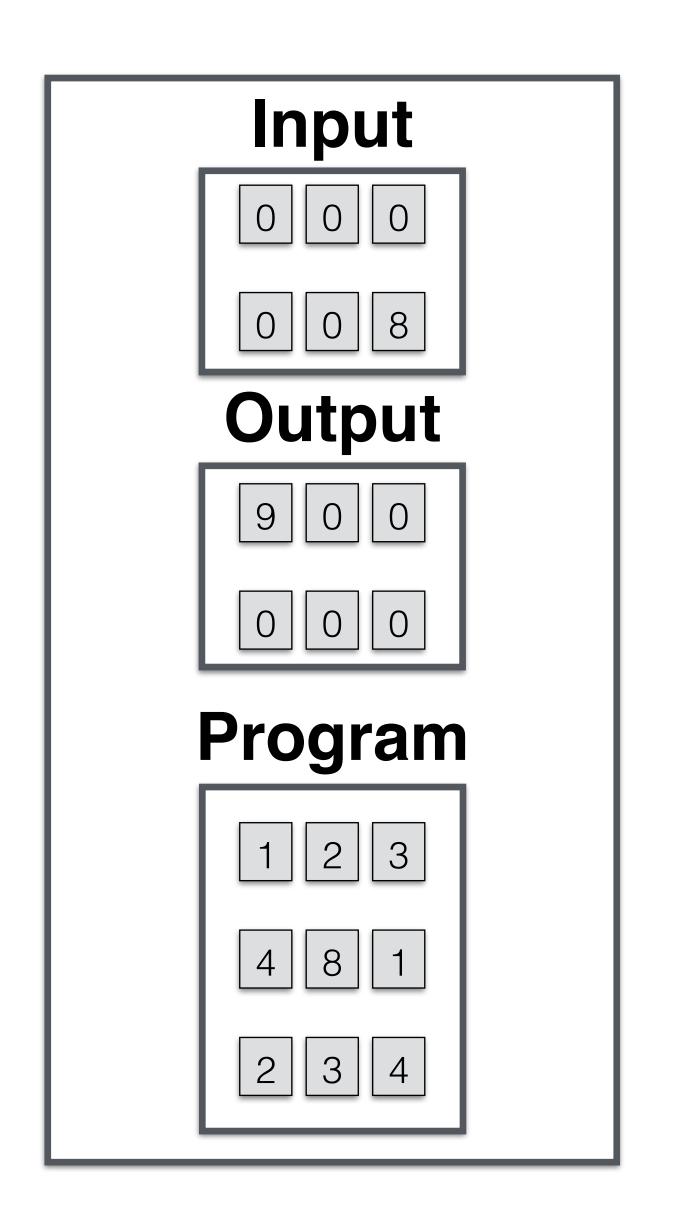


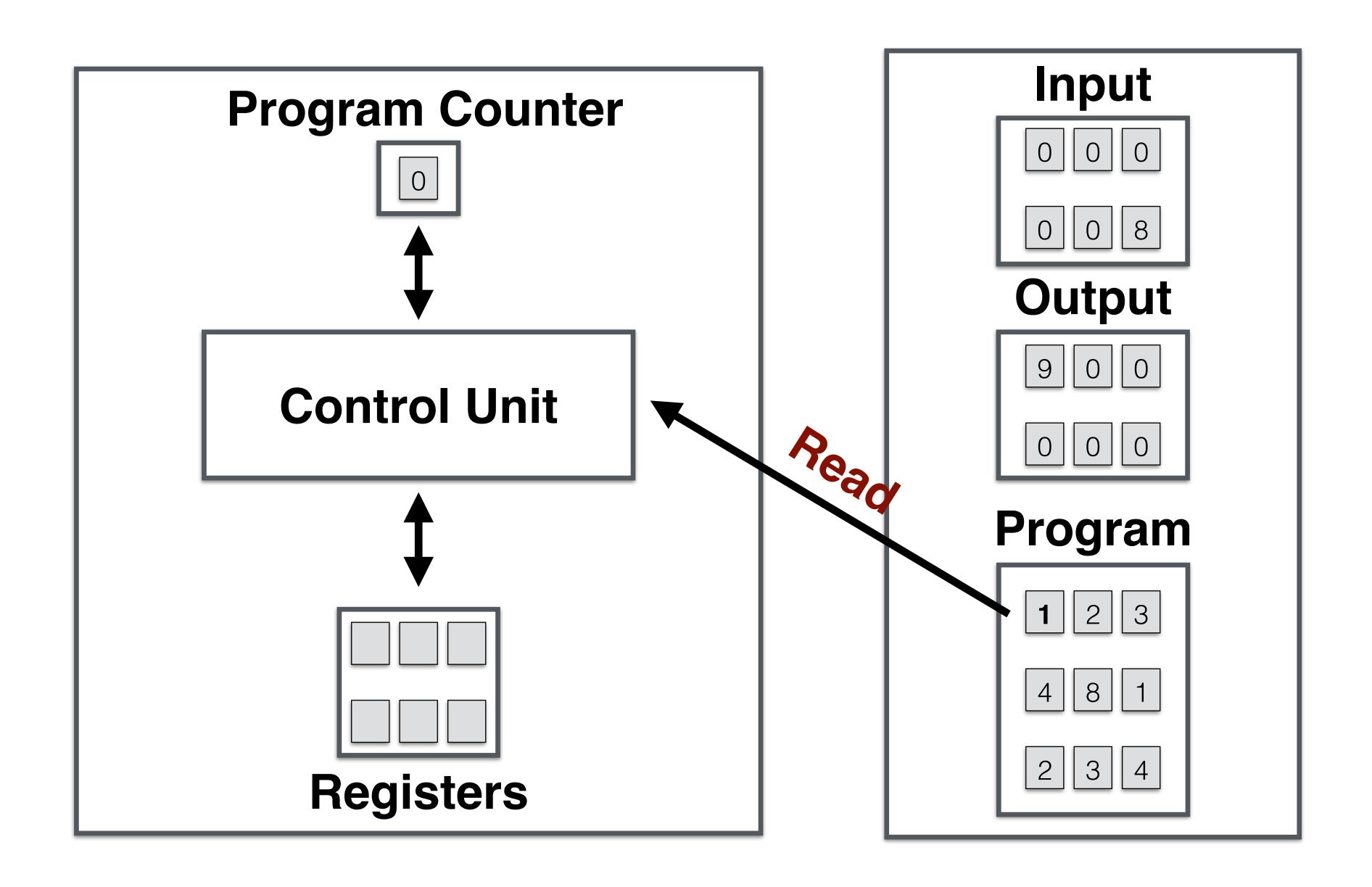
Each memory unit can store an arbitrary integer

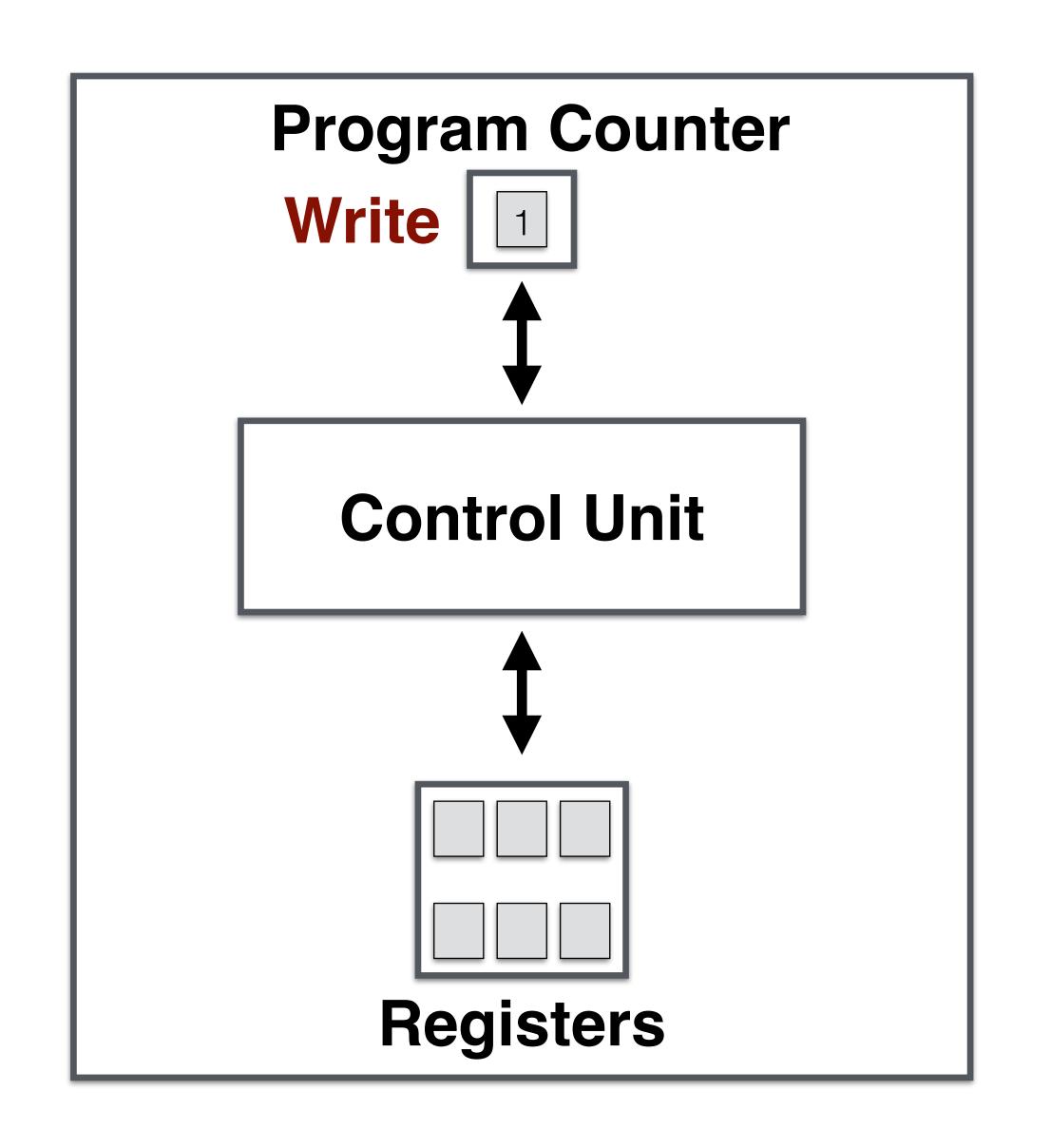
Must be non-negative for Program Counter

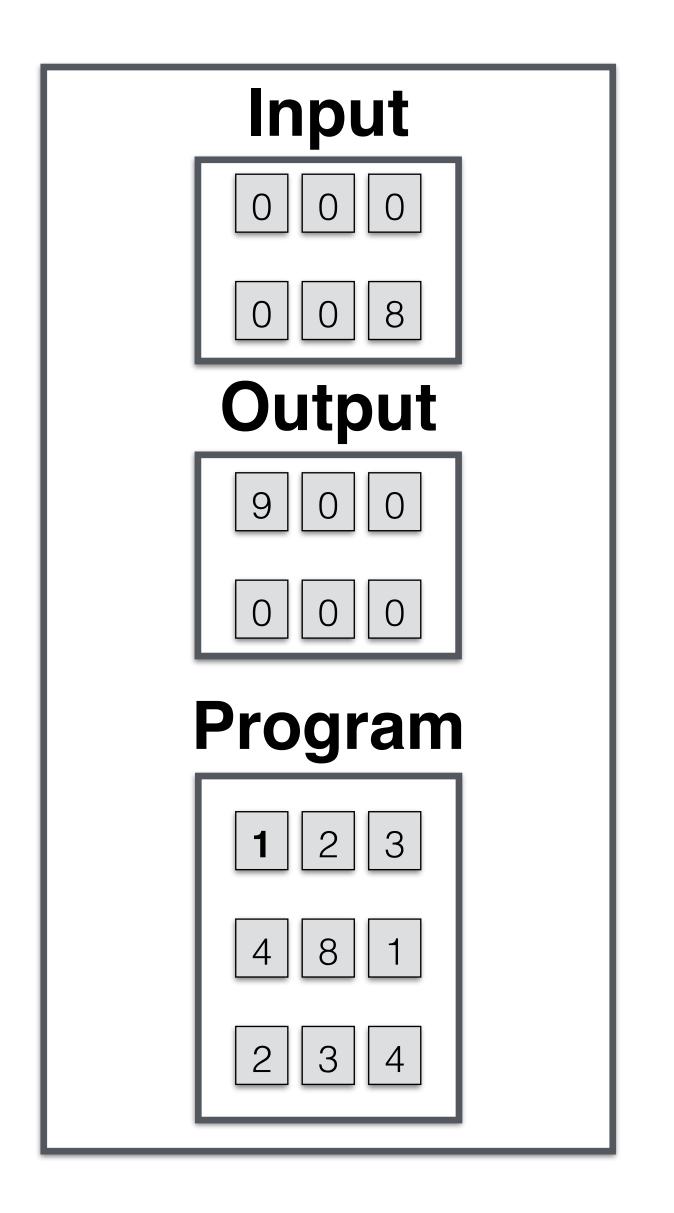
Depending on values, Control Unit does an operation

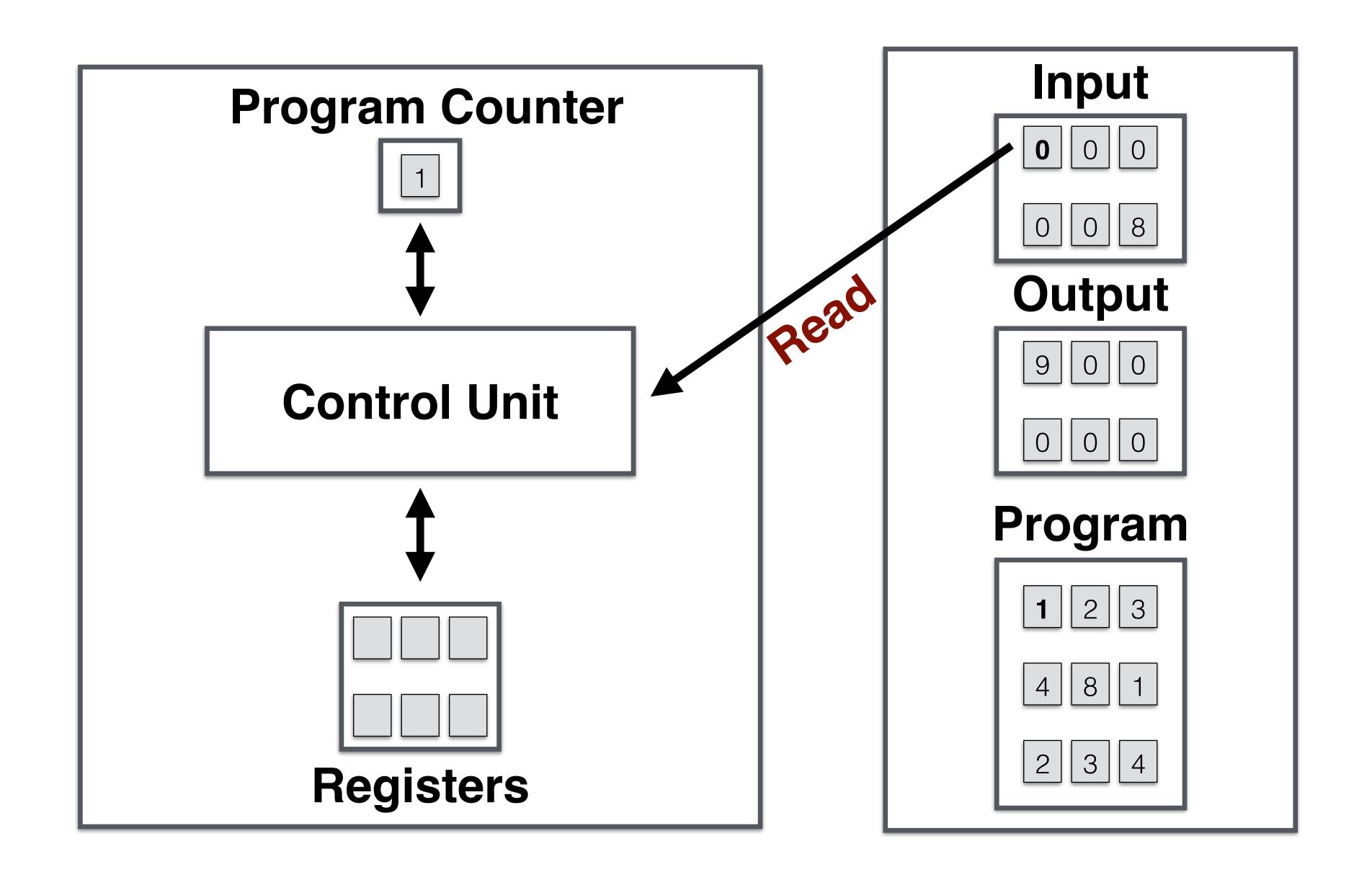


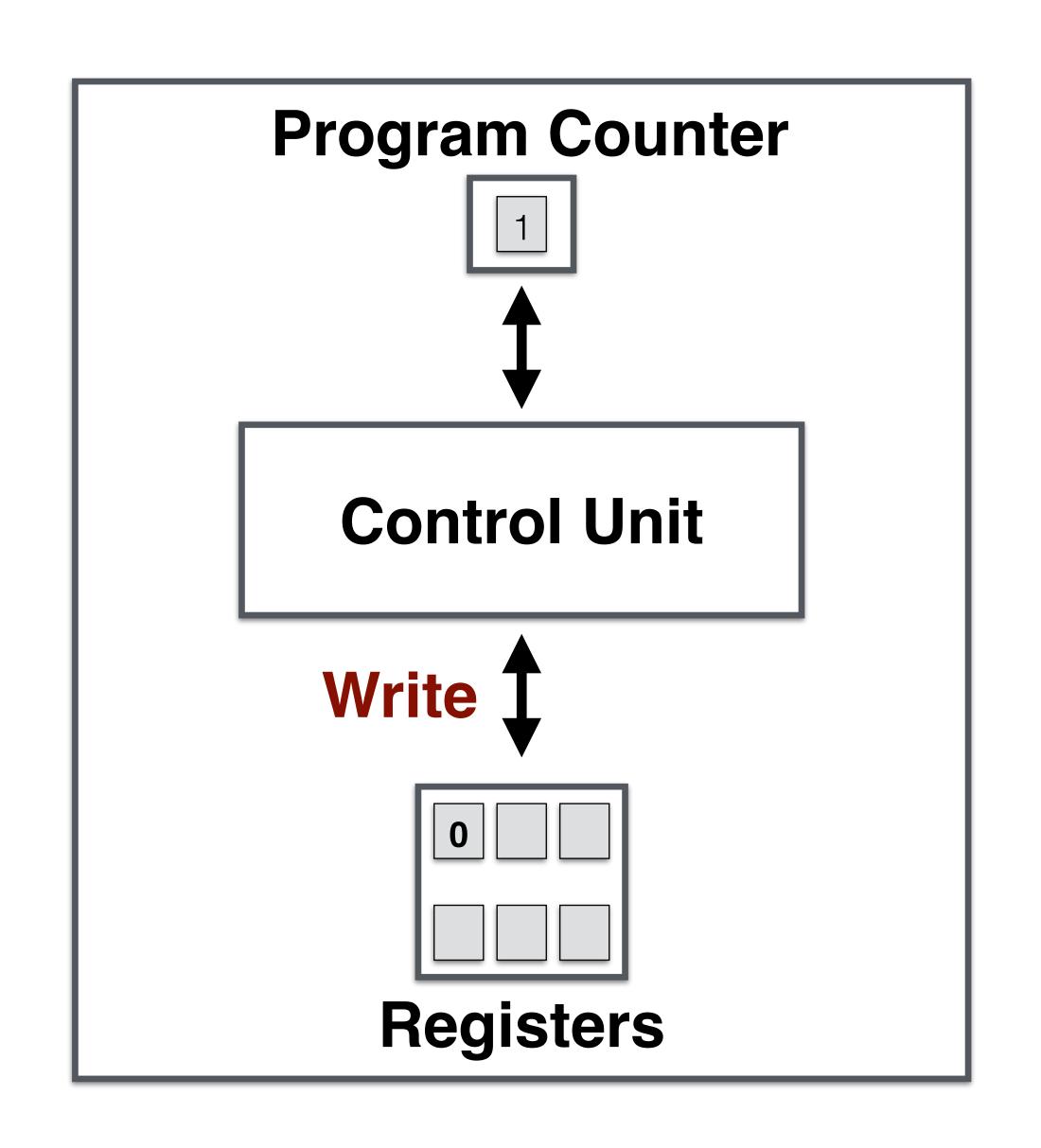


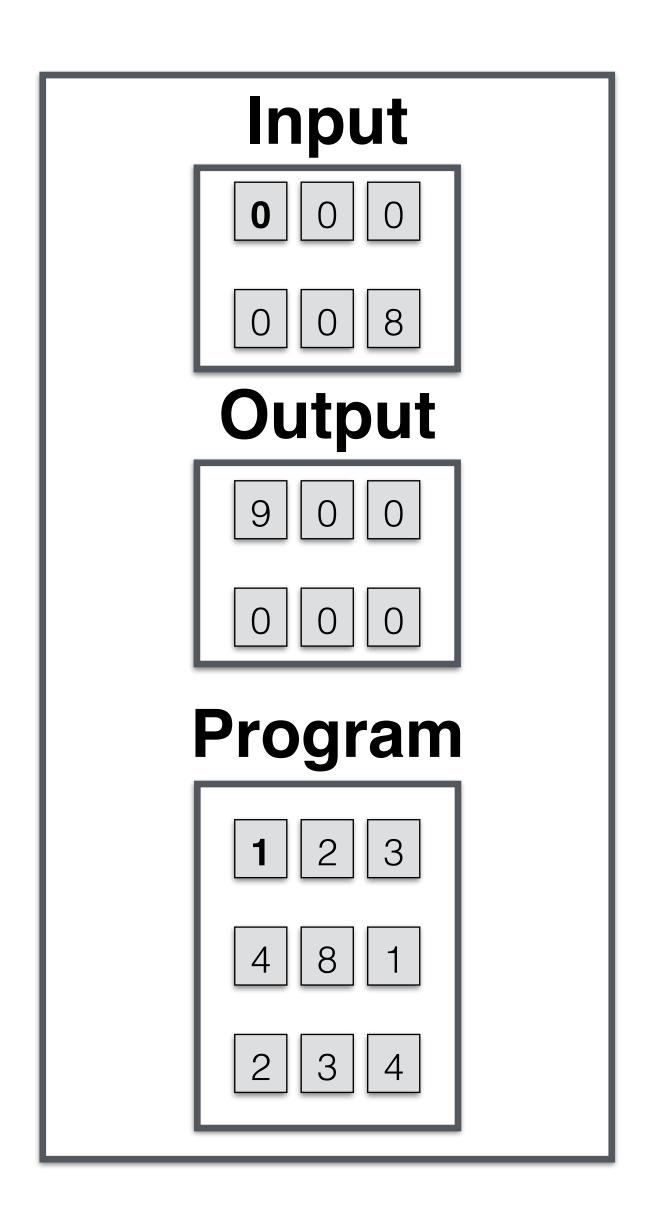


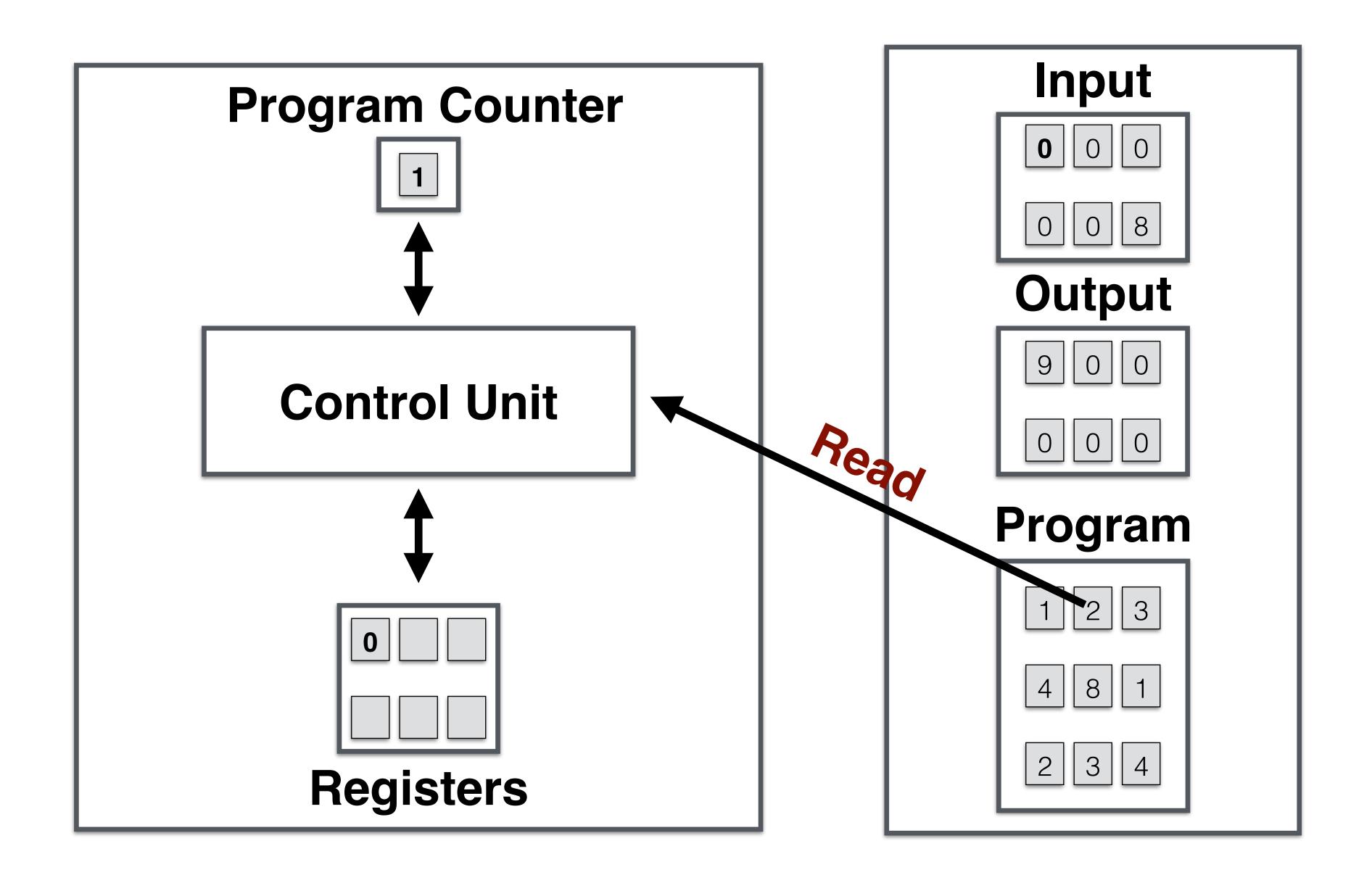












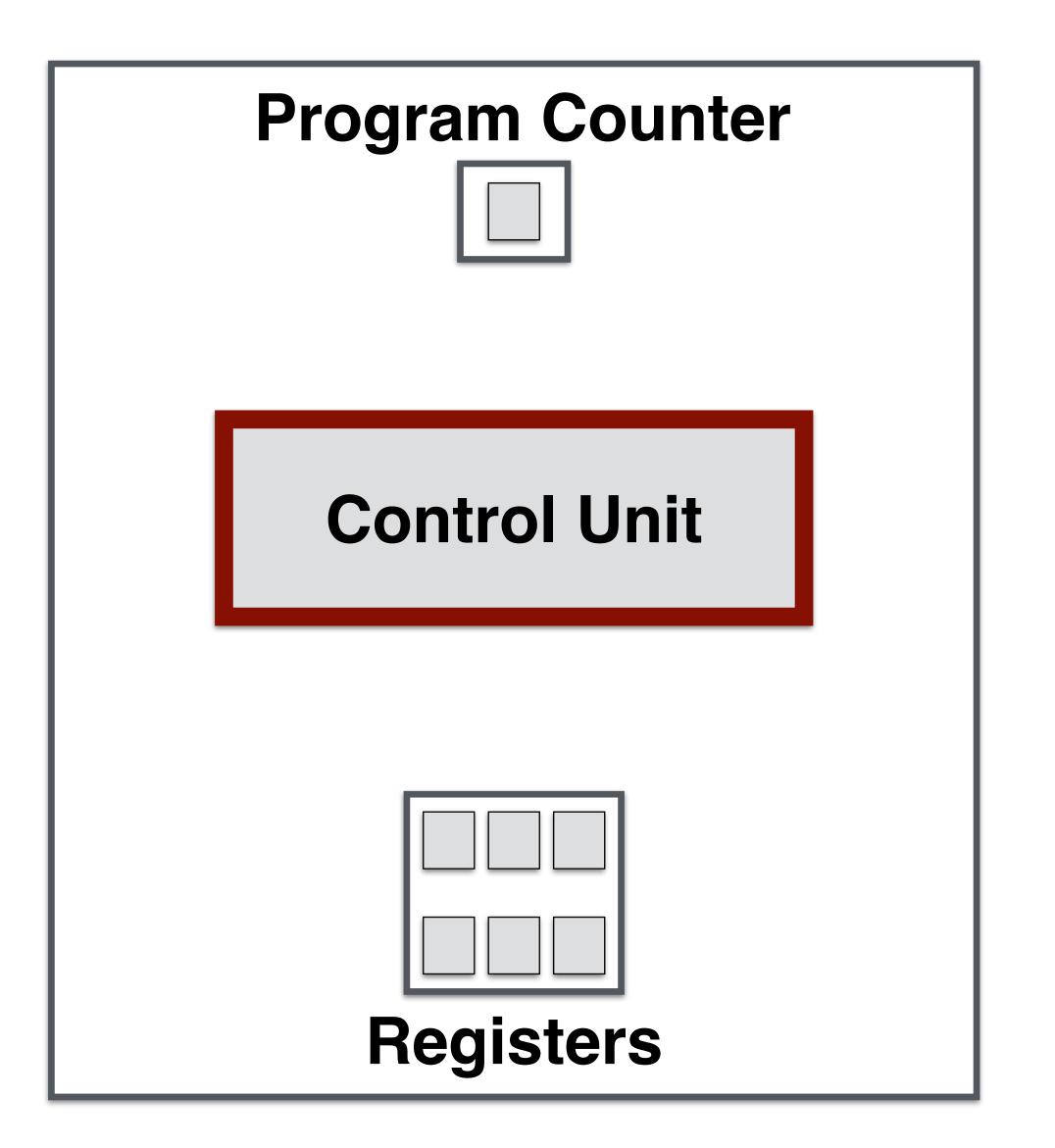
The Control Unit can read and write values in memory units if the index of that unit is specified in the program

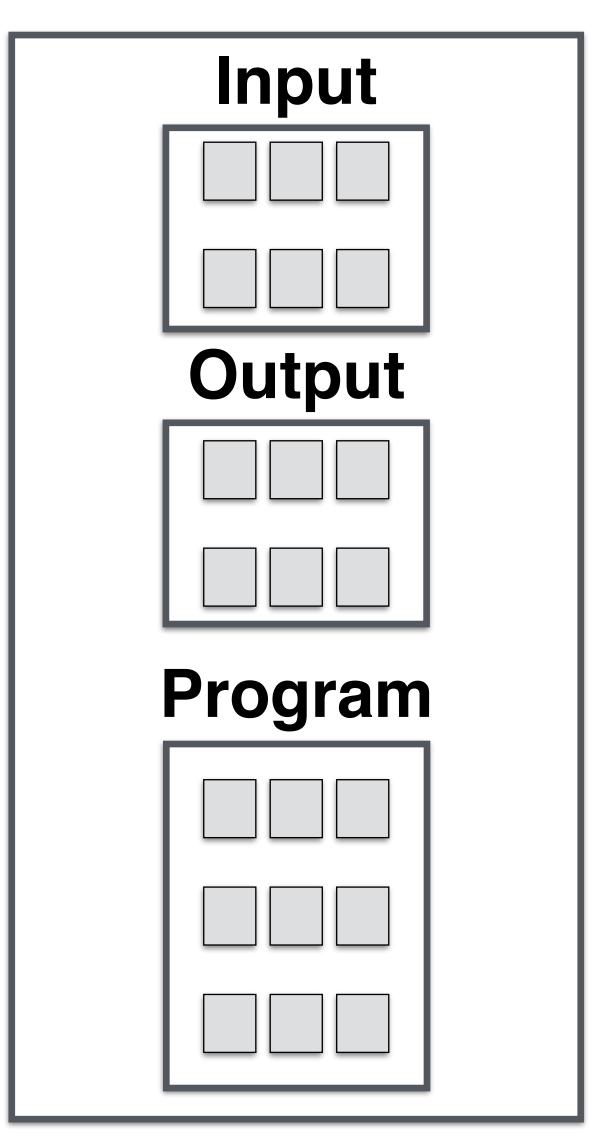
The Program Counter keeps track of the index of the next instruction to be carried out

The Control Unit

Processor

Memory





Control Unit can:

Read and write values in single memory units

Do simple arithmetic (add, subtract, multiply, divide)

Control Unit can:

Read and write values in single memory units

Do simple arithmetic (add, subtract, multiply, divide)

Each individual operation done in one time-step

Read and write values in single memory units

Do simple arithmetic (add, subtract, multiply, divide)

Above operations done in one time-step

Perform conditional operations: if then conditionals

One time-step for comparison in if statement

e.g. if (a*b == 0) takes two time-steps to compute

The "size" of the program stored in memory is not necessarily the number of time-steps in an implementation

Read, write, and copy values of memory units

Do simple arithmetic (add, subtract, multiply, divide)

Do conditional operations (if then)



Read, write, and copy values of memory units

Do simple arithmetic (add, subtract, multiply, divide)

Do conditional operations (if then)

Basically a basic calculator



Read, write, and copy values of memory units

Do simple arithmetic (add, subtract, multiply, divide)

Do conditional operations (if then)

Give RAM implementation for:

If x AND y == TRUE output 1

