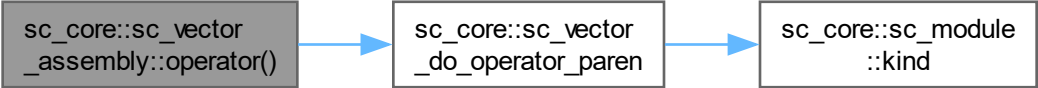


sc\_core::sc\_vector  
\_assembly::operator()

sc\_core::sc\_vector  
\_do\_operator\_paren

sc\_core::sc\_module  
::kind



```
graph LR; A["sc_core::sc_vector  
_assembly::operator()"] --> B["sc_core::sc_vector  
_do_operator_paren"]; B --> C["sc_core::sc_module  
::kind"]
```