C:/Users/BJRODIER/Documents /QT_REPO/QT_VHDL_simulator /code/Ressources/SystemC/include /sysc/communication/sc_prim_channel.h

C:/Users/BJRODIER/Documents /QT_REPO/QT_VHDL_simulator /code/Ressources/SystemC/include /sysc/communication/sc_fifo.h C:/Users/BJRODIER/Documents /QT_REPO/QT_VHDL_simulator /code/Ressources/SystemC/include /sysc/communication/sc_signal.h

C:/Users/BJRODIER/Documents /QT_REPO/QT_VHDL_simulator /code/Ressources/SystemC/include /sysc/communication/sc_buffer.h C:/Users/BJRODIER/Documents /QT_REPO/QT_VHDL_simulator /code/Ressources/SystemC/include /sysc/communication/sc_clock.h C:/Users/BJRODIER/Documents /QT_REPO/QT_VHDL_simulator /code/Ressources/SystemC/include /sysc/communication/sc_signal _resolved.h

C:/Users/BJRODIER/Documents /QT_REPO/QT_VHDL_simulator /code/Ressources/SystemC/include /sysc/communication/sc_signal_rv.h

C:/Users/BJRODIER/Documents /QT_REPO/QT_VHDL_simulator /code/Ressources/SystemC/include /sysc/communication/sc_signal _rv_ports.h