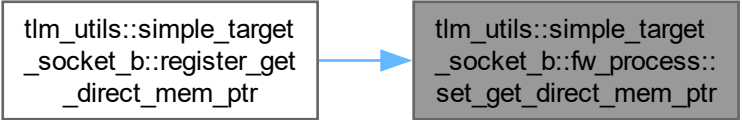


```
tlm_utils::simple_target  
_socket_b::register_get  
_direct_mem_ptr
```



```
graph LR; A["tlm_utils::simple_target  
_socket_b::register_get  
_direct_mem_ptr"] --> B["tlm_utils::simple_target  
_socket_b::fw_process::  
set_get_direct_mem_ptr"]
```

```
tlm_utils::simple_target  
_socket_b::fw_process::  
set_get_direct_mem_ptr
```