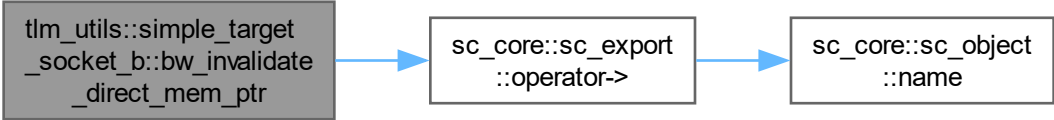


```
tlm_utils::simple_target  
_socket_b::bw_invalidate  
_direct_mem_ptr
```



```
graph LR; A["tlm_utils::simple_target  
_socket_b::bw_invalidate  
_direct_mem_ptr"] --> B["sc_core::sc_export  
::operator->"]; B --> C["sc_core::sc_object  
::name"]
```

```
sc_core::sc_export  
::operator->
```

```
sc_core::sc_object  
::name
```