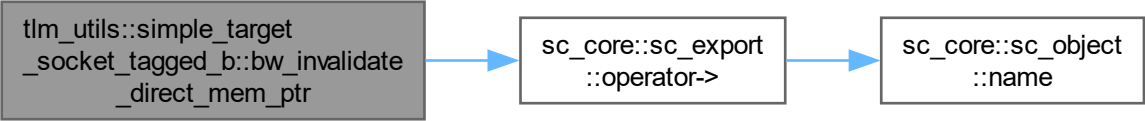


```
tlm_utils::simple_target  
_socket_tagged_b::bw_invalidate  
_direct_mem_ptr
```



```
graph LR; A["tlm_utils::simple_target  
_socket_tagged_b::bw_invalidate  
_direct_mem_ptr"] --> B["sc_core::sc_export  
::operator->"]; B --> C["sc_core::sc_object  
::name"]
```

The diagram consists of three rectangular boxes arranged horizontally, connected by blue arrows pointing from left to right. The first box on the left has a gray background and contains three lines of C++ code. The second box in the middle has a white background and contains two lines of C++ code. The third box on the right also has a white background and contains two lines of C++ code. All boxes have a thin black border.

```
sc_core::sc_export  
::operator->
```

```
sc_core::sc_object  
::name
```