

C:/Users/BJRODIER/Documents
/QT_REPO/QT_VHDL_simulator
/code/Qt_VHDL_SVG_proj/systemc
_modules/sc_clk.h

sc_config.h

systemc.h

```
graph TD; A["C:/Users/BJRODIER/Documents<br>/QT_REPO/QT_VHDL_simulator<br>/code/Qt_VHDL_SVG_proj/systemc<br>_modules/sc_clk.h"] --> B["sc_config.h"]; A --> C["systemc.h"]; B --> C;
```