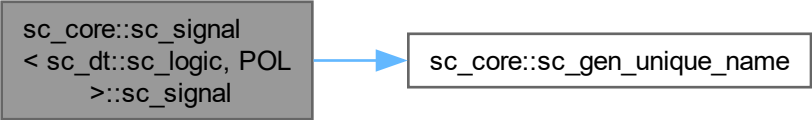


```
sc_core::sc_signal  
< sc_dt::sc_logic, POL  
  >::sc_signal
```



The diagram consists of two rectangular boxes connected by a blue arrow pointing from left to right. The left box is shaded gray and contains a C++-style type definition for `sc_core::sc_signal`. The right box is white with a black border and contains the text `sc_core::sc_gen_unique_name`.

```
sc_core::sc_gen_unique_name
```