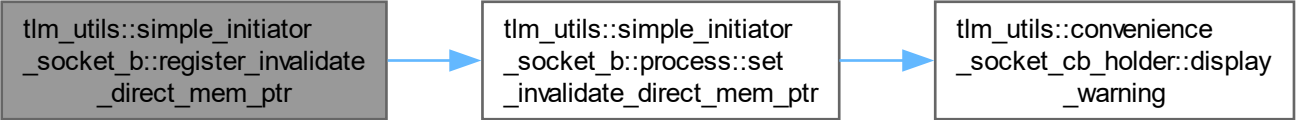


tlm\_utils::simple\_initiator  
\_socket\_b::register\_invalidate  
\_direct\_mem\_ptr



```
graph LR; A["tlm_utils::simple_initiator<br/>_socket_b::register_invalidate<br/>_direct_mem_ptr"] --> B["tlm_utils::simple_initiator<br/>_socket_b::process::set<br/>_invalidate_direct_mem_ptr"]; B --> C["tlm_utils::convenience<br/>_socket_cb_holder::display<br/>_warning"]
```

tlm\_utils::simple\_initiator  
\_socket\_b::process::set  
\_invalidate\_direct\_mem\_ptr

tlm\_utils::convenience  
\_socket\_cb\_holder::display  
\_warning