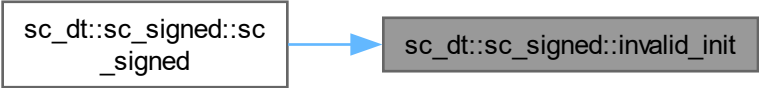


```
sc_dt::sc_signed::sc  
_signed
```



```
graph LR; A[sc_dt::sc_signed::sc_signed] --> B[sc_dt::sc_signed::invalid_init];
```

A diagram illustrating a state transition. On the left, a white rectangular box with a black border contains the text `sc_dt::sc_signed::sc_signed`. A blue arrow points from the right side of this box to the right side of a gray rectangular box with a black border on the right. The gray box contains the text `sc_dt::sc_signed::invalid_init`.

```
sc_dt::sc_signed::invalid_init
```