

sc\_core::sc\_strhash  
\_iter::contents

sc\_core::sc\_phash\_iter  
::contents

sc\_core::sc\_phash  
\_iter::contents

sc\_core::sc\_phash\_base  
\_iter::contents

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graph LR; A[sc_core::sc_strhash_iter::contents] --> D[sc_core::sc_phash_base_iter::contents]; B[sc_core::sc_phash_iter::contents] --> D; C[sc_core::sc_phash_iter::contents] --> D;
```

The diagram illustrates a mapping or inheritance relationship. Three white rectangular boxes on the left point via blue arrows to a single gray rectangular box on the right. The top box is labeled 'sc\_core::sc\_strhash\_iter::contents', the middle box 'sc\_core::sc\_phash\_iter::contents', and the bottom box 'sc\_core::sc\_phash\_iter::contents'. The target box on the right is labeled 'sc\_core::sc\_phash\_base\_iter::contents'.