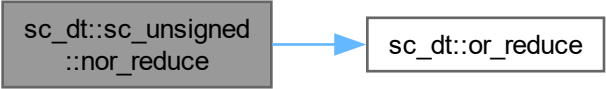


sc\_dt::sc\_unsigned  
::nor\_reduce



```
graph LR; A["sc_dt::sc_unsigned  
::nor_reduce"] --> B["sc_dt::or_reduce"]
```

The diagram illustrates a transformation from a C++ expression to a hardware module name. On the left, a gray rectangular box contains the C++ code `sc_dt::sc_unsigned::nor_reduce`. A blue arrow points from this box to a white rectangular box on the right, which contains the module name `sc_dt::or_reduce`. This represents a mapping where the `nor_reduce` operation is implemented by the `or_reduce` module in the `sc_dt` namespace.

sc\_dt::or\_reduce