

sc_dt::sc_signed_subref
_r::concat_get_data

sc_dt::sc_unsigned
_subref_r::concat_get_data

sc_dt::nor_reduce

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graph LR; A["sc_dt::sc_signed_subref_r::concat_get_data"] --> C["sc_dt::nor_reduce"]; B["sc_dt::sc_unsigned_subref_r::concat_get_data"] --> C;
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The diagram illustrates a logic operation where two data inputs are combined. On the left, there are two white rectangular boxes with black borders. The top box contains the text 'sc_dt::sc_signed_subref_r::concat_get_data' and the bottom box contains 'sc_dt::sc_unsigned_subref_r::concat_get_data'. Blue arrows point from the right side of each of these boxes to a single gray rectangular box on the right. This gray box contains the text 'sc_dt::nor_reduce', representing the output of the operation.