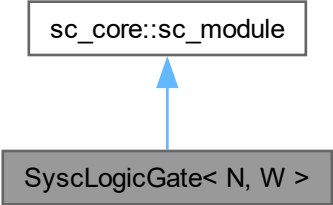


sc\_core::sc\_module



```
graph BT; A[SyscLogicGate< N, W >] --> B[sc_core::sc_module]
```

The diagram illustrates an inheritance relationship. A light gray box at the bottom contains the text 'SyscLogicGate< N, W >'. A blue arrow points vertically upwards from the top center of this box to the bottom center of a white box at the top. The white box contains the text 'sc\_core::sc\_module'.

SyscLogicGate< N, W >