

C:/Users/BJRODIER/Documents  
/QT\_REPO/QT\_VHDL\_simulator  
/code/Ressources/SystemC/include  
/sysc/tracing/sc\_trace\_file\_base.h

```
graph BT; A["C:/Users/BJRODIER/Documents<br>/QT_REPO/QT_VHDL_simulator<br>/code/Ressources/SystemC/include<br>/sysc/tracing/sc_vcd_trace.h"] --> C["C:/Users/BJRODIER/Documents<br>/QT_REPO/QT_VHDL_simulator<br>/code/Ressources/SystemC/include<br>/sysc/tracing/sc_trace_file_base.h"]; B["C:/Users/BJRODIER/Documents<br>/QT_REPO/QT_VHDL_simulator<br>/code/Ressources/SystemC/include<br>/sysc/tracing/sc_wif_trace.h"] --> C;
```

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