	SyscFlexInt< DEFAULTCLKEDGE, DEFAULT_FLEXINT >	SyscReg< 32, DEFAULT _CLKEDGE, DEFAULT_FLEXINT >
	SyscFlexInt< CLKEDGE, FLEXINT >	SyscAdd< 2, 32 >
	SyscLogicGate< 2, 1 >	SyscMux< 2, 32 >
	SyscLogicGate< 1, 1 >	SyscCnt< W, CLKEDGE, FLEXINT >
	SyscLogicGate< N, W >	SyscReg< W, CLKEDGE, FLEXINT >
	SyscLogicGate_pv< 2, 1 >	U1block< CLKEDGE, FLEXINT >
sc_core::sc_module	SyscTerminal< 1 >	SyscAdd< N, W >
sc_core::sc_process_host sc_core::sc_spawn_object< T >	tlm::tlm_req_rsp_channel < REQ, RSP, tlm_fifo< REQ >, tlm_fifo< RSP >>	SyscAnd< N, W >
sc_core::sc_spawn_object _v< T >	SyscFlexInt< CLKEDGE, FLEXINT >	SyscMux< N, W, SELW, SEL_INVALID_BEH, SEL _XORZ_CONV >
		SyscNand< N, W >
	SyscLogicGate< N, W >	SyscNor< N, W >
		SyscOr< N, W >
		SyscSub< N, W >
		SyscTri< W, TRI_XORZ _CONV >
		SyscXnor< N, W >
		SyscXor< N, W >
		SyscAdd_pv< N, W >
		SyscAnd_pv< N, W >
		SyscMux_pv< N, W, SELW, SEL_INVALID_BEH, SEL _XORZ_CONV >
	SyscLogicGate_pv< N, W >	SyscNand_pv< N, W >
	SyscTerminal< W >	SyscNor_pv< N, W >
	sc_core::sc_event_queue	SyscOr_pv< N, W >
	tlm::tlm_req_rsp_channel < REQ, RSP, REQ_CHANNEL, RSP_CHANNEL >	SyscSub_pv< N, W >
	tlm::tlm_slave_to_transport < REQ, RSP >	SyscXnor_pv< N, W > SyscXor_pv< N, W >
	tlm::tlm_transport	Syscln< W >
	_channel< REQ, RSP, REQ_CHANNEL, RSP_CHANNEL >	SyscOut< W >
	tlm::tlm_transport _to_master< REQ, RSP >	