

2013-2014

Page No.

Section

Page No.

Section 1: Analog and Digital Electronics

April 2013

Index Number: 1131313

- 1. ANSWER ALL QUESTIONS.
- 2. SECTION A: SHOW THE ANSWER TO ALL QUESTIONS FOR A TOTAL OF 20 MARKS. AND CIRCLE THE CORRECT ANSWER IN EACH QUESTION.
- 3. ANSWER SECTION B IN THE ANSWER BOOK.
- 4. BOOLEAN ALGEBRA THEOREMS ARE PROVIDED IN THE

1. Digital circuits play a very important role in today's electronic system employed in almost every facet of electronics, excluding:
- a) Control instrumentation
 - b) Communication
 - c) Computing
 - ☒ d) Amplification of weak signals
2. A logic gate is:
- a) A special type of amplifier circuit designed to amplify voltage corresponding to binary 1's and 0's.
 - b) A special type of amplifier circuit designed to amplify current corresponding to binary 1's and 0's.
 - ☒ c) A special type of voltage amplifier circuit designed to generate corresponding to binary 1's and 0's.
 - d) A special type of amplifier circuit designed to accept and generate signals corresponding to binary 1's and 0's.

3. Which of the following statements does NOT describe an advantage of CMOS technology?
- a) The convenient feature of CMOS is that it has very low static power consumption.

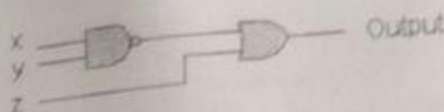
- d) In digital systems, that the circuitry used to generate the output is called a logic gate.
e) None of the above

4. What type of logic gate is this?
☒ a) 3 Inputs OR
☐ b) 3 Inputs AND
☐ c) 3 Inputs NOR
☐ d) 3 Inputs NAND
☐ e) none of the above

A	B	C	X
0	0	0	1
0	0	1	1
0	1	0	1
0	1	1	1
1	0	0	1
1	0	1	1
1	1	0	1
1	1	1	0

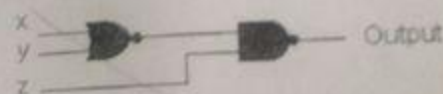
5. What function is implemented by the circuit shown

- ☒ a) $\overline{x}y + z$
☐ b) $\overline{(x+y)}z$
☐ c) $\overline{x}y\overline{z}$
☐ d) $\overline{x+y+z}$



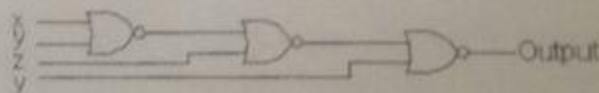
6. What function is implemented by the circuit shown

- a) $\overline{x+y+z}$
☒ b) $\overline{x+y+z}$
☐ c) $\overline{x}y\overline{z}$
☐ d) $\overline{x+y+z}$



7. What function is implemented by the circuit shown

- ☒ a) $\overline{xz} + y$
☐ b) $\overline{xz} + y$
☐ c) $\overline{x}y + y\overline{z}$
☐ d) $\overline{x}y + y\overline{z}$



8. Use Boolean algebra to simplify the logic function $A + (\overline{A} \cdot B)$
 (a) $A \cdot B + \overline{A}$

valuations for input switches A, B and C that generate a 1 at the output: (A=1, B=1, C=0), (A=1, B=0, C=1), (A=0, B=1, C=1),

- Complete the truth table
- Write the Boolean expression
- Draw the Logic circuit

A	B	C	output
0	0	0	
1	1	0	
1	0	1	
0	1	1	

F. K AMPONG

Appendix A

Single-Variable Theorems

1a. $x \cdot 0 = 0$

1b. $x + 1 = 1$

2a. $x \cdot 1 = x$

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Appendix A

Single-Variable Theorems

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1b. $x + 1 = 1$

2a. $x \cdot 1 = x$

Index number.....

6225111

Electronics II

March 2014

TIME : 1 Hr

Answer all questions

Answer section A on the question paper and section B in your answer booklet.
(Find attached: Appendix A - list of Boolean algebra theorems)

Section A [10 Marks]

1. Digital circuits play a very important role in today's electronic systems. They are employed in almost every facet of electronics, excluding,
 - a) Control Instrumentation
 - b) Communication
 - c) Computing
 - d) Amplification of weak signals
 - ☒ e) None of the above
2. A logic gate is:
 - a) A special type of amplifier circuit designed to amplify voltage signals corresponding to binary 1's and 0's.
 - b) A special type of amplifier circuit designed to amplify current signals corresponding to binary 1's and 0's.
 - c) A special type of voltage amplifier circuit designed to generate voltage signals corresponding to binary 1's and 0's.
 - ☒ d) A special type of amplifier circuit designed to accept and generate voltage signals corresponding to binary 1's and 0's.
 - e) None of the above
3. Which of the following statements about a logic gate is false?
 - a) Gate circuits are most commonly represented in a schematic by their own unique symbols rather than by their constituent transistors and resistors.
 - b) Logic circuits provide solution to a problem. They implement functions that are needed to carry out specific tasks
 - ☒ c) Within the framework of a computer, logic circuits do not provide complete capability for execution of programs and processing of data.

Index number.....

6225111

Electronics II

March 2014

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Answer all questions

Answer section A on the question paper and section B in your answer booklet.
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Section A [10 Marks]

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 - a) Control Instrumentation
 - b) Communication
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 - a) Gate circuits are most commonly represented in a schematic by their own unique symbols rather than by their constituent transistors and resistors.
 - b) Logic circuits provide solution to a problem. They implement functions that are needed to carry out specific tasks
 - ☒ c) Within the framework of a computer, logic circuits do not provide complete capability for execution of programs and processing of data.

- d) In digital systems, the convenient feature of using 100% VCC voltage levels is that the circuitry used to generate, manipulate and store them is very simple
- e) None of the above

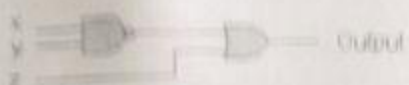
4. What type of logic gate is this?

- a) 3 inputs OR
- b) 3 inputs AND
- c) 3 inputs NOR
- d) 3 inputs NAND
- e) none of the above

A	B	C	X
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	0
1	0	0	0
1	0	1	0
1	1	0	0
1	1	1	0

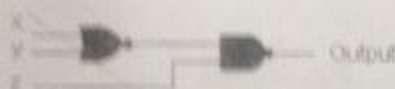
5. What function is implemented by the circuit shown

- a) $\overline{x} \cdot \overline{y} + z$
- b) $\overline{x}(\overline{x} + y)z$
- c) $\overline{x} \cdot \overline{y} \cdot z$
- d) $\overline{x} + \overline{y} + z$



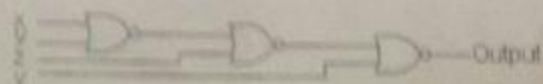
6. What function is implemented by the circuit shown

- a) $\overline{x} + \overline{y} + z$
- b) $\overline{x} + \overline{y} + \overline{z}$
- c) $\overline{x} \cdot \overline{y} \cdot z$
- d) $\overline{x} + \overline{y} + \overline{z}$



What function is implemented by the circuit shown

- a) $\overline{x}z + y$
- b) $\overline{x}z + \overline{y}$
- c) $\overline{x} \cdot \overline{y} + y \cdot z$
- d) $\overline{x} \cdot \overline{y} + \overline{y} \cdot z$



Use Boolean algebra to simplify the logic function $A + (\overline{A} \cdot B)$

- (a) $A \cdot B + \overline{A}$

- d) In digital systems, the convenient feature of using 10V/0V voltage levels is that the circuitry used to generate, manipulate and store them is very simple
- e) None of the above

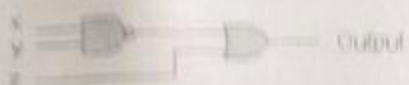
4. What type of logic gate is this?

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- d) 3 inputs NAND
- e) none of the above

A	B	C	X
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	0
1	0	0	0
1	0	1	0
1	1	0	0
1	1	1	0

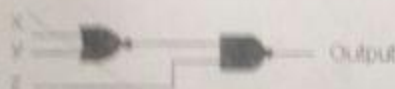
5. What function is implemented by the circuit shown

- a) $\overline{x} \cdot \overline{y} + z$
- b) $\overline{x}(\overline{x} + y)z$
- c) $\overline{x} \cdot \overline{y} \cdot z$
- d) $\overline{x} + \overline{y} + z$



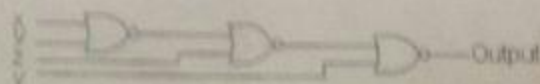
6. What function is implemented by the circuit shown

- a) $\overline{x} + \overline{y} + z$
- b) $\overline{x} + \overline{y} + \overline{z}$
- c) $\overline{x} \cdot \overline{y} \cdot z$
- d) $\overline{x} + \overline{y} + \overline{z}$



What function is implemented by the circuit shown

- a) $\overline{x}z + y$
- b) $\overline{x}z + \overline{y}$
- c) $\overline{x} \cdot \overline{y} + y \cdot z$
- d) $\overline{x} \cdot \overline{y} + \overline{y} \cdot z$



Use Boolean algebra to simplify the logic function $A + (\overline{A} \cdot B)$

- (a) $A \cdot B + \overline{A}$

30. Simplify the expression: $\overline{(A + C)(B + D)} = F$

(a) $A\bar{C} + B\bar{D}$

(b) $\bar{A}\bar{C} + \bar{B}\bar{D}$

(c) $A\bar{C} + \bar{B}D$

(d) $A\bar{C} + \bar{B}\bar{D}$

$(\bar{A} + \bar{C}) + \bar{B} + \bar{D}$



Figure 1

31. Figure 1 is the alternate logic gate representation for a

(a) NOR

(b) AND

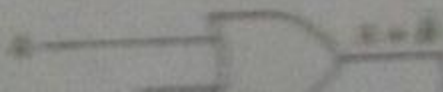
(c) NOT

(d) NAND

SECTION B THEORY: ANSWER THIS SECTION IN YOUR BOOKLETS

32. For the circuit in figure 2 perform the following:

- I. Write down the Boolean expression.
- II. Simplify the Boolean expression.
- III. Draw the simplified circuit.



4. A standard way of representing the behavior of logic circuits is to use
- (a) Boolean algebra
 - (b) Boolean algebra theorems and axioms
 - (c) Universal gates
 - (d) Truth tables

5. The output of an AND gate with three inputs, A, B, and C, is HIGH when
- (a) $A = 1, B = 1, C = 0$
 - (b) $A = 0, B = 0, C = 0$
 - (c) $A = 1, B = 1, C = 1$
 - (d) $A = 1, B = 0, C = 1$

A	B	C
0	0	0
0	0	1

6. If a 3-input NOR gate has eight input possibilities, how many of those possibilities will result in a HIGH output?

(a) 1

(b) 2

(c) 7

(d) 8

7. Use Boolean algebra to simplify the logic function $A + (\bar{A}B)$

(a) $AB + \bar{A}$

(b) $A + B$

(c) $\bar{A} + A$

(d) $\bar{A}A$

$$A + (\bar{A}B)$$

$$A + B$$

8. Use Boolean algebra to simplify the logic function $A(B + \bar{B}C)$

(a) $A(B + C)$

(b) $A + \bar{B}$

(c) $AC + B$

(d) $A + BC$

$$AB + B\bar{B}C$$

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(b) 2

(c) 7

(d) 8

7. Use Boolean algebra to simplify the logic function $A + (\bar{A}B)$.

(a) $A\bar{B} + \bar{A}$

(b) $A + B$

(c) $\bar{A} + A$

(d) $\bar{A}A$

$$A + (\bar{A}B)$$

$$A + B$$

8. Use Boolean algebra to simplify the logic function $A(B + \bar{B}C)$.

(a) $A(B + C)$

(b) $A + \bar{B}$

(c) $AC + B$

(d) $A + B\bar{C}$

$$AB + \bar{B}C$$

ANALOG AND DIGITAL ELECTRONICS

QUESTION PAPER

For Candidates (Private) who cannot participate in the examination

SECTION VI-A

Exam 351 Analog and Digital Electronics

April 2013

Index Number: 1131313

- 1. ANSWER THE QUESTIONS
- 2. SECTION A HAS THE ANSWER TO ALL QUESTIONS FOR A SINGLE SHEET, AND GIVE THE CORRECT ANSWER IN THE ANSWER BOOK
- 3. ANSWER SECTION B IN THE ANSWER BOOK
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 - b) Communication
 - c) Computing
 - ☒ d) Amplification of weak signals
- 2. A logic gate is:
 - a) A special type of amplifier circuit designed to amplify voltages corresponding to binary 1's and 0's.
 - b) A special type of amplifier circuit designed to amplify currents corresponding to binary 1's and 0's.
 - ☒ c) A special type of voltage amplifier circuit designed to generate voltages corresponding to binary 1's and 0's.
 - d) A special type of amplifier circuit designed to accept and generate signals corresponding to binary 1's and 0's.

- 3. Which of the following statements does NOT describe an advantage of CMOS technology?
 - a) The convenient feature of using 0V and VDD voltage levels is that

(d) \overline{B}

7. Use Boolean algebra to simplify the logic function $X = (A \oplus B)$

(a) $A(B) + \overline{A}\overline{B}$

$A + (\overline{A} \cdot \overline{B})$

(b) $A + B$

$A + B$

(c) $\overline{A} + \overline{B}$

(d) $\overline{A} \cdot \overline{B}$

8. Use Boolean algebra to simplify the logic function $A(B + \overline{B} \cdot C)$

(a) $A(B + C)$

$A(B + \overline{B} \cdot C)$

(b) $A + \overline{B}$

(c) $A \cdot C + B$

(d) $A + B \cdot C$

9. Simplify the logic function $F = (B + A)(B + C)$

(a) $B + A \cdot C$

$AB + AC + BC$

(b) $B \cdot A \cdot B$

$A + B + C$

(c) $(A + B)(B + C)$

$BC + AC + AB$

(d) $C + A \cdot B$

$AB + AC$

$A + B + C$

$B + A \cdot C$

10. Simplify the expression: $\overline{(A + C)(B + D)} = F$

(a) $A\bar{C} + B\bar{D}$

(b) $\bar{A}\bar{C} + \bar{B}\bar{D}$

(c) $A\bar{C} + \bar{B}D$

☒ (d) $A\bar{C} + \bar{B}\bar{D}$

$\overline{(A + C)} + \overline{(B + D)}$



Figure 1

11. Figure 1 is the alternate logic gate representation for a

(a) NOR

(b) AND

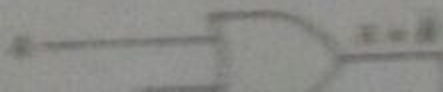
(c) NOT

☒ (d) NAND

SECTION B THEORY: ANSWER THIS SECTION IN YOUR BOOKLETS

12. For the circuit in figure 2 perform the following:

- I. Write down the Boolean expression.
- II. Simplify the Boolean expression.
- III. Draw the simplified circuit.



$$AB + B = B$$

$$B(A+1)$$

$$B \cdot 1 = B$$

Alternate:

$$B \cdot 1 = B$$

$$B(A+1)$$

$$AB + B$$

Single Variable Theorems

- 1a) $x \cdot 0 = 0$
- 1b) $x + 1 = 1$
- 2a) $x \cdot 1 = x$
- 2b) $x + 0 = x$
- 3a) $x \cdot x = x$
- 3b) $x + x = x$
- 4a) $x \cdot \bar{x} = 0$
- 4b) $x + \bar{x} = 1$
- 5) $\bar{\bar{x}} = x$

Two and Three Variable Properties

- 1a) $x \cdot y = y \cdot x$ Cumulative
- 1b) $x + y = y + x$
- 2a) $x \cdot (y \cdot z) = (x \cdot y) \cdot z$ Associative
- 2b) $x + (y + z) = (x + y) + z$
- 3a) $x \cdot (y + z) = x \cdot y + x \cdot z$ Distributive
- 3b) $x + y \cdot z = (x + y) \cdot (x + z)$
- 4a) $x + x \cdot y = x$ Absorption
- 4b) $x \cdot (x + y) = x$
- 5a) $x \cdot y + x \cdot \bar{y} = x$ Combining
- 5b) $(x + y) \cdot (x + \bar{y}) = x$
- 6a) $\overline{x \cdot y} = \bar{x} + \bar{y}$ DeMorgan's Theorem
- 6b) $\overline{x + y} = \bar{x} \cdot \bar{y}$
- 7a) $x + \bar{x} \cdot y = x + y$
- 7b) $x \cdot (\bar{x} + y) = x \cdot y$
- 8a) $x \cdot y + \bar{x} \cdot y = y$ Consensus
- 8b) $(x + y) \cdot (y + z) \cdot (x + z) = (x + y) \cdot (x + z)$

4. A standard way of representing the behavior of logic circuits is to use
- (a) Boolean algebra
 - (b) Boolean algebra theorems and axioms
 - (c) Universal gates
 - (d) Truth tables

5. The output of an AND gate with three inputs, A, B, and C, is HIGH when
- (a) $A = 1, B = 1, C = 0$
 - (b) $A = 0, B = 0, C = 0$
 - (c) $A = 1, B = 1, C = 1$
 - (d) $A = 1, B = 0, C = 1$

A	B	C
0	0	0
0	0	1

6. If a 3-input NOR gate has eight input possibilities, how many of those possibilities will result in a HIGH output?

(a) 1

(b) 2

(c) 7

(d) 8

7. Use Boolean algebra to simplify the logic function $A + (\bar{A}B)$

(a) $AB + \bar{A}$

(b) $A + B$

(c) $\bar{A} + A$

(d) $\bar{A}A$

$$A + (\bar{A}B)$$

$$A + B$$

8. Use Boolean algebra to simplify the logic function $A(B + \bar{B}C)$

(a) $A(B + C)$

(b) $A + \bar{B}$

(c) $AC + B$

(d) $A + BC$

$$AB + \bar{B}AC$$

4. A standard way of representing the behavior of logic circuits is to use
- ☒ Boolean algebra
 - ☐ Boolean algebra theorems and axioms
 - ☐ Universal gates
 - ☐ Truth tables

5. The output of an AND gate with three inputs A, B, and C is HIGH
- ☒ if $A = 1, B = 1, C = 0$
 - ☐ if $A = 0, B = 0, C = 1$
 - ☐ if $A = 1, B = 1, C = 1$
 - ☐ if $A = 1, B = 0, C = 1$

A	B	C
0	0	0
0	0	1

6. If a 3-input NOR gate has eight input possibilities, how many of them will result in a HIGH output?

- ☒ 1
- ☐ 2
- ☐ 7
- ☐ 8

7. Use Boolean algebra to simplify the logic function $X = (\bar{A}B)$

- ☐ $X(B) + \bar{A}$
- ☒ $X = B$
- ☐ $\bar{A} + X$
- ☐ $\bar{A}X$

$$X = (\bar{A}B)$$

$$X = B$$

8. Use Boolean algebra to simplify the logic function $X(B + \bar{C})$

- ☒ $X(B + \bar{C})$
- ☐ $X = \bar{B}$
- ☐ $X(C + B)$
- ☐ $X = B + C$

$$X = B + \bar{C}$$

4. A standard way of representing the behavior of logic circuits is to use:
- ☒ Boolean algebra
 - Boolean algebra theorems and axioms
 - Universal gates
 - Truth tables

5. The output of an AND gate with three inputs A, B, and C, is HIGH when:
- ☒ A = 1, B = 1, C = 0
 - A = 0, B = 0, C = 0
 - A = 1, B = 1, C = 1
 - A = 1, B = 0, C = 1

A	B	C
0	0	0
0	0	1

6. If a 3-input NOR gate has eight input possibilities, how many of those possibilities will result in a HIGH output?

☒ 1

(b) 2

(c) 7

(d) 8

7. Use Boolean algebra to simplify the logic function $A + (\bar{A}B)$.

(a) $A.B + \bar{A}$

☒ (b) $A + B$

(c) $\bar{A} + A$

(d) $\bar{A}A$

$$A + (\bar{A}B)$$

$$A + B$$

8. Use Boolean algebra to simplify the logic function $A(B + \bar{B}C)$.

☒ (a) $A(B + C)$

(b) $A + \bar{B}$

(c) $A.C + \bar{B}$

(d) $A + B.C$

$$A(B + \bar{B}C)$$

$$A + B.C$$

10. Simplify the expression $(\overline{A} + C)(B + D) + \overline{A}$

- (a) $\overline{A} + BD$
- (b) $\overline{A} + BC$
- (c) $\overline{A} + BH$
- ☒ (d) $\overline{A} + BH$

$$(\overline{A} + C)(B + D) + \overline{A}$$



Figure 1

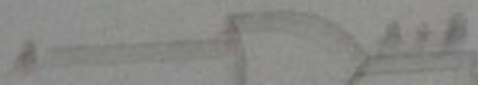
11. Figure 1 is the alternate logic gate representation for a

- (a) NOR
- (b) AND
- (c) NOT
- ☒ (d) NAND

SECTION B THEORY: ANSWER THIS SECTION IN YOUR A BOOKLETS

12. For the circuit in figure 2 perform the following:

- I. Write down the Boolean expression
- II. Simplify the Boolean expression
- III. Draw the simplified circuit



- ☒ (a) $A + B$
☐ (c) $\bar{A} + A$
☐ (d) $\bar{A}A$

9. Use Boolean algebra to simplify the logic function $A(B + \bar{B}C)$.

- ☐ (a) $A(B + C)$
☐ (b) $A + \bar{B}$
☐ (c) $A.C + B$
☒ (d) $A + B.C$

10. Simplify the logic function $F = (B+A)(B+C)$.

- ☐ (a) $B+A.C$
☐ (b) $B.A+B$
☒ (c) $(A+B)(B.C)$
☐ (d) $C+A.B$

SECTION B [10 MARKS]

ANSWER ALL QUESTIONS FROM THIS SECTION IN YOUR ANSWER BOOKS

11. Draw the Logic circuit that would be used to implement the following Boolean equations

- a) $P = (AC + BC)(A + C)$
 b) $R = BC + D + AD$
 c) $S = B(A + C) + AC + D$

12.

INPUT			OUTPUT
C	B	A	Y
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	1
1	1	1	0

Let us assume that we are designing a simple electronic lock. The lock will open only when certain switches are activated. A 1 at the output will open the lock. There are three

valuations for input switches A, B and C that generate a 1 at the output: (A=1, B=1, C=0), (A=1, B=0, C=1), (A=0, B=1, C=1),

- Complete the truth table
- Write the Boolean expression
- Draw the Logic circuit

A	B	C	output
0	0	0	
1	1	0	
1	0	1	
0	1	1	

F. K AMPONG

Appendix A

Single-Variable Theorems

1a. $x \cdot 0 = 0$

1b. $x + 1 = 1$

2a. $x \cdot 1 = x$

- d) In digital systems, the convenient feature of using 10V/0V voltage levels is that the circuitry used to generate, manipulate and store them is very simple
- e) None of the above

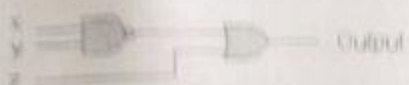
4. What type of logic gate is this?

- a) 3 inputs OR
- b) 3 inputs AND
- c) 3 inputs NOR
- d) 3 inputs NAND
- e) none of the above

A	B	C	Y
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	0
1	0	0	0
1	0	1	0
1	1	0	0
1	1	1	0

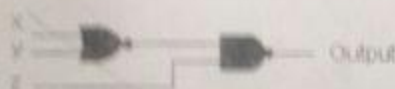
5. What function is implemented by the circuit shown

- a) $\overline{x} \cdot \overline{y} + z$
- b) $\overline{x}(\overline{x} + y)z$
- c) $\overline{x} \cdot \overline{y} \cdot z$
- d) $\overline{x} + \overline{y} + z$



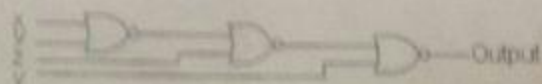
6. What function is implemented by the circuit shown

- a) $\overline{x} + \overline{y} + z$
- b) $\overline{x} + \overline{y} + \overline{z}$
- c) $\overline{x} \cdot \overline{y} \cdot z$
- d) $\overline{x} + \overline{y} + \overline{z}$



What function is implemented by the circuit shown

- a) $\overline{x}z + y$
- b) $\overline{x}z + \overline{y}$
- c) $\overline{x} \cdot \overline{y} + y \cdot z$
- d) $\overline{x} \cdot \overline{y} + \overline{y} \cdot z$



Use Boolean algebra to simplify the logic function $A + (\overline{A} \cdot B)$

- (a) $A \cdot B + \overline{A}$

Index number.....

6225111

Electronics II

March 2014

TIME : 1 Hr

Answer all questions

Answer section A on the question paper and section B in your answer booklet.
(Find attached: Appendix A - list of Boolean algebra theorems)

Section A [10 Marks]

1. Digital circuits play a very important role in today's electronic systems. They are employed in almost every facet of electronics, excluding,
 - a) Control Instrumentation
 - b) Communication
 - c) Computing
 - d) Amplification of weak signals
 - ☒ e) None of the above
2. A logic gate is:
 - a) A special type of amplifier circuit designed to amplify voltage signals corresponding to binary 1's and 0's.
 - b) A special type of amplifier circuit designed to amplify current signals corresponding to binary 1's and 0's.
 - c) A special type of voltage amplifier circuit designed to generate voltage signals corresponding to binary 1's and 0's.
 - ☒ d) A special type of amplifier circuit designed to accept and generate voltage signals corresponding to binary 1's and 0's.
 - e) None of the above
3. Which of the following statements about a logic gate is false?
 - a) Gate circuits are most commonly represented in a schematic by their own unique symbols rather than by their constituent transistors and resistors.
 - b) Logic circuits provide solution to a problem. They implement functions that are needed to carry out specific tasks
 - ☒ c) Within the framework of a computer, logic circuits do not provide complete capability for execution of programs and processing of data.

- ☒ (a) $A + B$
 (c) $\bar{A} + A$
 (d) $\bar{A}A$

9. Use Boolean algebra to simplify the logic function $A(B + \bar{B}C)$.

- (a) $A(B + C)$
 (b) $A + \bar{B}$
 (c) $A.C + B$
☒ (d) $A + B.C$

10. Simplify the logic function $F = (B+A)(B+C)$.

- (a) $B+A.C$
 (b) $B.A+B$
☒ (c) $(A+B)(B.C)$
 (d) $C+A.B$

SECTION B [10 MARKS]

ANSWER ALL QUESTIONS FROM THIS SECTION IN YOUR ANSWER BOOKS

11. Draw the Logic circuit that would be used to implement the following Boolean equations

- a) $P = (AC + BC)(A + C)$
 b) $R = BC + D + AD$
 c) $S = B(A + C) + AC + D$

12.

INPUT			OUTPUT
C	B	A	Y
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	1
1	1	1	0

Let us assume that we are designing a simple electronic lock. The lock will open only when certain switches are activated. A 1 at the output will open the lock. There are three

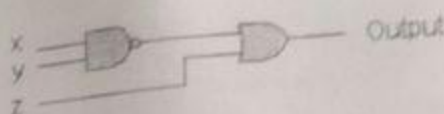
- d) In digital systems, that the circuitry used to generate
e) None of the above

4. What type of logic gate is this?
☒ a) 3 Inputs OR
 b) 3 Inputs AND
 c) 3 Inputs NOR
 d) 3 Inputs NAND
 e) none of the above

A	B	C	X
0	0	0	1
0	0	1	1
0	1	0	1
0	1	1	1
1	0	0	1
1	0	1	1
1	1	0	1
1	1	1	0

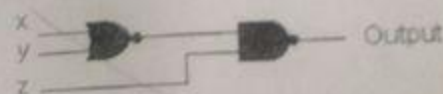
5. What function is implemented by the circuit shown

- ☒ a) $\overline{x}y + z$
 b) $\overline{(x+y)}z$
 c) $\overline{x}y\overline{z}$
 d) $\overline{x+y+z}$



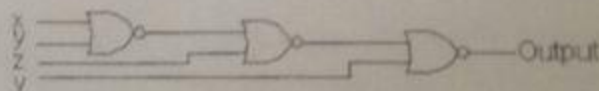
6. What function is implemented by the circuit shown

- a) $x + y + z$
☒ b) $\overline{x + y + z}$
 c) $\overline{x}y\overline{z}$
 d) $\overline{x + y + z}$



7. What function is implemented by the circuit shown

- ☒ a) $\overline{xz} + y$
 b) $\overline{xz} + \overline{y}$
 c) $\overline{x}y + y\overline{z}$
 d) $\overline{x}y + y\overline{z}$



8. Use Boolean algebra to simplify the logic function $A + (\overline{A} \cdot B)$
 (a) $A \cdot B + \overline{A}$