AC1042B Datasheet

Zhuhai Jieli Technology Co.,LTD

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AC1042B Features

CPU Core

- 32-bit CPU,the highest frequency is 160MHz
- Maximum 16KB 4Way ICache, configurable part Way as a common memory for the CPU use or other Peripheral

Memory

- On-chip 32KB SRAM(not including ICache)
- ICache SRAM: 4KB~12KB configurable

Clock Source

- RC Clock frequency about 16MHz
- LRC(low power RC) clock frequency about 32KHz
- HTC(low drift internal high frequency RC)clock frequency is 5MHz

Digital I/O

- 8 programmable digital I/O pins
- USB DP/DM can be configured to normal I/O pins
- General the IO supports pull-up(10k),pull-down(60k), strong,weak output,input and high impedance
- Up to 8 external interrupt/wake-up source(low power available,can be multiplexed to any I/O, with hardware filter)
- Input channel and Output channel, provide arbitrary IO input and output options for some modules

Digital peripherals

- One Full Speed USB 1.1 PHY
- Two UART Controllers(UART0/1) UART1 supports DMA and Flow Control
- Two SPI Controllers with DMA(SPI0/1) support master mode and slave mode,SPI0 support 4bit,SPI1support 2bit
- One Spi Flash Controller to run code
- One SD host controller
- Two 16-bit Asynchronous Divider Timers
- One IIC Controller
- Four channel PWM output
- 0.5 watt Class-D audio amplifier output
- Infrared remote control decoder
- Watchdog
- 64-bit EFUSE

Analog Peripherals

- MIC amplifier circuit
- One analog audio input channels
- 10-bit high precision ADC
- 16-bit high precision ADC (mainly as recording)
- 16-bit high precision DAC
- Low voltage protection
- Power on reset

Operating Conditions

- Working voltage
  VBAT: 2.0v - 5.5v
  HPVDD: 2.0v - 5.5v
  VDDIO: 2.0v - 3.4v
- Operating Temperature: -40°C to +85°C

Package

- SOP16

Application

- Sound Toy
- Audio player

1、Pin Definition

1.1 Pin Assignment

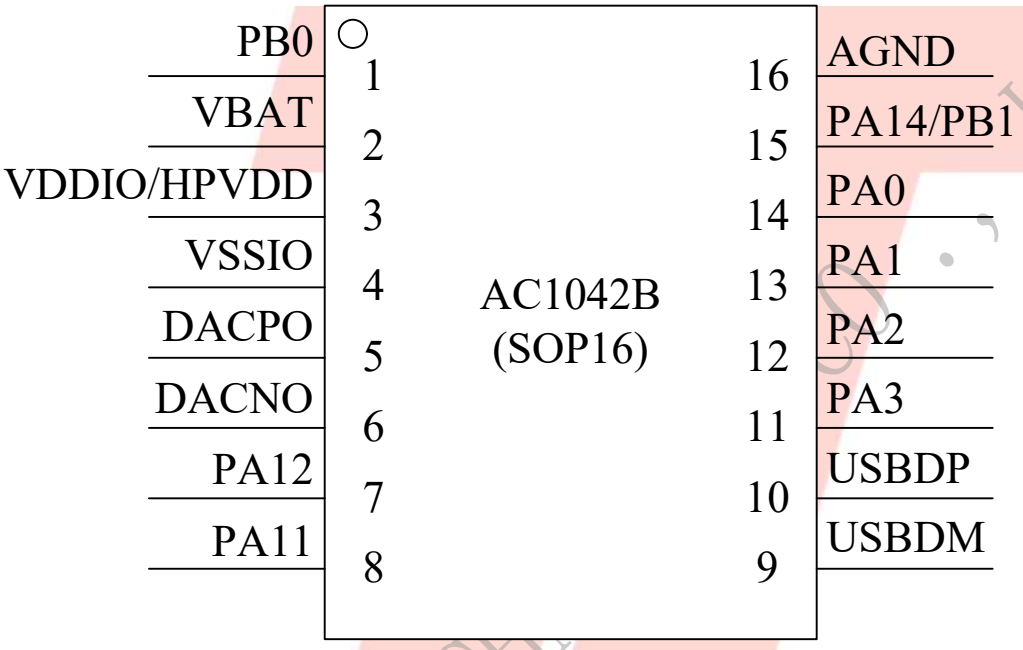


Figure 1-1 AC1042B_SOP16 Package Diagram

1.2 Pin Description

Table 1-1 AC1042B_SOP16 Pin Description

PIN NO.	Name	Type	Drive (mA)	Function	Description
1	PB0	I/O	8/64	GPIO	**DAC:Analog Audio Output;** ADC13:ADC Input Channel 13; LVD:Low Voltage Detect;
2	VBAT	P	/		Battery Power Supply;
3	VDDIO	P	/		GPIO Power;
	HPVDD	P	/		Class-D APA Power Supply;
4	VSSIO	G	/		Digital Ground;
5	DACPO	O	/		Class-D APA Positive Output;
6	DACNO	O	/		Class-D APA Negative Output;
7	PA12	I/O	8/64	GPIO	SPI1DOB:SPI1 Data Out(B); MCAP3:Motor Timer3 Capture;
8	PA11	I/O	8/64	GPIO	ADC9:ADC Input Channel 9; SPI1CLKB:SPI1 Clock(B); MCAP2:Motor Timer2 Capture;
9	USBDM	I/O	10	USB Negative Data (pull down)	ADC5:ADC Input Channel 5; SPI1DOA:SPI1 Data Out(A); SD0DATC:SD0 Data(C); UART1TXA:Uart1 Data Out(A); I2C_SDA(A);
10	USBDP	I/O	10	USB Positive Data (pull down)	ADC4:ADC Input Channel 4; SPI1CLKA:SPI1 Clock(A); UART1RXA:Uart1 Data In(A); I2C_SCL(A);
11	PA3	I/O	8/64	GPIO	ADC3:ADC Input Channel 3; SPI0DIB(1):SPI0 Data1 In(B); SPI1DIA:SPI1 Data In(A); SD0DATA:SD0 Data(A); PWM2L; MCAP0:Motor Timer2 Capture;
12	PA2	I/O	8/64	GPIO	ADC2:ADC Input Channel 2; SPI0DOB(0):SPI0 Data0 Out(B); SD0CMDA:SD0 Command(A); I2C_SDA(B); PWM2H;

13	PA1	I/O	8/64	GPIO	ADC1:ADC Input Channel 1; SPI0CLKB:SPI0 Clock(B); SD0CLKA:SD0 Clock(A); UART0RXB:Uart0 Data In(B); I2C_SCL(B); CAP2:Timer2 Capture;
14	PA0	I/O	8/64	GPIO (pull up)	Long Press Reset; ADC0:ADC Input Channel 0; UART0TXB:Uart0 Data Out(B);
15	PA14	I/O	8/64	GPIO	ADC11:ADC Input Channel 11; AUX1:Analog Channel 1 Input;
	PB1	I/O	8/64	GPIO	MIC_IN：MIC Input Channel;
16	AGND	G	/		Analog Ground;

2、Electrical Characteristics

2.1 Absolute Maximum Ratings

Table 2-1

Symbol	Parameter	Min	Max	Unit
Tamb	Ambient Temperature	-40	+85	℃
Tstg	Storage temperature	-65	+150	℃
VBAT	Supply Voltage	-0.3	5.5	V
$V_{VDDIO33}$	3.3V IO Input Voltage	-0.3	3.6	V

Note : The chip can be damaged by any stress in excess of the absolute maximum ratings listed below

2.2 PMU Characteristics

Table 2-2

Symbol	Parameter	Min	Typ	Max	Unit	Test Conditions
VBAT	Voltage Input	2.0	3.7	5.5	V	_
V_{HPVDD}	Voltage Input	_	3.0	_	V	_
V_{VDDIO}	Voltage output	2.0	3.0	3.4	V	VBAT = 3.7V, 100mA loading
I_{VDDIO}	Loading current	_	_	100	mA	VBAT=3.7V

2.4 IO Input/Output Electrical Logical Characteristics

Table 2-3

IO input characteristics						
Symbol	**Parameter**	**Min**	**Typ**	**Max**	**Unit**	**Test Conditions**
V_{IL}	Low-Level Input Voltage	-0.3	_	0.3* VDDIO	V	VDDIO = 3.3V
V_{IH}	High-Level Input Voltage	0.7* VDDIO	_	VDDIO+0.3	V	VDDIO = 3.3V
IO output characteristics						
V_{OL}	Low-Level Output Voltage	_	_	0.33	V	VDDIO = 3.3V
V_{OH}	High-Level Output Voltage	2.7	_	_	V	VDDIO = 3.3V

2.5 Internal Resistor Characteristics

Table 2-4

Port	General Output	High Drive	Internal Pull-Up Resistor	Internal Pull-Down Resistor	Comment
PA0~PA14 PB0、PB1	8mA	64mA	10K	60K	1、PA0 default pull up 2、USBDM & USBDP default pull down 3、internal pull-up/pull-down resistance accuracy ±20%
USBDP	10mA	_	1.5K	15K	
USBDM	10mA	_	180K	15K	

2.6 Analog DAC(PB0) Characteristics

Table 2-5

Parameter	Min	Typ	Max	Unit	Test Conditions
Frequency Response	20	_	16K	Hz	1KHz/0dB 100kohm loading With A-Weighted Filter
THD+N	_	-65	_	dB	
S/N	_	95	_	dB	
Output Swing	_	0.54	_	Vrms	
Dynamic Range	_	92	_	dB	1KHz/-60dB 100kohm loading With A-Weighted Filter
Output Resistance	_	8.3	_	K	_

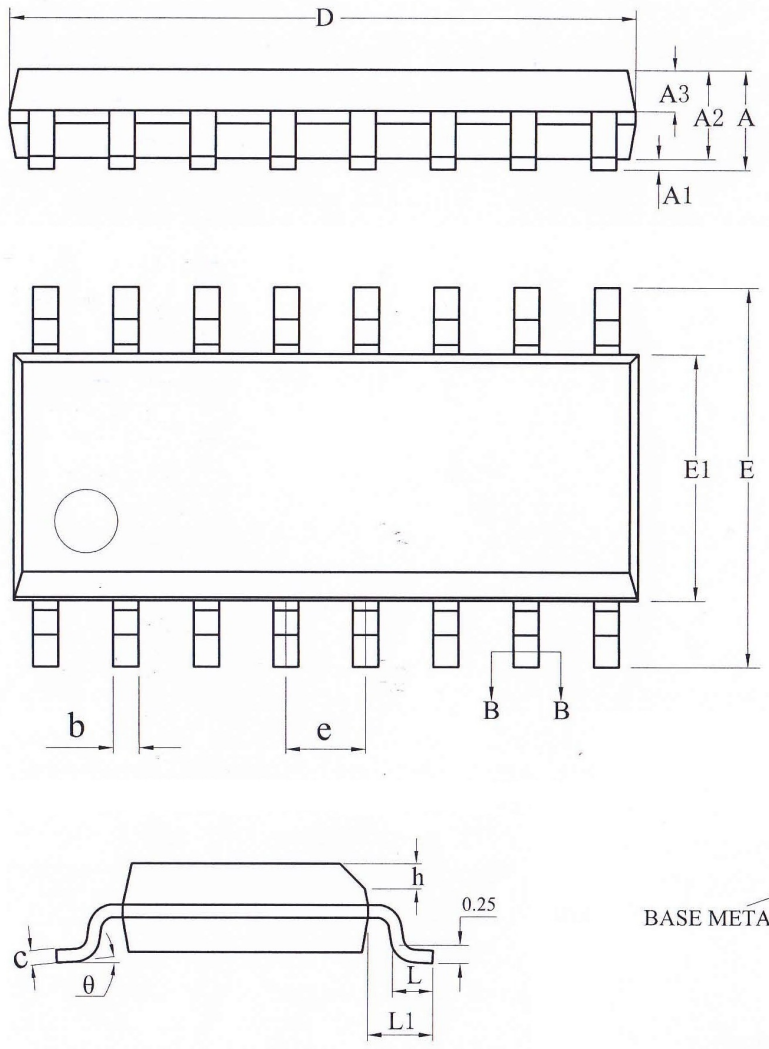
2.7 ADC Characteristics

Table 2-6

Parameter	Min	Typ	Max	Unit	Test Conditions
Dynamic Range	_	75	_	dB	1KHz/210mVrms line mode :6dB with cap PGAIS=2
S/N	_	79	_	dB	
THD+N	_	-70	_	dB	

3、 Package Information

3.1 SOP16



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	—	—	1.75
A1	0.10	—	0.225
A2	1.30	1.40	1.50
A3	0.60	0.65	0.70
b	0.39	—	0.47
b1	0.38	0.41	0.44
c	0.20	—	0.24
c1	0.19	0.20	0.21
D	9.80	9.90	10.00
E	5.80	6.00	6.20
E1	3.80	3.90	4.00
e	1.27BSC		
h	0.25	—	0.50
L	0.50	—	0.80
L1	1.05REF		
θ	0	—	8°

Figure 3-1. AC1042B_SOP16 Package

4、 Revision History

Date	Revision	Description
2021.04.21	V1.0	Initial Release