AD142A0 Datasheet

Zhuhai Jieli Technology Co.,LTD

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AD142A0 Features

CPU Core

- 32-bit CPU,the highest frequency is 160MHz
- Maximum 16KB 4Way ICache, configurable part Way as a common memory for the CPU use or other Peripheral

Memory

- On-chip 32KB SRAM(not including ICache)
- ICache SRAM: 4KB~12KB configurable

Clock Source

- RC Clock frequency about 16MHz
- LRC(low power RC) clock frequency about 32KHz
- HTC(low drift internal high frequency RC)clock frequency is 5MHz

Digital I/O

- 8 programmable digital I/O pins
- USB DP/DM can be configured to normal I/O pins
- General the IO supports
 pull-up(10k),pull-down(60k),
 strong,weak output,input and high
 impedance
- Up to 8 external interrupt/wake-up source(low power available,can be multiplexed to any I/O, with hardware filter)
- Input channel and Output channel, provide arbitrary IO input and output options for some modules

Digital peripherals

One Full Speed USB 1.1 PHY

- Two UART Controllers(UART0/1) UART1 supports DMA and Flow Control
- Two SPI Controllers with DMA(SPI0/1) support master mode and slave mode.
- One Spi Flash Controller to run code
- One SD host controller
- Two 16-bit Asynchronous Divider Timers
- One IIC Controller
- Four channel PWM output
- 0.5 watt Class-D audio amplifier output
- Infrared remote control decoder
- Watchdog
- 64-bit EFUSE

Analog Peripherals

- MIC amplifier circuit
- Two analog audio input channels
- 10-bit high precision ADC
- 16-bit high precision ADC (mainly as recording)
- 16-bit high precision DAC
- Low voltage protection
- Power on reset

Operating Conditions

Working voltage VBAT: 2.0v - 5.5v

VDDIO: 2.0v - 3.4v

Operating Temperature: -40°C to +85°C

Package

SOP16

Application

- Sound Toy
- Audio player

1. Pin Definition

1.1 Pin Assignment

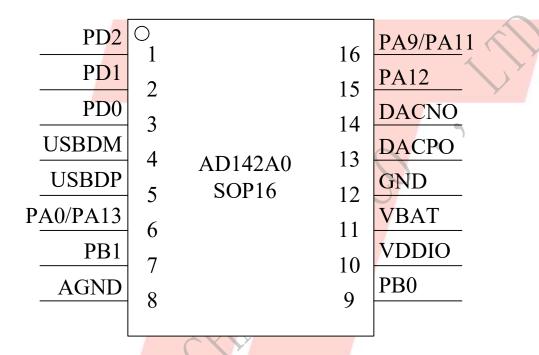


Figure 1-1 AD142A0_SOP16 Package Diagram

1.2 Pin Description

Table 1-1 AD142A0_SOP16 Pin Description

PIN NO.	Name	Туре	Drive (mA)	Function	Description
1	PD2	I/O	8/64	GPIO (11)	SPIOCSA:SPIO Chip Select(A);
2	PD1	I/O	8/64	(pull up) GPIO	SFCCSA:SFC Chip Select(A); SPI0DOA(0):SPI0 Data0 Out(A);
	ועז	1/0	0/04	Grio	SFCDOA(0):SFC Data0 Out(A);
3	PD0	I/O	8/64	GPIO	SPI0CLKA:SPI0 Clock(A);
	TDU	1/0	0/01	GITO	SFCCLKA:SFC Clock(A);
					ADC5:ADC Input Channel 5;
4	USBDM	I/O	10	USB Negative Data	SPI1DOA:SPI1 Data Out(A);
				(pull down)	UARTITXA:Uart1 Data Out(A);
					I2C_SDA(A);
					ADC4:ADC Input Channel 4;
5	USBDP	SBDP I/O	10	USB Positive Data	SPI1CLKA:SPI1 Clock(A);
				(pull down)	UART1RXA:Uart1 Data In(A);
					I2C_SCL(A);
	DAO	I/O	8/64	GPIO	Long Press Reset;
	PA0	1/0	8/04	(pull up)	ADC0:ADC Input Channel 0; UART0TXB:Uart0 Data Out(B);
6	A				ADC10:ADC Input Channel 10;
0				Y ///	AUX0: Analog Channel 0 Input;
	PA13	13 I/O	8/64	GPIO	MIC BIAS: Microphone Bias Output;
		-6			CAP0:Timer0 Capture
7	PB1	I/O	8/64	GPIO	MIC_IN: MIC Input Channel;
8	AGND	G	/		Analog Ground;
		2			DAC:Analog Audio Output;
9	PB0	I/O	8/64	GPIO	ADC13:ADC Input Channel 13;
1					LVD:Low Voltage Detect;
10	VDDIO	P	/		GPIO Power;
11	VBAT	P	/		Battery Power Supply;
12	GND	G	/		Digital Ground;
13	DACPO	О	/		Class-D APA Positive Output;
14	DACNO	О	/		Class-D APA Negative Output;
1.5	D4 12	1/0	0/54	CDIO	SPI1DOB:SPI1 Data Out(B);
15	PA12	I/O	8/64	GPIO	MCAP3:Motor Timer3 Capture;

					ADC9:ADC Input Channel 9;
	PA11	I/O	8/64	GPIO	SPI1CLKB:SPI1 Clock(B);
					MCAP2:Motor Timer2 Capture;
16					UART1TXB:Uart1 Data Out(B);
10				GPIO	UART1RXB:Uart1 Data In(B);
	PA9	I/O	8	(High Voltage	I2C_SDA(D);
				Resistance)	CAP1:Timer1 Capture;
					PWM3:PWM Channel3 Output;



2, Electrical Characteristics

2.1 Absolute Maximum Ratings

Table 2-1

Symbol	Parameter	Min	Max	Unit
Tamb	Ambient Temperature	-40	+85	°C
Tstg	Storage temperature	-65	+150	°C
VBAT	Supply Voltage	-0.3	5.5	V
V _{VDDIO33}	3.3V IO Input Voltage	-0.3	3.6	V

Note: The chip can be damaged by any stress in excess of the absolute maximum ratings listed below

2.2 PMU Characteristics

Table 2-2

Symbol	Parameter	Min	Тур	Max	Unit	Test Conditions
VBAT	Voltage Input	2.0	3.7	5.5	V	_
V_{VDDIO}	Voltage output	2.0	3.0	3.4	V	VBAT = 3.7V, 100mA loading
I_{VDDIO}	Loading current	_/	<u></u>	100	mA	VBAT=3.7V

2.3 IO Input/Output Electrical Logical Characteristics

Table 2-3

IO input characteristics								
Symbol	Parameter	Min	Тур	Max	Unit	Test Conditions		
V_{IL}	Low-Level Input Voltage	-0.3	_	0.3* VDDIO	V	VDDIO = 3.3V		
$V_{ m IH}$	High-Level Input Voltage	0.7* VDDIO	-	VDDIO+0.3	V	VDDIO = 3.3V		
IO output c	10 output characteristics							
V_{OL}	Low-Level Output Voltage	_	_	0.33	V	VDDIO = 3.3V		
$ m V_{OH}$	High-Level Output Voltage	2.7	_	_	V	VDDIO = 3.3V		

2.4 Internal Resistor Characteristics

Table 2-4

Port	General Output	High Drive	Internal Pull-Up Resistor	Internal Pull-Down Resistor	Comment
PA0、PA11、 PA12、PA13 PB0、PB1、 PD0~PD2	8mA	64mA	10K	60K	1、PA0&PD2 default pull up 2、USBDM & USBDP
PA9 (high voltage I/O)	8mA	_	10K	60K	default pull down 3 internal pull-up/pull-down
USBDP	10mA	_	1.5 K	15K	resistance accuracy ±20%
USBDM	10mA	-	180K	15K	

2.5 Analog DAC(PB0) Characteristics

Table 2-5

Parameter	Min	Тур	Max	Unit	Test Conditions
Frequency Response	20	(-\\\	16K	Hz	1KHz/0dB
THD+N		-65	_	dB	
S/N	1	95	_	dB	100kohm loading
Output Swing	_	0.54	_	Vrms	With A-Weighted Filter
		1/-/		7	1KHz/-60dB
Dynamic Range	<u> </u>	92	_	dB	100kohm loading
		7/			With A-Weighted Filter
Output Resistance	- 6	8.3	_	K	_

2.6 ADC Characteristics

Table 2-6

Parameter	Min	Тур	Max	Unit	Test Conditions
Dynamic Range	_	75	_	dB	1KHz/210mVrms
S/N	_	79	_	dB	line mode :6dB with cap
THD+N	_	-70	_	dB	PGAIS=2

3. Package Information

3.1 SOP16

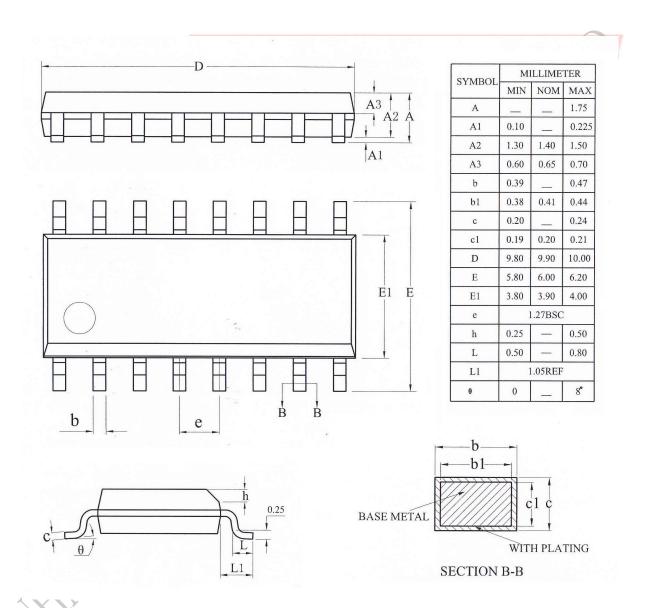


Figure 3-1. AD142A0_SOP16 Package

4. Revision History

Date	Revision	Description
2021.03.09	V1.0	Initial Release

