TRISC Control Signals

Control Signal Definitions (Active High)

C0 – *Clear* **Program Counter (PC)**

C1 – *Load* **PC**

C2 – *Increment* **PC**

C3 – *Select* **Memory Address Register (MAR) source (C3=0: PC, C3=1: MDO)**

C4 – *Execute* **RAM READ/WRITE cycle**

C5 – *Enable* **RAM WRITE cycle**

C7 – *Load* **Instruction Regiser (IR)**

C8 – *Clear* **Accumulator (ACC)**

C9 – *Increment* **ACC**

C10 – *Select* **ACC source (C10=0: ALU, C10=1:MDO)**

C11 – *Load* **ACC**

C12,C13 – **Arithmetic Logic Unit (ALU) Function Code (00:ADD, 10:SUB, 01:AND, 11:XOR)**

C14 – *Load* **Buffer Register (BR)**

C15 – *Load* **Flag Register (FR)**