**ICS 632**

**Assignment #1**

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Note:

All tests were run on 20 cores and 1 node. All C code is compiled with -Ofast flag.

To run under basic naïve algorithm, run command:

make basic

make run

To run under tiled version, run command:

make tiled

make run

To collect tiled version data for each tiled size, run command:

python compile\_N\_run.py (generate raw.dat)

python beautify.py (generate beauty.dat)

python final\_step.py (generate perf.dat, l1\_miss.dat, llc\_miss.dat)

All generated data are stored in a folder called “data”.

To run compile\_N\_run.py in compute node, you have to load icc compiler first.

Question 1:

Below is my exercise.c file. I used C preprocessor and the -D compiler flag to define values for N and b at compile-time, as well as to deter- mine at compile time whether the code runs the basic algorithm or the tiled algorithm. I also made a makefile for a easier compiling time. See Appendix A for my makefile.



Figure 1: C source file for this assignment

I tried multiple times with basic algorithm, and found if I define N to be 17600, it runs about 2 seconds under 20-core 1-node environment.

Question 2:

Method:

I used python to collect the performance data. See Appendix B, C, D for my python code of automation data collecting. First, I used Compile\_N\_Run.py to compile and run the C code for 10 trials for each tile size between 1 and 300. This python code will redirect the performance data to a file called raw.dat. Then I used beautify.py to make the data more readable. These data is stored in beauty.dat. At last step, I used final\_step.py to calculate the average elapsed time for each tile size.

Result:

Here is the plot of average elapsed time vs. tile size:

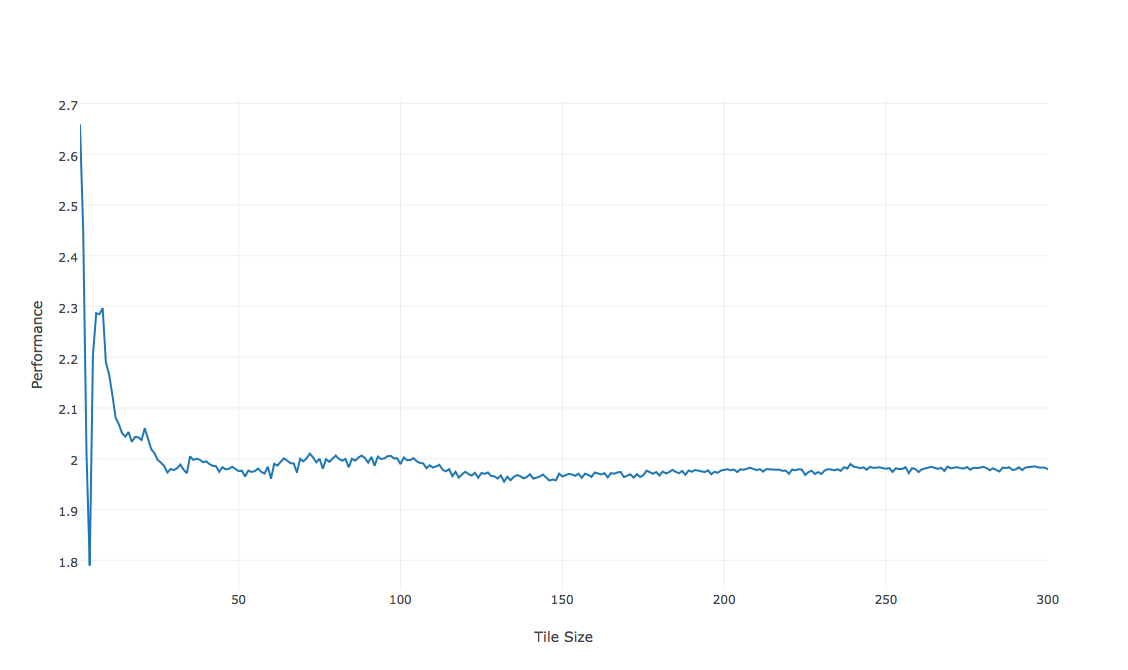


Figure 2: Elapsed Time VS. Tile Size

As clearly stated in the graph and data, for this program, tile size 4 achieves best elapsed time. With tile size 1, the elapsed time is the max. This is because with tile size 1, it is basically same with the basic algorithm. Moreover, the program has to compute extra variables, which increases the execution time. As tile size increasing, the elapsed time decreases generally. Before the tile size decreasing to 35, the elapsed time decreases greatly. With tile size between 35 and 100, it keeps around same elapsed time. With between 100 and 120, it decreases lightly. After the tile size bigger than 120, the elapsed time remains in a stable range.

With tile size of 4, the elapsed time reaches its minimum of 1.79 seconds. The speedup with regard to the basic version is 2/1.79 = 1.12.

Question 3:

Below is the graph of average L1 cache misses and LLC cache misses vs. tile size.

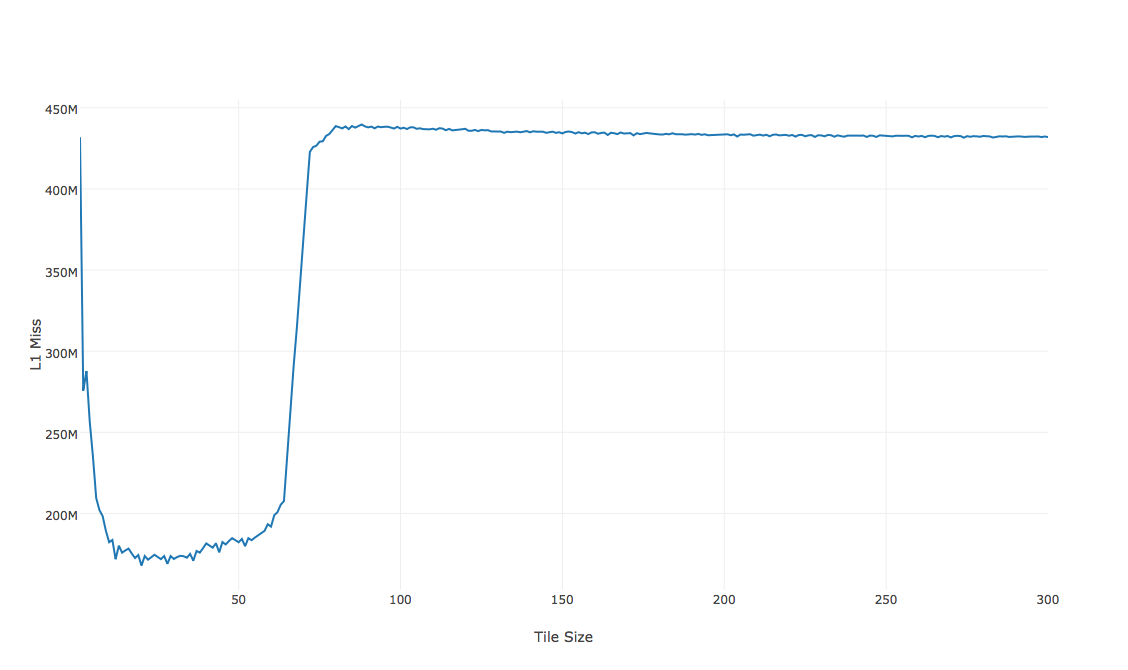


Figure 3: L1 Miss VS. Tile Size

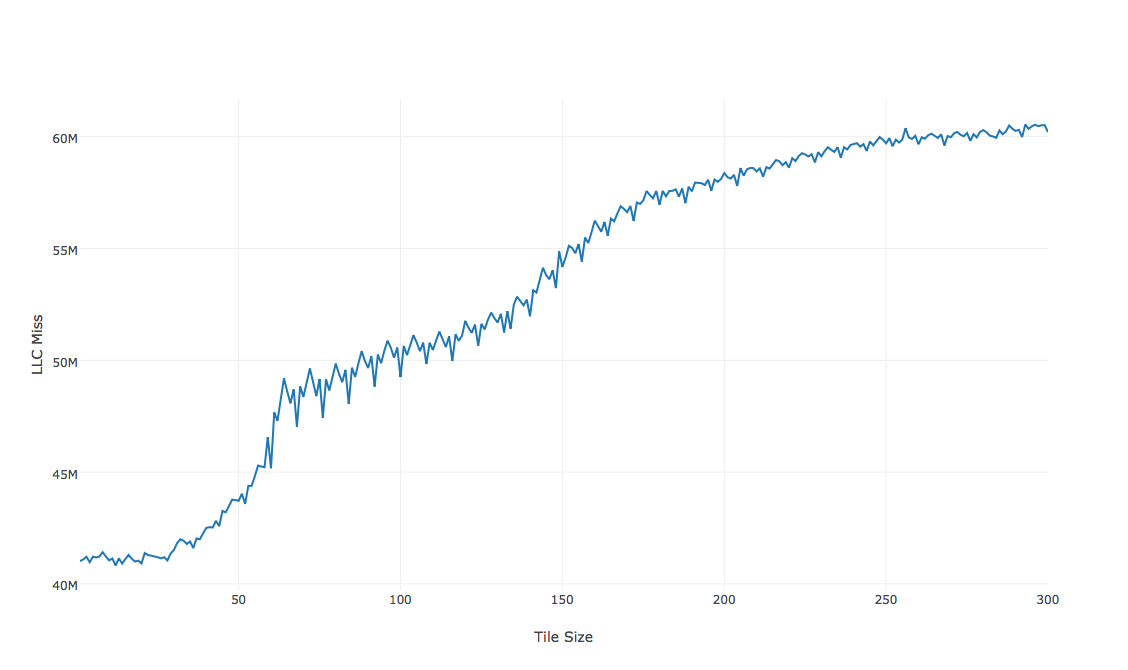


Figure 4: LLC Miss VS. Tile Size

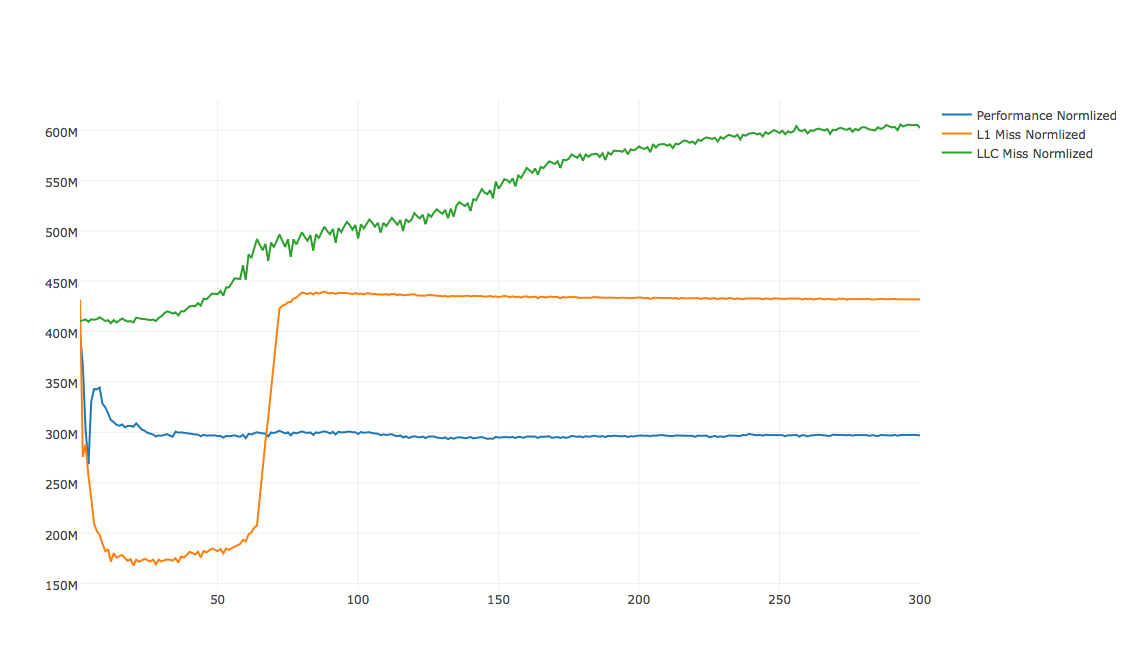


Figure 5: Elapsed Time, L1 Miss, LLC Miss VS. Tile size

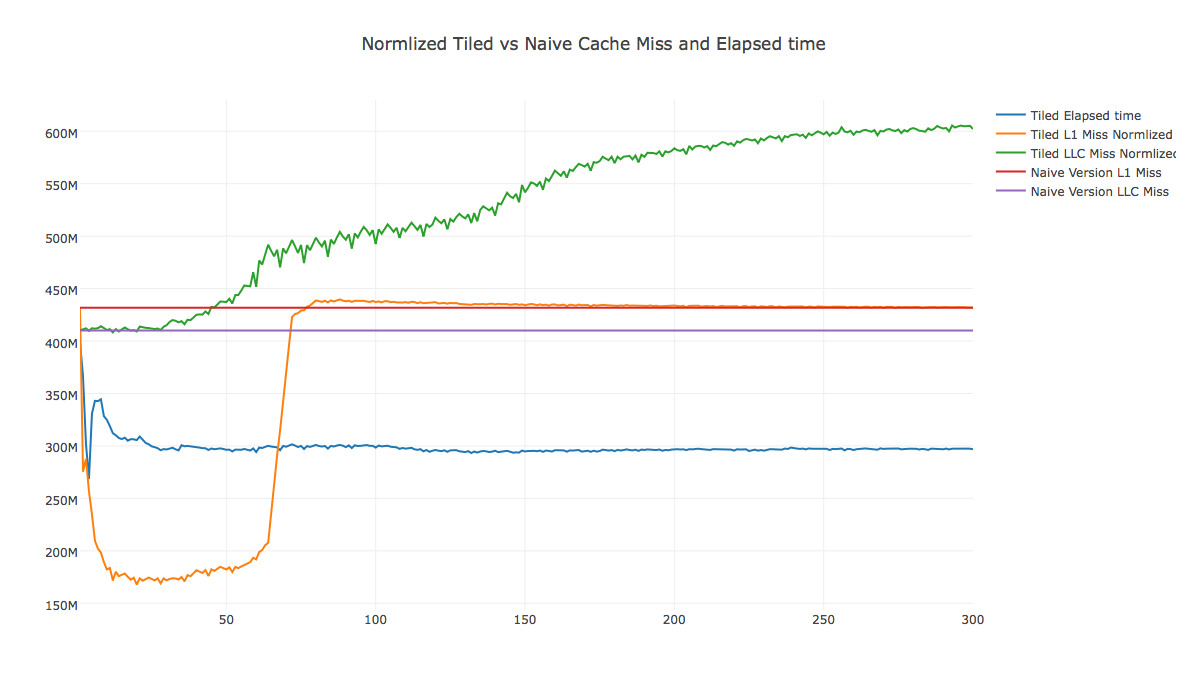


Figure 6: Tiled version VS. Naïve version

For the basic version the L1 cache miss is 431 million, LLC cache miss is 41 million, while with tiled tile size of 4 version, the L1 caches miss is 257 million, LLC cache miss is 41million. We can see that L1 dcache load misses is much smaller than the naïve version. While the LLC cache misses for both version starts almost same, but the tiled version’s LLC load misses increases as tile size grows.

From the graph, we can see that, generally, the elapsed time decreases as L1 load misses decreases, which makes sense. However, there is some situation where this is not true. For example, our best tile size of 4, the L1 load miss is not in its lowest, which means the lowest L1 cache miss does not guarantee the lowest elapsed time. Based on this result, it is usually difficult to pick a decent tile size for best performance.

**Appendix A**

makfile:



**Appendix B**

compile\_N\_run.py:



**Appendix C**

beautify.py:



**Appendix D**

final\_step.py:

