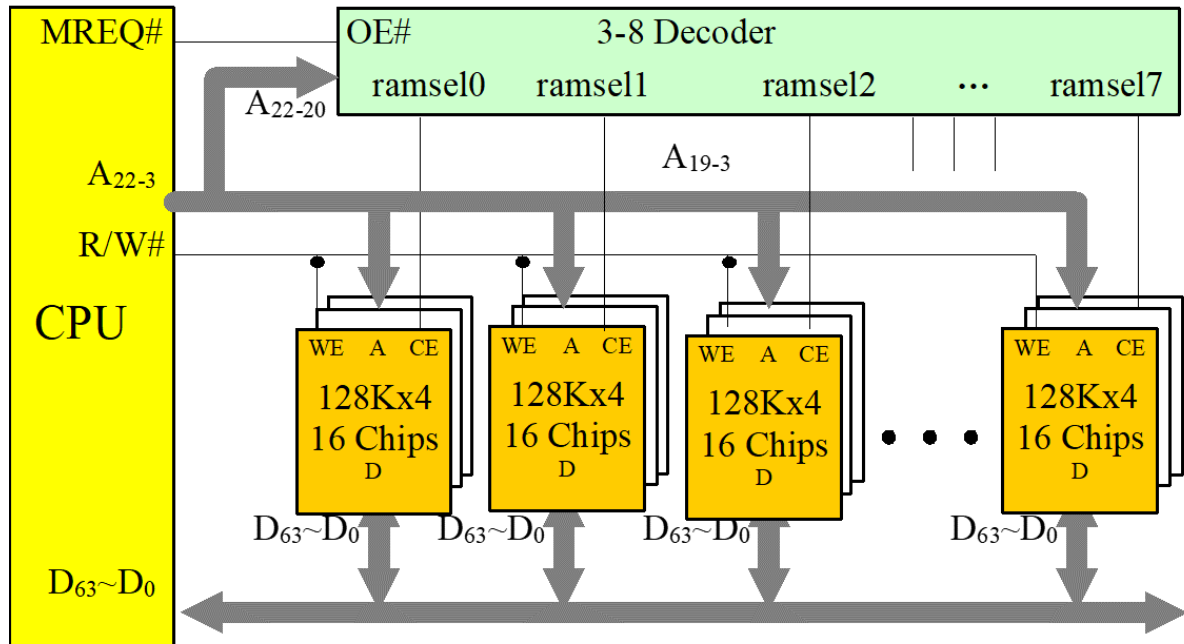


Assignment2 of EI209

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Since data bus width \div chip word width = 64bits \div 4bits = 16, we do 16 times bit extension.

Since 1M \div 128K = 8 = 2^3 , we do 8 times word extension and need 3 to 8 bits decoder.

Since data bus width \div addressable unit = 64bits \div 8bits = 8 = 2^3 , we discard the lowest 3 bits of address bus.