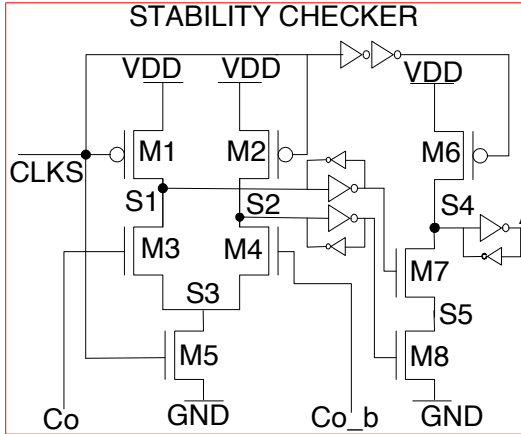
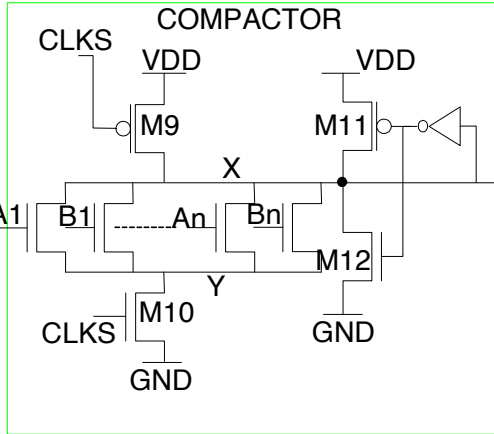


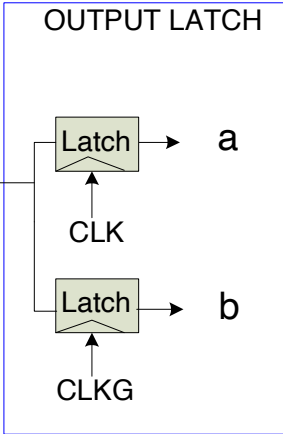
(a) Host circuit and XOR protection



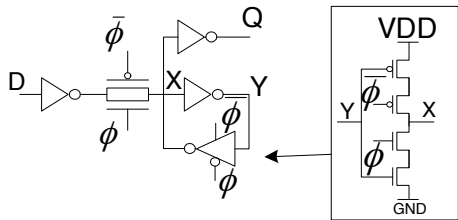
(b) Stability checker



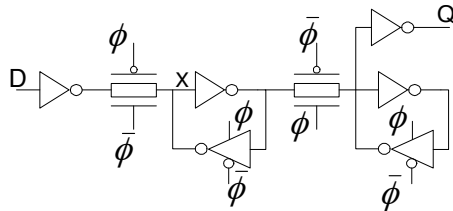
(c) Cehcker outputs Compactor



(d) Output latch



(e) Latch



(f) Flip-flop