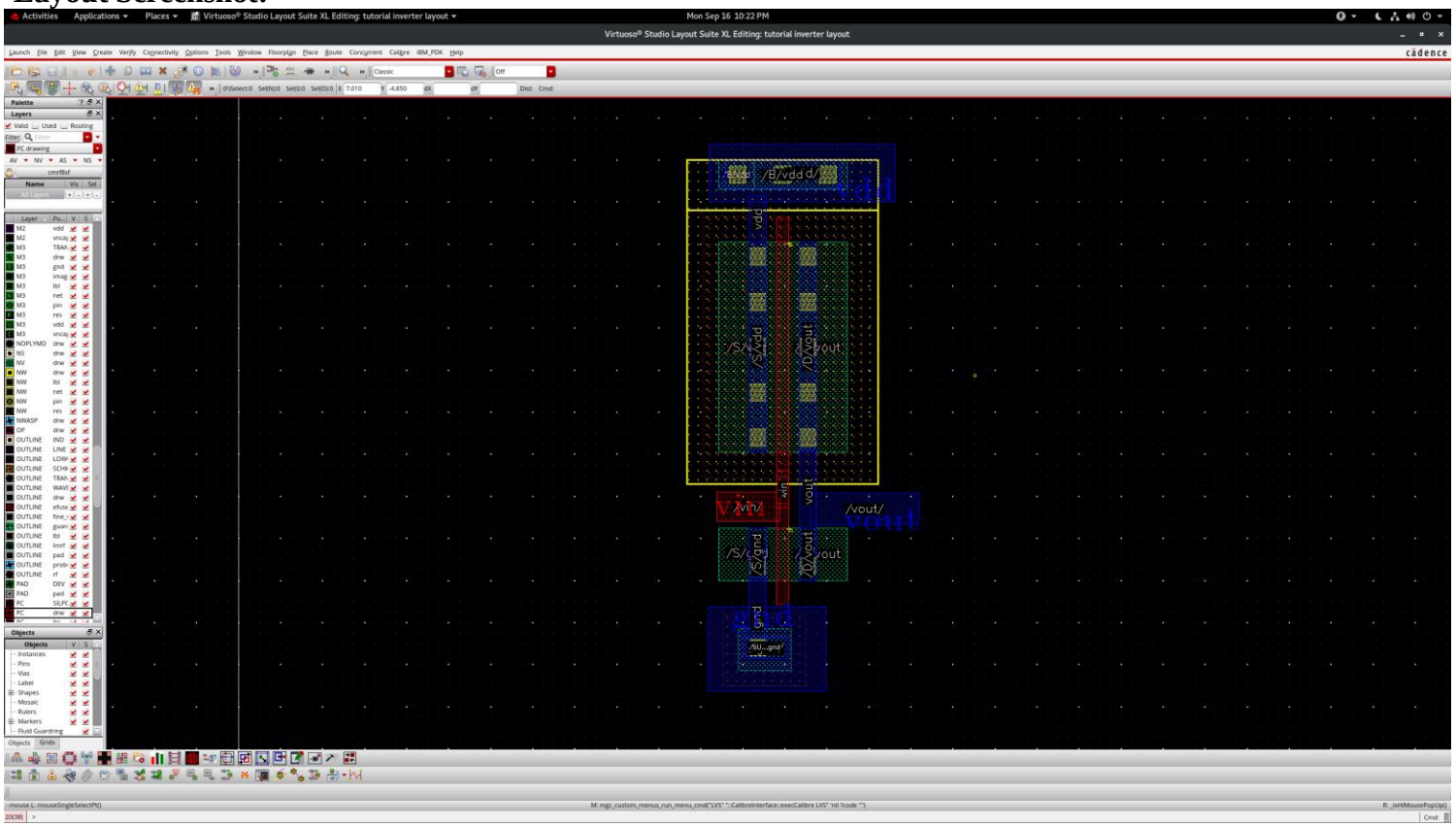
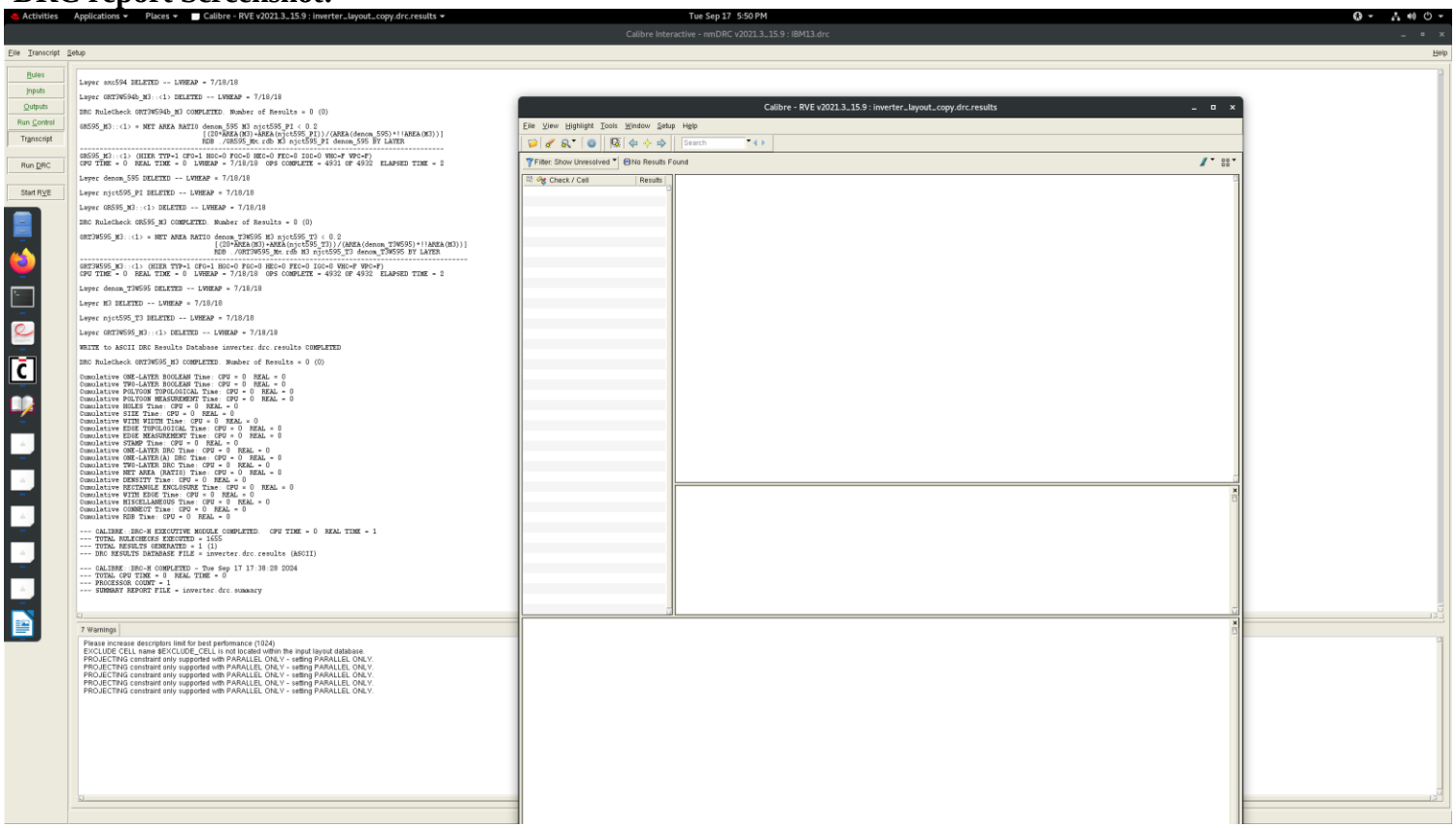
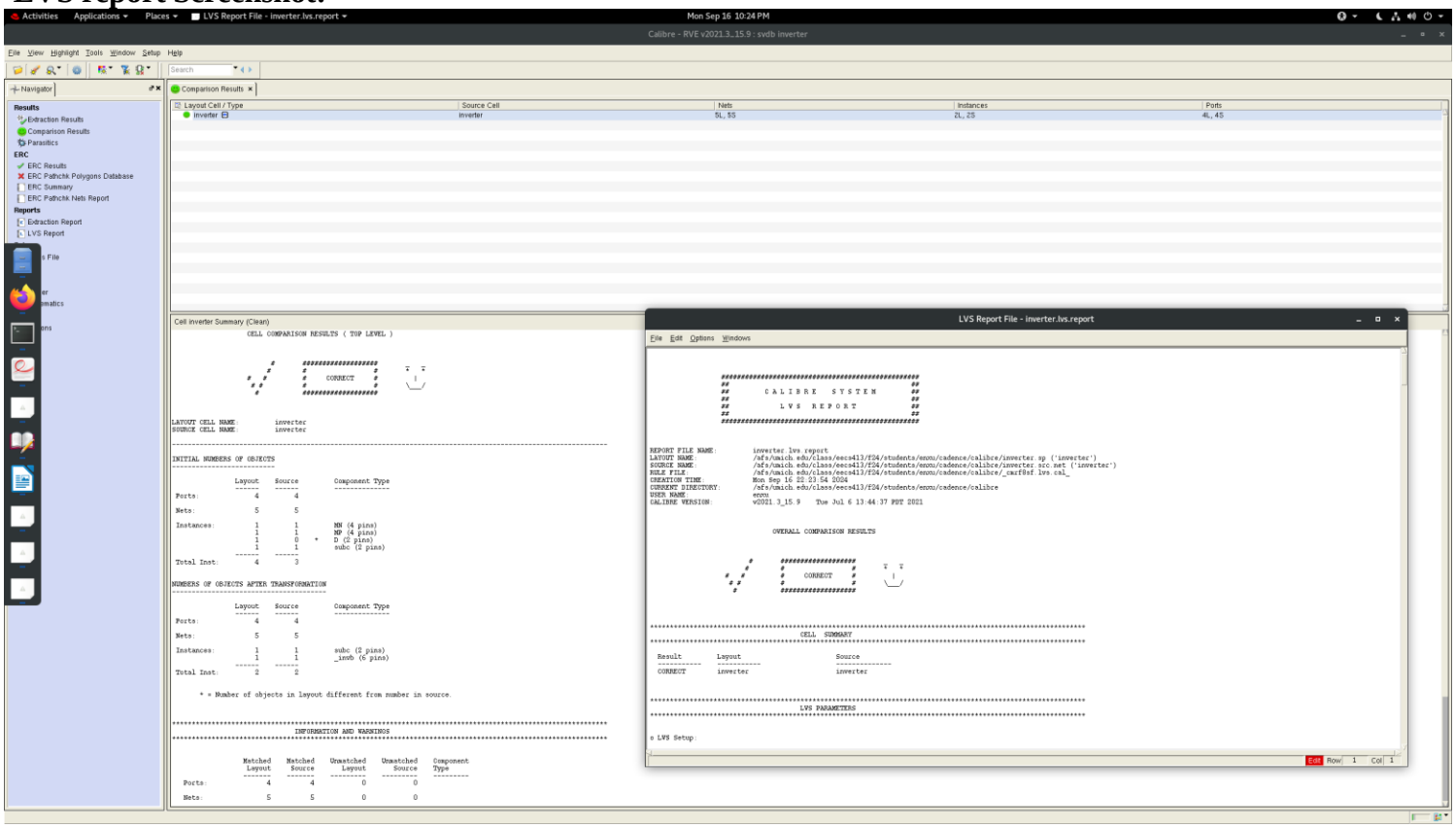
Layout Screenshot:



DRC report Screenshot:

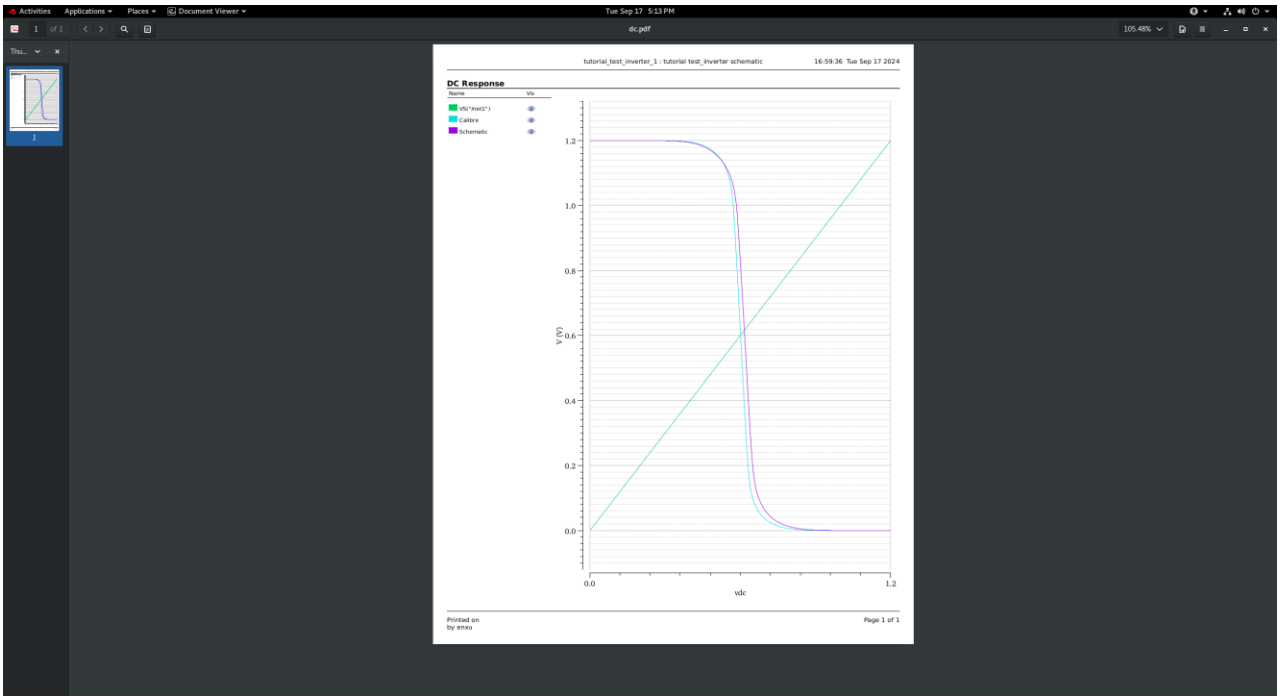


LVS report Screenshot:

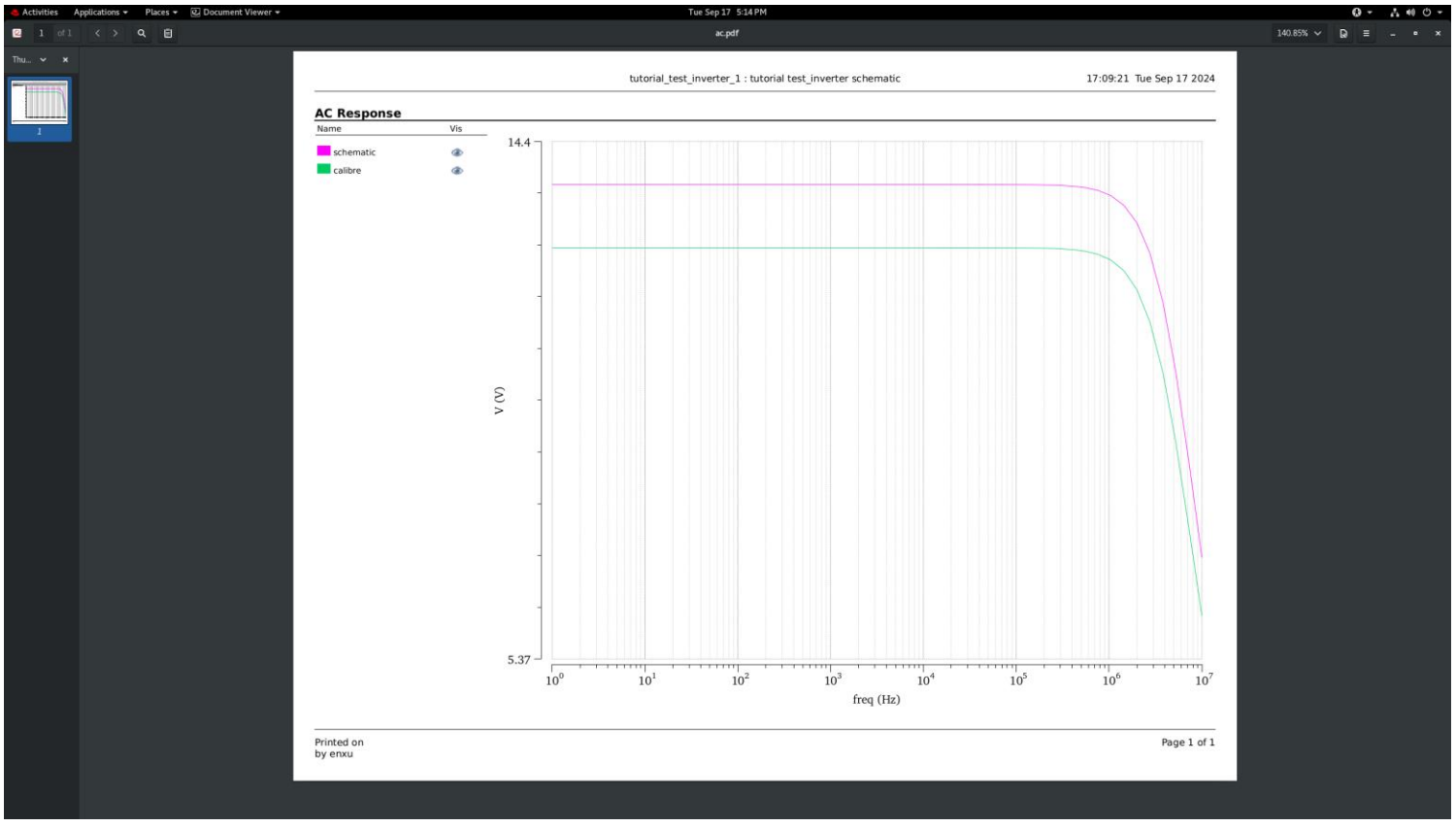


AC Gain magnitude for Schematic and PEX view:

test dc:



test ac:



The calibre generally has a lower magnitude than the schematic. This is because with the parasitic capacitors simulated, the low frequency signals are more attenuated.