

# CS302 OS Week3 Assignment - Report

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## 1) Explain the function of each parameter in `make qemu`

- `qemu-system-riscv64`: QEMU riscv64 emulator
- `-machine virt`: select emulated machine type, RISC-V VirtIO board
- `-nographic`: disable graphical output and redirect serial I/Os to console
- `-bios default`: set the filename for the BIOS, here default
- `-device loader,file=bin/ucore.bin,addr=0x80200000`: add device based on "Generic Loader", and sets driver properties, driver file path: `bin/ucore.bin`, driver program counter on start: `0x80200000`.

## 2) Explain the function of each line in `/lab3/tools/kernel.ld`

- `OUTPUT_ARCH(riscv)`: Specify a particular output machine architecture, here `riscv`.
- `ENTRY(kern_entry)`: The first instruction to execute in a program, here set to symbol `kern_entry`.
- `BASE_ADDRESS = 0x80200000`: Set the variable `BASE_ADDRESS` to `0x80200000`.
- `SECTIONS{}`: Define the regions to set section mapping and memory layout.
- `. = BASE_ADDRESS`: Set the location counter to be the value of `BASE_ADDRESS`.
- `.text : {*(.text.kern_entry .text .stub .text.* .gnu.linkonce.t.*)}`: Defines an output section `.text`, `*` is a wildcard which matches any file name, here maps all `.text.kern_entry`, `.text`, `.stub`, `.text.*`, `.gnu.linkonce.t.*` input sections in all input files to be placed into `.text` output section at **BASE\_ADDRESS**, where `.text.*` and `.gnu.linkonce.t.*` refer to all sections beginning with `.text.` and `.gnu.linkonce.t.` in input files, also the location counter will self-increase when executing this command.
- `PROVIDE(etext = .);`: If the program defines `etext`, the linker will silently use the definition in the program. If the program references `etext` but does not define it, the linker will use the definition set as the current location counter.
- `.rodata : {*(.rodata .rodata.* .gnu.linkonce.r.*)}`: The same as above, defines an output section `.rodata` and maps all `.rodata`, `.rodata.*`, `.gnu.linkonce.r.*` input sections in all input files to be placed into `.rodata` output section at address **immediately after the .text output section** in memory with a **correct alignment**.
- `. = ALIGN(0x1000);`: Insert padding bytes until current location counter becomes aligned on `0x1000`-byte boundary.
- `.data : {*(.data)*(.*.data.*)}`: The same as above, defines an output section `.data` and maps all `.data`, `.data.*` input sections in all input files to be placed into `.data` output section at address **specified by the location counter**.
- `.sdata : {*(.sdata)*(.*.sdata.*)}`: The same as above, defines an output section `.sdata` and maps all `.data`, `.sdata.*` input sections in all input files to be placed into `.sdata` output section at address **specified by the location counter**.

- `PROVIDE(edata = .);`: If the program defines `edata`, the linker will silently use the definition in the program. If the program references `edata` but does not define it, the linker will use the definition set as the current location counter.
- `bss : {*(.bss)*(.bss.*)*(.sbss*)}`: The same as above, defines an output section `.bss` and maps all `.bss`, `.bss.*` and `.sbss*` input sections in all input files to be placed into `.bss` output section at address **specified by the location counter**.
- `PROVIDE(end = .);`: If the program defines `end`, the linker will silently use the definition in the program. If the program references `end` but does not define it, the linker will use the definition set as the current location counter.
- `/DISCARD/ : {*(.eh_frame .note.GNU-stack)}`: Drop the sections `.eh_frame` and `.note.GNU-stack` in all input files and thus they will not be contained in the output files.

### 3) Explain the function of `memset(edata, 0, end - edata);` and parameters

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In `kernel.ld`, we have defined `edata` and `end` to be two memory locations. The output `bss` section is in this segment of memory. So this function is to set `end - edata` bytes (the `bss` section) to `0` starting from the memory location pointed by `edata`.

### 4) Describe how to call `ecall` instruction step by step after the kernel boot up

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- When the kernel boot up, the first instruction to execute is set to `kern_entry` in `kernel.ld`.
- Then in `entry.S`, the function `kern_init` in `init.c` is invoked in `kern_entry`.
- In function `kern_init`, `cputs` function is invoked.
- Then in `stdio.c`, `cputs` invokes `cputch` which further invokes `cons_putc` in `console.c`.
- In `console.c`, `cons_putc` invokes `sbi_console_putchar` in `sbi.c`.
- Finally, in `sbi.c`, `sbi_console_putchar` invokes `sbi_call` where `ecall` is called.

### 5) Refer to `ecall`, Implement `shutdown()` to shutdown the system.

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- Modified codes:

```

File Edit Selection View Go Run Terminal Help
EXPLORER
LAB3
  .vscode
  bin
    kernel
    ucore.bin
  kern
  driver
    console.c
    console.h
  init
    entry.S
    init.c
  libs
    sbi.c
    sbi.h
    stdarg.h
    stdio.h
    string.c
    string.h
  obj
  tools
    function.mk
    kernel.ld
    Makefile
  ...
C init.c M Makefile kernel.ld C stdio.h M C sbi.c M X C stdio.c M entry.S
libs > C sbi.c > sbi_call(uint64_t, uint64_t, uint64_t, uint64_t)
1 // libs/sbi.c
2 #include <sbi.h>
3 #include <defs.h>
4
5
6 uint64_t SBI_SET_TIMER = 0;
7 uint64_t SBI_CONSOLE_PUTCHAR = 1;
8 uint64_t SBI_CONSOLE_GETCHAR = 2;
9 uint64_t SBI_CLEAR_IPI = 3;
10 uint64_t SBI_SEND_IPI = 4;
11 uint64_t SBI_REMOTE_FENCE_I = 5;
12 uint64_t SBI_REMOTE_SFENCE_VMA = 6;
13 uint64_t SBI_REMOTE_SFENCE_VMA_ASID = 7;
14 uint64_t SBI_SHUTDOWN = 8;
15
16 uint64_t sbi_call(uint64_t sbi_type, uint64_t arg0, uint64_t arg1, uint64_t arg2) {
17     uint64_t ret_val;
18     asm__ volatile (
19         "mv x17, %[sbi_type]\n"
20         "mv x10, %[arg0]\n"
21         "mv x11, %[arg1]\n"
22         "mv x12, %[arg2]\n"
23         "ecall\n"
24         "mv %[ret_val], x10"
25         : [ret_val] "=r" (ret_val)
26         : [sbi_type] "r" (sbi_type), [arg0] "r" (arg0), [arg1] "r" (arg1), [arg2] "r" (arg2)
27         : "memory"
28     );
29     return ret_val;
30 }
31
32 void sbi_console_putchar(unsigned char ch) {
33     sbi_call(SBI_CONSOLE_PUTCHAR, ch, 0, 0);
34 }
35
36 void sbi_set_timer(unsigned long long stime_value) {
37     sbi_call(SBI_SET_TIMER, stime_value, 0, 0);
38 }
39
40 void sbi_shutdown(void) {
41     sbi_call(SBI_SHUTDOWN, 0, 0, 0);
42 }
43

```

```

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    sbi.h
    stdarg.h
    stdio.h
    string.c
    string.h
  obj
  tools
    function.mk
    kernel.ld
    Makefile
  ...
C init.c M Makefile kernel.ld C stdio.h M C sbi.c M C stdio.c M X entry.S
kern > libs > C stdio.c > shutdown(void)
1 #include <console.h>
2 #include <defs.h>
3 #include <stdio.h>
4 #include <sbi.h>
5
6 void shutdown(void) {
7     sbi_shutdown();
8 }
9
10 /* HIGH level console I/O */
11
12 /* *
13  * cputch - writes a single character @c to stdout, and it will
14  * increace the value of counter pointed by @cnt.
15  * */
16 static void cputch(int c, int *cnt) {
17     cons_putc(c);
18     (*cnt)++;
19 }
20
21 /* *
22  * vprintf - format a string and writes it to stdout
23  *
24  * The return value is the number of characters which would be
25  * written to stdout.
26  *
27  * Call this function if you are already dealing with a va_list.
28  * Or you probably want printf() instead.
29  * */
30 int vprintf(const char *fmt, va_list ap) {
31     int cnt = 0;
32     vprintfmt((void *)cputch, &cnt, fmt, ap);
33 }

```

```

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  console.c
  console.h
  init
  entry.S
  init.c
  libs
  stdio.c
  mm
  memlayout.h
  mmu.h
  libs
  defs.h
  error.h
  printfmt.c
  readline.c
  riscv.h
  sbi.c
  sbi.h
  stdarg.h
  stdio.h
  string.c
  string.h
  obj
  tools
  function.mk
  kernel.ld
  Makefile
C init.c M Makefile kernel.ld C stdio.h M X C sbi.c M C stdio.c M entry.S
libs > C stdio.h > shutdown(void)
1 #ifndef _LIBS_STDIO_H_
2 #define _LIBS_STDIO_H_
3
4 #include <defs.h>
5 #include <stdarg.h>
6
7 /* kern/libs/stdio.c */
8 void shutdown(void);
9 int cprintf(const char *fmt, ...);
10 int vprintf(const char *fmt, va_list ap);
11 void putchar(int c);
12 int cputs(const char *str);
13 int double_puts(const char *str);
14 int getchar(void);
15
16 /* libs/readline.c */
17 char *readline(const char *prompt);
18
19 /* libs/printfmt.c */
20 void printfmt(void (*putch)(int, void *), void *putdat, const char *fmt, ...);
21 void vprintfmt(void (*putch)(int, void *), void *putdat, const char *fmt, va_list ap);
22 int snprintf(char *str, size_t size, const char *fmt, ...);
23 int vsnprintf(char *str, size_t size, const char *fmt, va_list ap);
24
25 #endif /* !_LIBS_STDIO_H_ */
26
27

```

```

File Edit Selection View Go Run Terminal Help
EXPLORER
LAB3
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  kernel
  ucore.bin
  kern
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  console.c
  console.h
  init
  entry.S
  init.c
  libs
  stdio.c
  mm
  memlayout.h
  mmu.h
  libs
  defs.h
  error.h
  printfmt.c
  readline.c
  riscv.h
  sbi.c
  sbi.h
  stdarg.h
  stdio.h
  string.c
  string.h
  obj
  tools
  function.mk
  kernel.ld
  Makefile
C init.c M X Makefile kernel.ld C stdio.h M C sbi.c M C stdio.c M entry.S
kern > init > C init.c > kern_init(void)
1 #include <stdio.h>
2 #include <string.h>
3 #include <console.h>
4
5 int kern_init(void) __attribute__((noreturn));
6
7 int kern_init(void)
8 {
9     extern char edata[], end[];
10    memset(edata, 0, end - edata);
11
12    const char *message = "os is loading ...\n";
13    cputs(message);
14
15    // const char *msg = "SUSTech OS\n";
16    // cputs(msg);
17
18    // const char *double_msg = "ILOVEOS\n";
19    // double_puts(double_msg);
20
21    //-----
22    cputs("The system will close.\n");
23    shutdown();
24    // -----
25
26    while (1)
27    {
28    }
29

```

- Result:

```
lrj11911808@lrj-Precision-3630-Tower: ~/2022_os/lab/lab03/code_lab3/lab3
lrj11911808@lrj-Precision-3630-Tower:~/2022_os/lab/lab03/code_lab3/lab3$ make qemu
+ cc kern/init/init.c
+ cc kern/libs/stdio.c
+ cc libs/printfmt.c
+ cc libs/readline.c
+ cc libs/sbi.c
+ ld bin/kernel
riscv64-unknown-elf-objcopy bin/kernel --strip-all -O binary bin/ucore.bin

OpenSBI v0.6

      _ _ _ _ _
     / /   / /
    / /   / /
   / /   / /
  / /   / /
 / /   / /
/_/_/_/_/_

Platform Name      : QEMU Virt Machine
Platform HART Features : RV64ACDFIMSU
Platform Max HARTs   : 8
Current Hart        : 0
Firmware Base       : 0x80000000
Firmware Size        : 120 KB
Runtime SBI Version  : 0.2

MIDELEG : 0x00000000000000222
MEDELEG : 0x0000000000000b109
PMP0     : 0x0000000080000000-0x000000008001ffff (A)
PMP1     : 0x0000000000000000-0xffffffffffffffff (A,R,W,X)
os is loading ...

The system will close.

lrj11911808@lrj-Precision-3630-Tower:~/2022_os/lab/lab03/code_lab3/lab3$
```