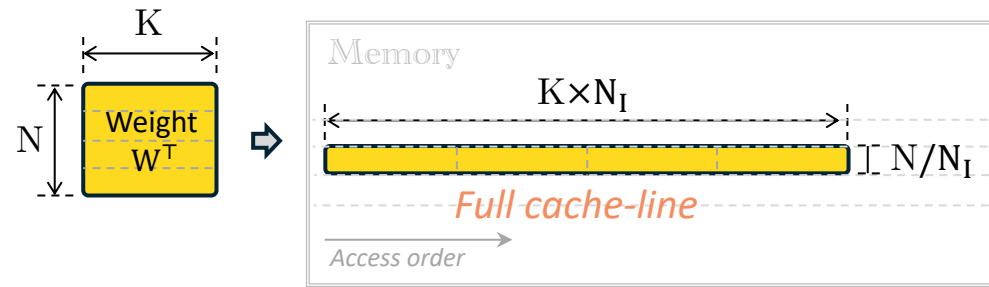(a) Store transposed matrix to enable contiguous read and write memory access

(b) Permute tiles to leverage full cache-line and further utilize contiguous memory access