

6 Applications

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graph LR; A[6 Applications] --- B[6.1 Hardware Development]; A --- C[6.2 Application Scenarios]; A --- D[6.3 Native Frameworks]; B --- B1[6.1.1 Commercial AI Chips]; B --- B2[6.1.2 Computational Power]; B --- B3[6.1.3 Memory Capacity]; C --- C1[6.2.1 Text Generation]; C --- C2[6.2.2 Image Generation]; C --- C3[6.2.3 Intelligent Assistant]; C --- C4[6.2.4 Autonomous driving]; D --- D1[Qualcomm AI Engine;]; D --- D2[NeuroPilot; MLX; Google AI Edge;]; D --- D3[TensorRT-LLM;]; D --- D4[OpenVINO; RKLLM];
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6.1 Hardware Development

6.1.1 Commercial AI Chips

6.1.2 Computational Power

6.1.3 Memory Capacity

6.2 Application Scenarios

6.2.1 Text Generation

6.2.2 Image Generation

6.2.3 Intelligent Assistant

6.2.4 Autonomous driving

6.3 Native Frameworks

Qualcomm AI Engine;
NeuroPilot; MLX; Google AI
Edge; TensorRT-LLM;
OpenVINO; RKLLM