

TMS320DM6437 Evaluation Module
509102-0001
SPECTRUM DIGITAL INCORPORATED

DM6437_EVM_SCHEMATIC.NET

Revised: Monday, March 19, 2007
Revision: D

[
C1
EIA0603
1uF

]
[
C10
EIA0603
1uF

]
[
C100
EIA0402
0.1uF

]
[
C101
EIA0402
0.1uF

]
[
C102
EIA1206
22uF

]
[
C103
EIA1206
22uF

]
[
C104
EIA0402
0.1uF

```
]
[
C105
EIA0402
0.1uF
```

```
]
[
C106
EIA0402
0.1uF
```

```
]
[
C107
EIA0402
0.1uF
```

```
]
[
C108
EIA0402
0.1uF
```

```
]
[
C109
EIA0402
0.1uF
```

```
]
[
C11
EIA0402
.1uF
```

```
]
[
C110
EIA0402
0.1uF
```

```
]
[
C111
EIA0402
0.1uF
```

] [
C112
EIA0402
0.1uF

] [
C113
EIA0402
0.1uF

] [
C114
EIA0402
0.1uF

] [
C115
EIA0402
0.1uF

] [
C116
EIA0402
0.1uF

] [
C118
EIA0402
0.1uF

] [
C119
EIA0402
0.1uF

] [
C12
EIA0603
1uF

] [
C120
EIA0402
0.1uF

] [
C121
EIA0402
0.1uF

] [
C122
EIA0402
.1uF

] [
C123
EIA0402
.1uF

] [
C124
EIA0402
0.1uF

] [
C125
EIA0402
0.1uF

] [
C126
EIA0402
0.1uF

] [
C127
EIA0402
0.1uF

] [
C128
EIA0402
0.1uF

] [
C129
EIA0402
0.1uF

] [
C13
EIA0402
.1uF

] [
C130
EIA0402
0.1uF

] [
C131
EIA0402
0.1uF

] [
C132
EIA1206
4.7uF

] [
C133
EIA1206
4.7uF

] [
C134
EIA0402

0.1uF

] [
C136
EIA0402
0.1uF

] [
C137
EIA1206
4.7uF

] [
C138
EIA0402
0.1uF

] [
C139
EIA1206
4.7uF

] [
C14
EIA0603
.001uF

] [
C140
EIA0402
0.1uF

] [
C141
EIA1206
4.7uF

] [
C142

EIA1812
1000pF 2kV

]
[
C143
EIA0402
0.1uF

]
[
C144
EIA0402
0.1uF

]
[
C146
EIA0402
0.1uF

]
[
C147
EIA0402
0.1uF

]
[
C148
EIA0603
100pF_NO-POP

]
[
C149
EIA0603
100pF_NO-POP

]
[
C15
EIA1206P
10uF

]
[

C150
EIA0402
0.1uF

]
[
C151
EIA0603
4700pF_NO-POP

]
[
C152
EIA0402
.1uF

]
[
C153
EIA0603
1uF

]
[
C154
EIA2816P
47uF

]
[
C155
EIA0603
10pF

]
[
C156
EIA0603
10pF

]
[
C157
EIA0603
10pF

]

[
C158
EIA0603
10pF

]
[
C159
EIA0603
1uF

]
[
C16
EIA1206P
10uF

]
[
C160
EIA0603
1uF

]
[
C161
EIA0603
1uF

]
[
C162
EIA0603
1uF

]
[
C163
EIA0402
0.1uF

]
[
C164
EIA0402
0.1uF

```
]
[
C165
EIA0402
0.1uF
```

```
]
[
C166
EIA0402
0.1uF
```

```
]
[
C167
EIA0402
0.1uF
```

```
]
[
C168
EIA0402
0.1uF
```

```
]
[
C169
EIA0402
NO POP
```

```
]
[
C17
EIA0402
.1uF
```

```
]
[
C170
EIA0402
0.1uF
```

```
]
[
C171
EIA0402
0.1uF
```

```
]
[
C172
EIA0402
0.1uF
```

```
]
[
C174
EIA0402
0.1uF
```

```
]
[
C175
EIA0402
0.1uF
```

```
]
[
C176
EIA0402
0.1uF
```

```
]
[
C177
EIA0402
0.1uF
```

```
]
[
C178
EIA0402
0.1uF
```

```
]
[
C179
EIA0402
0.1uF
```

```
]
[
C18
EIA0603
.001uF
```

```
]
[
C180
EIA0402
0.1uF
```

```
]
[
C181
EIA0402
0.1uF
```

```
]
[
C182
EIA0402
0.1uF
```

```
]
[
C183
EIA0402
0.1uF
```

```
]
[
C184
EIA0402
0.1uF
```

```
]
[
C185
EIA0402
0.1uF
```

```
]
[
C186
EIA0402
0.1uF
```

```
]
[
C187
EIA0402
.1uF
```

] [
C188
EIA0402
0.1uF

] [
C189
EIA0402
0.1uF

] [
C19
EIA0402
0.1uF

] [
C190
EIA0402
0.1uF

] [
C191
EIA0402
0.1uF

] [
C195
EIA0402
.1uF

] [
C196
EIA0402
0.1uF

] [
C197
EIA0402

.1uF

] [
C198
EIA0402
0.1uF

] [
C199
EIA0402
.1uF

] [
C2
EIA0603
18 pF

] [
C20
EIA0402
0.1uF

] [
C203
EIA0402
0.1uF

] [
C204
EIA1206P
33uF

] [
C205
EIA0402
0.1uF

] [
C206

EIA0402
0.1uF

]
[
C207
EIA0402
0.1uF

]
[
C208
EIA0402
0.1uF

]
[
C21
EIA0402
0.1uF

]
[
C219
EIA0402
0.1uF

]
[
C22
EIA0402
0.1uF

]
[
C220
EIA0402
0.1uF

]
[
C221
EIA1206P
10uF

]
[

C222
EIA0402
0.1uF

]
[
C223
EIA0402
0.1uF

]
[
C224
EIA0603
220pF

]
[
C225
EIA0402
0.1uF

]
[
C226
EIA0402
0.1uF

]
[
C227
EIA0402
0.1uF

]
[
C228
EIA0402
0.1uF

]
[
C229
EIA0402
0.1uF

]

[
C23
EIA0402
0.1uF

]
[
C230
EIA1206P
10uF

]
[
C231
EIA0402
0.1uF

]
[
C232
EIA0402
0.1uF

]
[
C233
EIA0402
0.1uF

]
[
C234
EIA0603
220pF

]
[
C235
EIA0402
0.1uF

]
[
C236
EIA0402
0.1uF

] [
C237
EIA0402
0.1uF

] [
C238
EIA0402
0.1uF

] [
C239
EIA0603
220pF

] [
C24
EIA0402
0.1uF

] [
C240
EIA1206P
33uF, 6.3V

] [
C241
EIA0402
0.1uF

] [
C242
EIA0402
0.1uF

] [
C243
EIA1206P
33uF, 6.3V

] [
C244
EIA0402
0.1uF

] [
C245
EIA1206P
10UF, 6.3V

] [
C246
EIA0402
0.1uF

] [
C247
EIA1206P
10UF, 6.3V

] [
C248
EIA0402
0.1uF

] [
C249
EIA0402
0.1uF

] [
C25
EIA0402
0.1uF

] [
C250
EIA0402
0.1uF

] [
C251
EIA0402
0.1uF

] [
C252
EIA0402
0.1uF

] [
C253
EIA0402
0.1uF

] [
C254
EIA0603
680pF

] [
C255
EIA0402
0.1uF

] [
C256
EIA0603
330pF

] [
C257
EIA0603
330pF

] [
C258
EIA0402
0.1uF

] [
C259
EIA0402
0.1uF

] [
C26
EIA0402
0.1uF

] [
C260
EIA0402
0.1uF

] [
C261
EIA0603
330pF

] [
C262
EIA0603
680pF

] [
C263
EIA0603
330pF

] [
C264
EIA0402
0.1uF

] [
C265
EIA0402

0.1uF

] [
C266
EIA0402
0.1uF

] [
C267
EIA0402
0.1uF

] [
C268
EIA0402
0.1uF

] [
C269
EIA0603
33pF

] [
C27
EIA1206P
33uF

] [
C270
EIA0402
0.1uF

] [
C271
EIA0603
330pF

] [
C272

EIA0603
680pF

]
[
C273
EIA0603
330pF

]
[
C274
EIA0603
33pF

]
[
C275
EIA0402
0.1uF

]
[
C276
EIA0402
0.1uF

]
[
C277
EIA0402
0.1uF

]
[
C278
EIA0402
0.1uF

]
[
C279
EIA0402
0.1uF

]
[

C28
EIA1206P
33uF

]
[
C280
EIA0402
0.1uF

]
[
C281
EIA0402
0.1uF

]
[
C282
EIA0402
0.1uF

]
[
C283
EIA0402
0.1uF

]
[
C284
EIA0402
0.1uF

]
[
C285
EIA0402
0.1uF

]
[
C286
EIA0402
0.1uF

]

[
C287
EIA0402
0.1uF

]
[
C288
EIA0402
0.1uF

]
[
C289
EIA0402
0.1uF

]
[
C29
EIA0402
0.1uF

]
[
C290
EIA0402
0.1uF

]
[
C291
EIA0402
0.1uF

]
[
C292
EIA0402
0.1uF

]
[
C293
EIA0402
0.01uF

] [
C294
EIA1206P
10uF

] [
C296
EIA0603
10pF

] [
C297
EIA0603
10pF

] [
C3
EIA0402
.1uF

] [
C30
EIA0402
0.1uF

] [
C300
EIA0603
10pF

] [
C301
EIA0603
10pF

] [
C303
EIA0603
10pF

] [
C304
EIA0603
10pF

] [
C307
EIA0603
10pF

] [
C308
EIA0603
10pF

] [
C31
EIA0402
0.1uF

] [
C312
EIA0603
NO-POP

] [
C313
EIA0603
0.01uF

] [
C314
EIA0402
0.1uF

] [
C315
EIA0603
0.039uF

] [
C316
EIA0603
560pF

] [
C317
EIA0603
3300pF

] [
C318
EIA0603
0.047uF

] [
C319
EIA0603
NO-POP

] [
C32
EIA0402
0.1uF

] [
C320
EIA0402
0.1uF

] [
C321
EIA1210
10uF LESR

] [
C322
EIA0402
0.1uF

] [
C323
EIA2220
100uF 4V

] [
C324
EIA0603
1000pF

] [
C325
EIA2816P
100 uF

] [
C326
EIA0402
0.1uF

] [
C327
EIA0402
NO-POP

] [
C328
EIA0402
0.1uF

] [
C329
EIA0402
NO-POP

] [
C33
EIA0402

0.1uF

] [
C330
EIA0402
0.1uF

] [
C331
EIA0402
NO-POP

] [
C332
EIA0603
8200pF

] [
C333
EIA0402
0.1uF

] [
C334
EIA0603
0.039uF

] [
C335
EIA0603
470pF

] [
C336
EIA0603
3300pF

] [
C337

EIA0603
0.047uF

]
[
C338
EIA0603
NO-POP

]
[
C339
EIA0402
0.1uF

]
[
C34
EIA0402
0.1uF

]
[
C340
EIA2816P
47uF

]
[
C341
EIA0402
0.1uF

]
[
C342
EIA1210
10uF LESR

]
[
C343
EIA2220
100uF 4V

]
[

C344
EIA0603
1000pF

]
[
C345
EIA2220
100 uF 4V

]
[
C346
EIA2816P
100 uF

]
[
C347
EIA0603
8200pF

]
[
C348
EIA0402
0.1uF

]
[
C349
EIA0603
0.039uF

]
[
C35
EIA0402
0.1uF

]
[
C350
EIA0603
470pF

]

[
C351
EIA0603
3300pF

]
[
C352
EIA0603
0.047uF

]
[
C353
EIA0603
NO-POP

]
[
C354
EIA0402
0.1uF

]
[
C355
EIA1210
10uF LESR

]
[
C356
EIA0402
0.1uF

]
[
C357
EIA2220
100uF 4V

]
[
C358
EIA0603
1000pF

] [
C359
EIA2816P
100 uF

] [
C36
EIA0402
0.1uF

] [
C361
EIA0402
5.6pF

] [
C362
EIA0402
NO-POP

] [
C363
EIA0402
NO-POP

] [
C364
EIA0402
5.6pF

] [
C365
EIA0402
5.6pF

] [
C366
EIA0402
0.1uF

] [
C367
EIA1206
10uF

] [
C368
EIA0603
1uF

] [
C37
EIA0402
0.1uF

] [
C370
EIA0603
10pF

] [
C371
EIA0805
0.33uF

] [
C372
EIA1206
22uF

] [
C373
EIA0603
470pF

] [
C374
EIA0805
0.33uF

] [
C375
EIA1206
22uF

] [
C376
EIA0603
470pF

] [
C377
EIA0805
0.33uF

] [
C378
EIA1206
22uF

] [
C379
EIA0603
470pF

] [
C380
EIA0603
470pF

] [
C381
EIA0805
0.33uF

] [
C382
EIA0603
470pF

] [
C383
EIA0805
0.33uF

] [
C384
EIA1206
4.7uF

] [
C385
EIA0805
0.33uF

] [
C386
EIA0805
0.33uF

] [
C39
EIA1206P
33uF

] [
C392
EIA0805
0.33uF

] [
C393
EIA0603
470pF

] [
C394
EIA0603

470pF

] [
C4
EIA0603
18 pF

] [
C401
EIA0402
0.1uF

] [
C402
EIA0402
0.1uF

] [
C403
EIA0402
0.1uF

] [
C404
EIA0402
0.1uF

] [
C405
EIA0603
470pF

] [
C41
EIA1206P
33uF

] [
C42

EIA0402
0.1uF

]
[
C43
EIA0402
0.1uF

]
[
C44
EIA0402
0.1uF

]
[
C45
EIA0402
0.1uF

]
[
C46
EIA0402
0.1uF

]
[
C47
EIA0402
0.1uF

]
[
C48
EIA0402
0.1uF

]
[
C49
EIA0402
0.1uF

]
[

C5
EIA0402
0.1uF

]
[
C50
EIA0402
0.1uF

]
[
C51
EIA0402
0.1uF

]
[
C52
EIA0402
0.1uF

]
[
C53
EIA0402
0.1uF

]
[
C54
EIA0402
0.1uF

]
[
C55
EIA1206P
33uF

]
[
C56
EIA1206P
33uF

]

[
C57
EIA1206P
33uF

]
[
C58
EIA0402
0.1uF

]
[
C59
EIA0402
0.1uF

]
[
C60
EIA0402
0.1uF

]
[
C61
EIA0402
0.1uF

]
[
C62
EIA0402
0.1uF

]
[
C63
EIA0402
0.1uF

]
[
C64
EIA0402
0.1uF

] [
C65
EIA0402
0.1uF

] [
C66
EIA0402
0.1uF

] [
C67
EIA0402
0.1uF

] [
C69
EIA1206P
33uF

] [
C7
EIA0402
.1uF

] [
C70
EIA0402
0.1uF

] [
C71
EIA0603
1000pF

] [
C72
EIA0402
0.1uF

] [
C73
EIA0603
1uF

] [
C77
EIA0402
0.1uF

] [
C78
EIA0603
NO-POP

] [
C79
EIA0603
NO-POP

] [
C8
EIA0402
0.1uF

] [
C80
EIA0402
0.1uF

] [
C81
EIA0402
0.1uF

] [
C82
EIA0402
0.1uF

] [
C83
EIA0402
0.1uF

] [
C84
EIA1206
10uF

] [
C85
EIA0402
0.1uF

] [
C86
EIA0402
0.1uF

] [
C87
EIA0402
0.1uF

] [
C88
EIA0603
100pF

] [
C89
EIA0402
0.1uF

] [
C9
EIA0402
0.1uF

] [
C90
EIA0402
.1uF

] [
C91
EIA0402
.1uF

] [
C92
EIA0402
0.1uF

] [
C93
EIA0402
0.1uF

] [
C94
EIA0402
0.1uF

] [
C95
EIA0402
0.1uF

] [
C96
EIA0402
0.1uF

] [
C97
EIA0402

0.1uF

] [
C98
EIA0402
0.1uF

] [
C99
EIA0402
0.1uF

] [
D1
EIA0603
PGB0010603_NO-PO

] [
D10
SOT23-B
BAS16-7-F

] [
D2
EIA0603
PGB0010603_NO-PO

] [
D3
SMC
SMCJ6A

] [
D5
LM4040-SOT23
LM4040DCIM3-4.1

] [
DC_P1

SMTCON50X2F
CONNECTOR 50 X 2

]
[
DC_P2
SMTCON45X2F
CONNECTOR 45 X 2

]
[
DC_P3
SMTCON10X2F
HEADER 10X2

]
[
DS1
EIA1206D
LED_GRN

]
[
DS2
EIA1206D
LED_GRN

]
[
DS3
EIA1206D
LED_GRN

]
[
DS4
EIA1206D
LED_RED

]
[
DS5
EIA1206D
GREEN

]
[

DS502
EIA1206D
Green

]
[
Embedded_USB_1
Embedded_USB
Embedded_USB

]
[
F1
F-SMF-OMNI-BLOK
F_4.0A

]
[
J1
RCA844
NO-POP

]
[
J10
RCA844
RCA JACK

]
[
J16
RASM712
RASM712

]
[
J2
RCA844
RCA JACK

]
[
J20
1318228-1
mPCI_VLYNQ_CONNE

]

[
J3
RCA844
RCA JACK

] [
J4
RCA844
RCA JACK

] [
J5
RCA844
RCA JACK

] [
J6
CON7X2
TSW-107-14-G-D-0

] [
JP1
CON3
3 PIN JUMPER

] [
JP2
CON4X2
CONN 4x2

] [
JP3
CON2
HEADER 2 NO-POP

] [
JP4
CON2
HEADER 2 NO-POP

```
]
[
L1
EIA0805
BLM21PG221SN1D
```

```
]
[
L11
EIA0805
BLM21PG221SN1D
```

```
]
[
L12
EIA0805
1uH
```

```
]
[
L13
EIA0805
1uH
```

```
]
[
L14
SMTIND
BLM41P750SPT
```

```
]
[
L15
SMTIND
BLM41P750SPT
```

```
]
[
L16
SMTIND
BLM41P750SPT
```

```
]
[
L18
EIA0805
BLM21PG221SN1D
```

```
]
[
L19
EIA0805
BLM21PG221SN1D
```

```
]
[
L20
EIA0805
BLM21PG221SN1D
```

```
]
[
L21
EIA0805
BLM21PG221SN1D
```

```
]
[
L22
EIA0805
BLM21PG221SN1D
```

```
]
[
L23
EIA0805
BLM21PG221SN1D
```

```
]
[
L24
EIA0805
BLM21PG221SN1D
```

```
]
[
L25
SMTIND
BLM41P750SPT
```

```
]
[
L26
SMTIND
BLM41P750SPT
```

```
]
[
L27
SMTIND
BLM41P750SPT
```

```
]
[
L28
SMTIND
BLM41P750SPT
```

```
]
[
L29
EIA0603
2.7uH
```

```
]
[
L3
EIA0805
BLM21PG221SN1D
```

```
]
[
L30
EIA0603
2.7uH
```

```
]
[
L31
EIA0603
2.7uH
```

```
]
[
L32
EIA0603
2.7uH
```

```
]
[
L33
EIA0603
2.7uH
```

] [
L34
EIA0603
2.7uH

] [
L35
SMTIND
BLM41P750SPT

] [
L37
EIA0805
1uH

] [
L4
EIA0805
BLM21PG221SN1D

] [
L40
EIA0805
1uH

] [
L41
EIA0805
1uH

] [
L46
EIA0805
1uH

] [
L49
SMTIND

BLM41P750SPT

] [
L5
EIA0805
BLM21PG221SN1D

] [
L50
ELL6
2.7 uH

] [
L51
SMTIND
BLM41P750SPT

] [
L52
IHLP2525CZ
3.3 uH

] [
L53
SMTIND
BLM41P750SPT

] [
L54
ELL6
2.7 uH

] [
L55
EIA0805
BLM21PG221SN1D

] [
L56

EIA0805
BLM21PG221SN1D

]
[
L57
EIA0805
BLM21PG221SN1D

]
[
L58
EIA0805
BLM21PG221SN1D

]
[
L6
EIA0805
BLM21PG221SN1D

]
[
L8
EIA0805
BLM21PG221SN1D

]
[
L9
EIA0805
BLM21PG221SN1D

]
[
P1
749181-1
749181-1

]
[
P10
ST-3500
Line In

]
[

P11
ST-3500
Mic In

]
[
P12
ST-3500
Headphone Out

]
[
P13
ST-3500
Line Out

]
[
P14
TOTX141P
TOTX141P

]
[
P2
749181-1
749181-1

]
[
P3
HF11-2450E
RJ45 HALO HFJ11-

]
[
P4
PCICNNKEYED
PCI Connector

]
[
P7
DB9FR-1200X500
DB9-FEMALE

]

[
P8
DB9MR-1225X475
DB9-MALE

]
[
Q1
SOT23-NPN
2N3904

]
[
Q4
SOT23-FET
BSS123

]
[
R1
EIA0402
10K

]
[
R10
EIA0402
NO-POP

]
[
R100
EIA0402
10K

]
[
R101
EIA0402
NO-POP

]
[
R102
EIA0402
10K

] [
R103
EIA0402
10K

] [
R104
EIA0402
10K

] [
R105
EIA0402
NO-POP

] [
R106
EIA0402
NO-POP

] [
R107
EIA0402
10K

] [
R109
EIA0603
49.9

] [
R11
EIA0402
1K

] [
R110
EIA0603
49.9

```
]
[
R111
EIA0402
22
```

```
]
[
R112
EIA0402
NO-POP
```

```
]
[
R113
EIA0402
NO-POP
```

```
]
[
R114
EIA0402
NO_POP
```

```
]
[
R115
EIA0603
49.9
```

```
]
[
R116
EIA0402
NO-POP
```

```
]
[
R117
EIA0603
49.9
```

```
]
[
R118
EIA0402
NO-POP
```

] [
R119
EIA0402
10K

] [
R12
EIA0402
NO-POP

] [
R120
EIA0402
NO-POP

] [
R121
EIA0402
10K

] [
R123
EIA0402
10K

] [
R124
EIA0603
6.65K

] [
R126
EIA0402
NO-POP

] [
R127
EIA0603
330

] [
R128
EIA0603
330

] [
R129
EIA0402
NO-POP

] [
R13
EIA0402
1K

] [
R130
EIA0402
NO-POP

] [
R131
EIA0402
10K

] [
R132
EIA0402
10

] [
R133
EIA0603
1.5K

] [
R134
EIA0402

NO-POP

] [
R135
EIA0402
NO-POP

] [
R136
EIA0402
NO-POP

] [
R137
EIA0402
NO-POP

] [
R138
EIA0402
10K

] [
R139
EIA0402
10K

] [
R14
EIA0402
33

] [
R140
EIA1206
0

] [
R141

EIA1206
120

]
[
R142
EIA0402
10K

]
[
R143
EIA0402
10K

]
[
R144
EIA0402
NO-POP

]
[
R145
EIA0402
0

]
[
R146
EIA0402
NO-POP

]
[
R147
EIA0402
10K

]
[
R148
EIA0603
NO POP

]
[

R149
EIA0603
150

]
[
R15
EIA0402
2.2K

]
[
R150
EIA0402
10K

]
[
R151
EIA0402
10K

]
[
R152
EIA0402
10K

]
[
R153
EIA0402
NO-POP

]
[
R154
EIA0402
0

]
[
R158
EIA0402
0

]

[
R16
EIA0402
2.2K

]
[
R161
EIA0402
1K

]
[
R163
EIA0402
10K

]
[
R164
EIA0402
NO-POP

]
[
R165
EIA0402
NO-POP

]
[
R166
EIA0402
20K

]
[
R167
EIA0402
20K

]
[
R168
EIA0402
NO-POP

] [
R17
EIA0402
1K

] [
R170
EIA0402
10K

] [
R171
EIA0402
0

] [
R172
EIA0402
0

] [
R173
EIA0402
1.5K

] [
R174
EIA0402
1.5K

] [
R175
EIA0402
20K

] [
R176
EIA0402
20K

] [
R177
EIA0402
33

] [
R178
EIA0402
0

] [
R179
EIA0402
0

] [
R18
EIA0402
NO-POP

] [
R180
EIA0402
0

] [
R181
EIA0402
NO-POP

] [
R182
EIA0402
no-pop

] [
R183
EIA0402
NO-POP

] [
R184
EIA0402
0

] [
R185
EIA0402
10K

] [
R186
EIA0402
10K

] [
R187
EIA0402
10K

] [
R188
EIA0402
10K

] [
R189
EIA0402
10K

] [
R19
EIA0402
NO-POP

] [
R192
EIA0402
10K

] [
R193
EIA0603
5.6K

] [
R194
EIA0603
5.6K

] [
R195
EIA0603
5.6K

] [
R196
EIA0603
5.6K

] [
R197
EIA0603
47K

] [
R198
EIA0603
330

] [
R199
EIA0402
0

] [
R2
EIA0402

33

```
]
[
R20
EIA0402
10K
```

```
]
[
R200
EIA0402
0
```

```
]
[
R201
EIA0402
10K
```

```
]
[
R202
EIA0603
330
```

```
]
[
R203
EIA0402
20K
```

```
]
[
R204
EIA0402
20K
```

```
]
[
R205
EIA0402
10K
```

```
]
[
R206
```

EIA0402
NO-POP

]
[
R207
EIA0402
NO-POP

]
[
R208
EIA0402
10K

]
[
R209
EIA0402
10

]
[
R210
EIA0402
20K

]
[
R211
EIA0402
20K

]
[
R212
EIA0402
20K

]
[
R213
EIA0402
10

]
[

R214
EIA0402
10

]
[
R215
EIA0402
10

]
[
R216
EIA0402
10

]
[
R217
EIA0402
20K

]
[
R218
EIA0402
10

]
[
R219
EIA0402
10

]
[
R221
EIA0402
10K

]
[
R222
EIA0402
10K

]

[
R224
EIA0603
220

]
[
R225
EIA0603
100

]
[
R226
EIA0402
0

]
[
R228
EIA0402
0

]
[
R229
EIA0603
75

]
[
R230
EIA0402
2.2K

]
[
R231
EIA0402
4.7K

]
[
R232
EIA0402
NO-POP

] [
R233
EIA0603
75

] [
R234
EIA0402
33

] [
R235
EIA0402
33

] [
R236
EIA0402
33

] [
R237
EIA0402
NO-POP

] [
R238
EIA0402
100K

] [
R239
EIA0603
75

] [
R240
EIA0402
2.2K

] [
R241
EIA0402
0

] [
R242
EIA0402
2.2K

] [
R243
EIA0603
75

] [
R245
EIA0603
75

] [
R246
EIA0402
10K

] [
R247
EIA0402
10K

] [
R248
EIA0402
10K

] [
R249
EIA0402
22

] [
R250
EIA0603
75

] [
R251
EIA0402
10K

] [
R252
EIA0402
NO-POP

] [
R255
EIA0603
75

] [
R256
EIA0402
33

] [
R257
EIA0402
33

] [
R258
EIA0402
20K

] [
R265
EIA0402
NO-POP

] [
R266
EIA0402
1K

] [
R267
EIA0402
NO-POP

] [
R268
EIA0402
NO-POP

] [
R27
EIA0402
22

] [
R270
EIA0402
NO-POP

] [
R272
EIA0402
0

] [
R273
EIA0603
56.0K 1%

] [
R274
EIA0603

56.0K 1%

] [
R275
EIA0603
71.5K 1%

] [
R276
EIA0603
1.65K 1%

] [
R277
EIA0603
107 1%

] [
R278
EIA0402
10K

] [
R279
EIA0603
10K 0.1%

] [
R280
EIA2512
0.025

] [
R281
EIA0402
1K

] [
R282

EIA0402
9.09K 1%

]
[
R283
EIA0402
10K 1%

]
[
R284
EIA0402
10K

]
[
R285
EIA0402
20K 1%

]
[
R287
EIA0402
10K 1%

]
[
R288
EIA0402
10K

]
[
R289
EIA0402
0

]
[
R29
EIA0402
22

]
[

R290
EIA0402
10K 1%

]
[
R291
EIA0603
3.74K 1%

]
[
R292
EIA0402
NO-POP

]
[
R293
EIA0603
2K 1%

]
[
R294
EIA0603
71.5K 1%

]
[
R295
EIA0402
10K

]
[
R296
EIA0603
107 1%

]
[
R297
EIA0603
10K 1%

]

[
R298
EIA2512
0.025

]
[
R299
EIA0603
360

]
[
R3
EIA0402
NO-POP

]
[
R30
EIA0402
10

]
[
R300
EIA0603
10.2K 1%

]
[
R301
EIA0603
71.5K 1%

]
[
R302
EIA0603
2K 1%

]
[
R303
EIA0603
107 1%

] [
R304
EIA0402
10K

] [
R305
EIA0603
10K 1%

] [
R306
EIA2512
0.025

] [
R307
EIA0402
22

] [
R308
EIA0402
0

] [
R309
EIA0402
10K

] [
R31
EIA0402
10

] [
R310
EIA0402
10K

] [
R311
EIA0402
0

] [
R312
EIA0402
0

] [
R313
EIA0402
0

] [
R314
EIA0402
0

] [
R315
EIA0402
0

] [
R316
EIA2512
NO-POP

] [
R317
EIA2512
0.025

] [
R319
EIA2512
0.025

] [
R32
EIA0603
200

] [
R320
EIA0603
0.1

] [
R322
EIA0603
0.1

] [
R323
EIA0603
0.1

] [
R324
EIA0402
NO-POP

] [
R326
EIA0402
NO-POP

] [
R328
EIA0402
NO-POP

] [
R329
EIA0402
0

] [
R33
EIA0603
200

] [
R331
EIA0402
0

] [
R332
EIA0402
0

] [
R333
EIA0402
22

] [
R334
EIA0402
0

] [
R335
EIA0402
0

] [
R336
EIA0402
22

] [
R337
EIA0402

0

] [
R338
EIA0402
NO-POP

] [
R339
EIA0402
NO-POP

] [
R34
EIA0402
0

] [
R342
EIA1206
0

] [
R343
EIA1206
0

] [
R344
EIA0402
NO-POP

] [
R345
EIA0402
10K

] [
R346

EIA0402
33

]
[
R347
EIA0402
0

]
[
R348
EIA0402
0

]
[
R349
EIA0402
0

]
[
R35
EIA0603
0.1

]
[
R350
EIA0402
33

]
[
R356
EIA0402
33

]
[
R358
EIA0402
1K

]
[

R36
EIA0402
1K

]
[
R360
EIA0402
0

]
[
R361
EIA0402
NO-POP

]
[
R362
EIA0402
0

]
[
R363
EIA0402
NO-POP

]
[
R364
EIA0402
20K

]
[
R366
EIA0402
10K

]
[
R367
EIA0402
NO-POP

]

[
R368
EIA0402
10K

]
[
R369
EIA0402
10K

]
[
R37
EIA0603
7.5K 1%

]
[
R370
EIA0402
10K

]
[
R371
EIA0402
10K

]
[
R372
EIA0402
NO-POP

]
[
R373
EIA0402
NO-POP

]
[
R374
EIA0402
NO-POP

] [
R375
EIA0402
NO-POP

] [
R376
EIA0402
10K

] [
R377
EIA0402
NO-POP

] [
R378
EIA0402
10K

] [
R38
EIA0402
0

] [
R39
EIA0603
4.99K 1%

] [
R393
EIA0402
NO-POP

] [
R394
EIA0402
NO-POP

] [
R4
EIA0402
NO-POP

] [
R41
EIA0402
NO-POP

] [
R419
EIA0402
1K

] [
R42
EIA0402
20K

] [
R429
EIA0402
0

] [
R43
EIA0402
20K

] [
R430
EIA0402
0

] [
R431
EIA0402
0

] [
R432
EIA0402
0

] [
R433
EIA0402
33

] [
R434
EIA0402
33

] [
R435
EIA0402
33

] [
R436
EIA0402
33

] [
R437
EIA0402
0

] [
R438
EIA0402
NO-POP

] [
R439
EIA0402
0

] [
R44
EIA0402
1.5K

] [
R441
EIA0402
NO-POP

] [
R442
EIA0402
NO-POP

] [
R443
EIA0402
0

] [
R444
EIA0402
NO-POP

] [
R445
EIA0402
0

] [
R446
EIA0402
0

] [
R447
EIA0402

NO-POP

] [
R448
EIA0402
0

] [
R45
EIA0402
1.5K

] [
R451
EIA0402
0

] [
R46
EIA0402
33

] [
R47
EIA0402
0

] [
R48
EIA0402
2.2K

] [
R49
EIA0603
100K

] [
R5

EIA0402
0

] [
R50
EIA0402
33

] [
R51
EIA0402
NO-POP

] [
R52
EIA0603
22.1K 1%

] [
R53
EIA0402
NO-POP

] [
R54
EIA0402
33

] [
R55
EIA0402
NO-POP

] [
R56
EIA0402
33

] [
R57

R57
EIA0402
NO-POP

]
[
R58
EIA0402
0

]
[
R59
EIA0402
NO-POP

]
[
R6
EIA0402
1K

]
[
R60
EIA0402
33

]
[
R62
EIA0402
33

]
[
R64
EIA0603
330

]
[
R65
EIA0603
330

]

[
R66
EIA0603
330

]
[
R67
EIA0603
330

]
[
R68
EIA0603
1.5K

]
[
R69
EIA0402
0

]
[
R7
EIA0402
1K

]
[
R70
EIA0402
NO-POP

]
[
R71
EIA0402
10K

]
[
R73
EIA0402
0

] [
R74
EIA0402
10K

] [
R75
EIA0402
NO-POP

] [
R76
EIA0402
1K 1%

] [
R77
EIA0402
1K 1%

] [
R78
EIA0402
22

] [
R79
EIA0402
22

] [
R8
EIA0402
33

] [
R80
EIA0402
22

] [
R81
EIA0402
22

] [
R82
EIA0402
22

] [
R83
EIA0402
22

] [
R84
EIA0402
22

] [
R85
EIA0402
22

] [
R86
EIA0402
22

] [
R87
EIA0402
22

] [
R88
EIA0402
NO-POP

] [
R89
EIA0402
47K

] [
R9
EIA0402
33

] [
R90
EIA0402
NO-POP

] [
R91
EIA0402
10K

] [
R93
EIA0402
10K

] [
R94
EIA0402
0

] [
R95
EIA0402
0

] [
R96
EIA0402
10K

] [
R97
EIA0402
10K

] [
R98
EIA0402
0

] [
R99
EIA0402
NO-POP

] [
RN1
EXB-2HVS
RPACK8-22

] [
RN10
EXB-28VS
RPACK4-22

] [
RN11
EXB-28VS
RPACK4-22

] [
RN12
EXB-2HVS
RPACK8-20K

] [
RN13
EXB-2HVS

RPACK8-1.5K

```
]
[
RN14
EXB-2HVS
RPACK8-22
```

```
]
[
RN15
EXB-2HVS
RPACK8-10k
```

```
]
[
RN2
EXB-2HVS
RPACK8-22
```

```
]
[
RN20
EXB-28VS
RPACK4-10K
```

```
]
[
RN22
EXB-28VS
RPACK4-10K
```

```
]
[
RN23
EXB-28VS
RPACK4-22
```

```
]
[
RN24
EXB-28VS
RPACK4-22
```

```
]
[
RN25
```

EXB-28VS
RPACK4-22

]
[
RN26
EXB-28VS
RPACK4-22

]
[
RN27
EXB-28VS
RPACK4-22

]
[
RN28
EXB-28VS
RPACK4-22

]
[
RN29
EXB-28VS
RPACK4-22

]
[
RN3
EXB-2HVS
RPACK8-22

]
[
RN30
EXB-28VS
RPACK4-22

]
[
RN31
EXB-2HVS
RPACK8-47K

]
[

RN32
EXB-2HVS
RPACK8-10

]
[
RN33
EXB-2HVS
RPACK8-33

]
[
RN34
EXB-28VS
RPACK4-10K

]
[
RN35
EXB-28VS
RPACK4-100K

]
[
RN36
EXB-28VS
RPACK4-100K

]
[
RN37
EXB-2HVS
RPACK8-10K

]
[
RN38
EXB-28VS
RPACK4-1K

]
[
RN4
EXB-2HVS
RPACK8-22

]

[
RN5
EXB-2HVS
RPACK8-33

]
[
RN6
EXB-2HVS
RPACK8-10

]
[
RN7
EXB-28VS
RPACK4-22

]
[
RN8
EXB-28VS
RPACK4-22

]
[
RN9
EXB-28VS
RPACK4-22

]
[
SW1
SMTDIPSW8A
TDA10H0SK1

]
[
SW2
SMTDIPSW4A
TDA10H0SK1

]
[
SW3
SMTDIPSW2A
TDA10H0SK1

```
]
[
SW4
TI_SMTDIPSW8R
PADDLE DIP-4
```

```
]
[
SW5
101215
PUSHBUTTON SW
```

```
]
[
SW6
101215
PUSHBUTTON SW
```

```
]
[
SW7
GS01MSAKE
GS01MSAKE
```

```
]
[
T1
ACT45B
ACT45B-510-2P_NO
```

```
]
[
TP1
TP030
TP30
```

```
]
[
TP10
CON1
TP-60
```

```
]
[
TP12
TP030
TP-30
```

```
]
[
TP13
CON1
TP-60
```

```
]
[
TP14
CON1
TP-60
```

```
]
[
TP15
TP030
TP-30
```

```
]
[
TP16
TP030
TP-30
```

```
]
[
TP17
TP030
TP-30
```

```
]
[
TP18
TP030
TP-30
```

```
]
[
TP19
TP030
TP-30
```

```
]
[
TP2
TP030
TP30
```

] [
TP20
TP030
TP-30

] [
TP21
TP030
TP-30

] [
TP22
CON1
TP-60

] [
TP23
CON1
TP-60

] [
TP25
CON1
TP-60

] [
TP26
CON1
TP-60

] [
TP27
CON1
TP-60

] [
TP28
CON1
TP-60

] [
TP29
CON1
TP-60

] [
TP30
CON1
TP-60

] [
TP31
TP030
TP-30

] [
TP32
TP030
TP-30

] [
TP33
CON1
TP-60

] [
TP34
CON1
TP-60

] [
TP36
TP030
TP-30

] [
TP37
CON1

TP-60

] [
TP38
CON1
TP-60

] [
TP4
TP030
TP-30

] [
TP40
TP030
TP-30

] [
TP41
TP030
TP-30

] [
TP42
CON1
TP-60

] [
TP43
CON1
TP-60

] [
TP46
CON1
TP-60

] [
TP47

CON1
TP-60

]
[
TP48
TP030
TP-30

]
[
TP49
TP030
TP-30

]
[
TP5
TP030
TP-30

]
[
TP52
TP030
TP-30

]
[
TP53
TP030
TP-30

]
[
TP54
TP030
TP-30

]
[
TP55
TP030
TP-30

]
[

TP58
TP030
TP-30

]
[
TP59
TP030
TP-30

]
[
TP6
TP030
TP-30

]
[
TP61
CON1
TP-60

]
[
TP64
CON1
TP-60

]
[
TP69
CON1
TP-60

]
[
TP7
CON1
TP-60

]
[
TP70
CON1
TP-60

]

[
TP8
CON1
TP-60

]
[
TP9
CON1
TP-60

]
[
U1
DM6437_0
DM6437_0

]
[
U10
TSSOP20
PCF8574APWRG4

]
[
U103
DCK6
OPA361AIDCKT

]
[
U104
DCK6
OPA361AIDCKT

]
[
U105
DCK6
OPA361AIDCKT

]
[
U106
DCK6
OPA361AIDCKT

]
[
U11
TSSOP20
PCF8574APWRG4

]
[
U12
TSSOP16
SN74CBT3257PW

]
[
U13
TSSOP20
PCF8574APWRG4

]
[
U14
DCK5
SN74LVC1G32

]
[
U15
DBV5
SN74LVC1G06DBVRG

]
[
U16
BGA21X9_92D
92-ball DDR Pack

]
[
U17
BGA21X9_92D
92-ball DDR Pack

]
[
U18
SO24PWA
SN74CBTLV3384PW

] [
U19
TSSOP16
74CBTLV3257PWR

] [
U2
ASFL3
NO-POP

] [
U20
TSSOP16
74CBTLV3257PWR

] [
U22
BGA8X6_9X11MM
IS62WV20488BLL-1

] [
U23
BGA63-9X11MM-.8M
NAND512W3A2BZA6E

] [
U24
BGA8X8_11X13MM
S29GL256N11FFI01

] [
U25
SO8WN
CAT24C256

] [
U26
TSSOP16
74CBTLV3257PWR

] [
U27
SO24PWA
SN74CBTLV3384PW

] [
U28
TSSOP16
74CBTLV3257PWR

] [
U3
DCK5
74LVC1G125DCKRG4

] [
U30
TI_TQFP48
KS8001L

] [
U31
SO8
SN65HVD235DRG4

] [
U32
DCK5
SN74AHC1G14DCKRG

] [
U33
TSSOP16
74CBTLV3257PWR

] [
U34
TSSOP16
MAX3221CPWRG4

] [
U35
DCK5
SN74LVC1G32

] [
U36
DCK5
SN74LVC1G07

] [
U37
DCK5
SN74LVC1G32

] [
U38
DCK5
SN74AHC1G14DCKRG

] [
U39
DCK5
SN74AHC1G14DCKRG

] [
U4
DBV5
TLV431ADB

] [
U40
DCK5
SN74LVC1G08

] [
U41
TSSOP16

SN74CBTLV3257PWR

```
]
[
U42
DCK5
SN74AHC1G14DCKRG
```

```
]
[
U43
BGA9X9ZQE
TVL320AIC33IZQE
```

```
]
[
U44
DCK5
SN74LVC1G32
```

```
]
[
U45
SO24PWA
SN74CBTLV3384
```

```
]
[
U46
DCK5
74LVC1G125DCKRG4
```

```
]
[
U48
DCK5
SN74CBTLV1G125
```

```
]
[
U49
DCK5
74LVC1G125DCKRG4
```

```
]
[
U5
```

DCK5
74LVC1G125DCKRG4

]
[
U50
PQFP80-PFP
TVP5146M2

]
[
U51
TSSOP56
SN74CBT16292DGGR

]
[
U52
TSSOP56
SN74CBT16292DGGR

]
[
U53
TSSOP56
SN74CBT16292DGGR

]
[
U539
TSSOP16
SN74CBTLV3257PWR

]
[
U54
TSSOP56
SN74CBT16292DGGR

]
[
U55
PSOP20T
TPS54310PWP

]
[

U56
DBV6
TPS3808G09DBVRG4

]
[
U57
DBV6
TPS3808G09DBVRG4

]
[
U58
DBV6
TPS3808G09DBVRG4

]
[
U59
PSOP20T
TPS54310PWP

]
[
U6
S08
PI6CX100-27W

]
[
U60
PSOP20T
TPS54310PWP

]
[
U62
DCK5
SN74LVC1G32

]
[
U64
TSSOP20
PCF8574APWRG4

]


```
[  
U65  
DIP8SOCKET  
SOCKETED_SPI_EEP
```

```
]  
[  
U66  
DCK5  
SN74AHC1G14DCKRG
```

```
]  
[  
U67  
DBV5  
SN74LVC1G06DBVRG
```

```
]  
[  
U69  
DCK5  
SN74AHC1G14DCKRG
```

```
]  
[  
U7  
DBQ20  
PLL1705DBQ
```

```
]  
[  
U71  
DCK5  
SN74LVC1G32
```

```
]  
[  
U72  
CB3LV  
25MHZ
```

```
]  
[  
U73  
DCK5  
SN74LVC1G32
```

] [
U74
DCK5
SN74LVC1G32

] [
U75
SO24PWA
SN74CBTLV3384PW

] [
U76
TSSOP14
SN74CBTLV3125PWR

] [
U77
DCK5
SN74CBTLV1G125

] [
U78
DCK5
SN74CBTLV1G125

] [
U8
DCK5
74LVC1G125DCKRG4

] [
U9
DCK5
SN74CBTLV1G125

] [
Y1
ABM7
27MHz

```

]
[
Y2
ABM7
27MHz

```

```

]
[
Y4
ABM7
14.31818MHz

```

```

]
(
DDR_D10
U1,U5
RN23,5
)
(
MEM.EM_D4
U20,7
U22,E5
U23,K6
U24,H5
)
(
N17325473
U30,10
R132,2
)
(
N24576886
U53,31
R335,1
)
(
N17219383
C220,1
C221,1
C222,1
C223,1
U43,C9
L18,2
)
(
N17145676
R273,1
Q4,DRAIN
)
(
DDR_D20
U1,U15
RN30,8
)
(
MEM.EM_D5
U20,4
U22,F5

```

```
U23,J7
U24,E5
)
(
N17323714
R137,2
R372,1
U30,29
)
(
PCI_P_PERRN
U53,34
P4,B40
)
(
N17219420
U43,A1
U43,B3
R202,2
)
(
N17145664
Q4,GATE
C312,1
R272,1
)
(
N23788693
L3,1
TP5,1
R35,2
)
(
CORE_VDD_SELECT
U64,20
RN15,9
R272,2
)
(
MEM_EM_A21
R89,2
U18,2
U24,B8
)
(
PHYAD4
R371,1
U30,6
RN32,14
)
(
N24576818
U53,33
R334,1
)
(
N17219456
U43,J6
C247,1
)
(
DDR_BS00
RN10,6
```

U17,P2
U16,P2
)
(
FLASH_CEN
JP2,2
R103,2
U24,F2
)
(
N173247911
R127,2
U30,26
)
(
N17219413
C243,1
U43,G1
)
(
DDR_D14
U1,W6
RN27,6
)
(
N18505772
U71,2
U69,4
)
(
N1732512725
R128,2
U30,28
)
(
PCI_P_STOPN
U53,36
P4,A38
)
(
N17219565
U43,A8
TP15,1
)
(
BDDR_A2
RN7,2
U1,V13
)
(
B.CI2
U18,18
U20,2
R172,1
DC_P1,23
)
(
N1732512729
R124,1
U30,37
)
(
PCI_P_SERRN

U53,39
P4,B42
)
(
N17219516
U43,A5
C236,1
)
(
BDDR_A6
RN8,2
U1,W11
)
(
X00034
U19,1
R91,2
U69,2
R451,1
U20,1
)
(
N17323885
R192,1
U72,1
)
(
PCI_P_C/BEN0
U54,47
P4,A52
)
(
N17219446
R210,1
P13,2
L23,2
)
(
DDR_D12
U1,W5
RN27,5
)
(
B.CI1
U18,21
U19,5
RN5,2
DC_P1,25
)
(
N17324015
R126,1
RN32,16
U30,21
)
(
N17219411
U43,B6
C237,1
)
(
N18174683
RN6,8

```

U1,F18
)
(
B.CI0
U18,22
U19,2
R171,1
DC_P1,27
)
(
VLYNQ_TXD2
R335,2
J20,65
DC_P3,14
)
(
N17219505
C234,1
R196,1
C235,2
R195,2
)
(
COUT3
RN1,12
U20,10
DC_P1,49
)
(
B_DR1_AXR0[1]
U75,23
R219,1
U75,10
)
(
N18174681
RN6,7
U1,F15
)
(
B.CI3
U18,17
U20,5
RN5,3
DC_P1,21
)
(
N16964655
U51,31
RN5,10
)
(
N17219506
R195,1
P10,3
)
(
N18174679
RN6,6
U1,G15
)
(
N25600151

```

R158,2
U51,2
)
(
N17219453
L24,1
C247,2
)
(
N17480747
U1,C16
RN2,10
)
(
B.CI7
U18,4
U19,14
RN5,6
DC_P1,28
)
(
N16964573
RN5,9
U51,29
)
(
MDC
U1,C1
U54,15
)
(
N20694095
R356,2
U62,4
)
(
PCICLK
U1,A7
R158,1
)
(
SPDIF_ONN
U64,12
RN15,14
U62,2
U76,10
R20,2
)
(
B.CI6
U18,7
U19,11
RN5,5
DC_P1,26
)
(
VLYNQ_TXD0
C364,1
R307,2
J20,36
DC_P3,6
)
(

3V3_PWR_OK
R344,2
U59,4
TP32,1
C338,1
R295,1
R439,2
)
(
N18174677
RN6,5
U1,G16
)
(
DC_EM_CS2N
JP2,8
DC_P1,66
)
(
VLYNQ_RXD1
R331,2
R338,1
J20,22
DC_P3,7
)
(
N17219492
R202,1
C238,2
)
(
N21564630
U1,D13
RN14,15
)
(
NAND_CEN
JP2,6
R97,2
U23,C6
)
(
PCI_DETECTN
U51,1
U52,1
U12,1
U53,1
U54,1
R315,2
)
(
N17219490
L21,1
C240,2
)
(
N18174675
RN6,4
U1,G17
)
(
N24572978
U52,53

```
R307,1
)
(
N17219432
R215,2
U43,F9
R216,2
)
(
C_WEN
RN14,2
DC_P1,17
)
(
N24572910
U52,51
R308,1
)
(
EM_A05
U1,B8
U52,15
)
(
BDDR_D31
U17,E9
RN24,1
)
(
N24572842
U52,48
R329,1
)
(
EM_A06
U1,D8
U52,18
)
(
BDDR_DQS2
U17,J7
R83,1
)
(
N24572774
U52,46
R347,1
)
(
CI6
U1,A10
U51,18
)
(
BDDR_D25
U17,F2
RN26,2
)
(
N24572706
U52,44
R331,1
)
)
```

```
(
N18174673
RN6,3
U1,H17
)
(
N17244022
U10,1
R74,1
U66,2
U66,1
)
(
BDDR_D20
RN30,1
U17,L1
)
(
N24572638
U52,42
R332,1
)
(
LCD_OE
RN14,3
DC_P1,68
)
(
N17244084
DS1,cathode
U11,10
)
(
BDDR_D24
U17,F8
RN24,2
)
(
PCI_P_REQN
U51,50
P4,B18
)
(
EM_A10
U1,A9
U52,25
)
(
N17244052
RN37,6
U10,12
)
(
BDDR_D19
RN30,4
U17,L3
)
(
PCI_P_AD16
U52,47
P4,A32
)
(
```

```
EM_A11
U1,C10
U51,25
)
(
N17244029
RN37,1
U10,20
SW4,4
)
(
BDDR_D15
U16,E9
RN23,2
)
(
PCI_P_GNTN
U51,52
P4,A17
)
(
1V8_PWR_OK
R349,2
U60,4
C353,1
R304,1
R393,2
TP36,1
)
(
N18174671
RN6,2
U1,H16
)
(
N17244060
DS3,cathode
U11,12
)
(
BDDR_D1
U16,K2
RN29,4
)
(
PCI_P_AD23
U52,32
P4,B27
)
(
N17480753
U1,D12
RN2,16
)
(
N17244033
RN37,4
SW4,1
U10,16
)
(
N23789889
R79,1
```

U17,N9
)
(
N16965679
RN5,12
U51,37
)
(
N19238510
U57,4
C329,1
)
(
EM_A08
U1,B9
U52,21
)
(
N17244106
SW7,1
RN37,7
)
(
BDDR_D6
U16,J1
RN29,2
)
(
PCI_P_AD19
U52,41
P4,B30
)
(
N19165622
C314,2
U55,17
)
(
MDIO
U1,D1
U54,21
)
(
N17244028
U11,14
DS4,cathode
)
(
BDDR_D26
U17,G7
RN24,4
)
(
PCI_P_AD22
U52,34
P4,A28
)
(
N19206871
U58,5
R290,2
R289,2
)

```
(
N18174669
RN6,1
U1,H15
)
(
N17244068
U11,17
RN20,3
)
(
N23789888
R78,1
U16,N9
)
(
PCI_P_AD18
U52,43
P4,A31
)
(
N19165618
R275,1
U55,20
)
(
TXD3
U1,C5
U53,13
)
(
N17244036
RN37,2
U10,19
SW4,3
)
(
BDDR_D3
RN29,3
U16,L3
)
(
N16965173
RN5,13
U51,42
)
(
N19165173
R280,1
TP22,1
C323,1
L50,2
C324,1
)
(
CRS
U1,B5
U53,18
)
(
N17244037
SW4,8
RN38,5
```

```
)  
(  
BDDR_D18  
U17,L7  
RN28,3  
)  
(  
PCI_P_AD21  
U52,36  
P4,B29  
)  
(  
CORE_PWR_OK  
U55,4  
TP21,1  
C319,1  
R278,1  
R292,2  
R438,2  
)  
(  
EM_A12  
U1,D10  
U51,27  
)  
(  
N17244051  
U11,11  
DS2,cathode  
)  
(  
BDDR_D17  
RN30,3  
U17,K2  
)  
(  
N16965084  
RN5,14  
U51,46  
)  
(  
N19242202  
R283,2  
R282,2  
U56,5  
)  
(  
VID_CLK  
R268,1  
R46,2  
)  
(  
N17244062  
R7,2  
SW7,3  
)  
(  
BDDR_D23  
U17,J9  
RN28,1  
)  
(  
PCI_P_AD20
```

U52,39
P4,A29
)
(
N19165568
C318,1
U55,5
)
(
VSYNC
RN14,4
DC_P1,32
)
(
N17244021
SW4,7
RN38,6
)
(
BDDR_D16
U17,K8
RN28,2
)
(
N16964995
RN5,15
U51,48
)
(
N19165285
U55,10
U55,9
U55,8
U55,7
U55,6
C318,2
L50,1
)
(
AD28
U1,E11
U51,15
)
(
N17244020
RN37,5
U10,14
)
(
BDDR_D10
U16,G7
RN23,4
)
(
N19207542
U58,4
C331,1
)
(
EM_A00
RN2,6
R42,2
SW2,1


```
U22,A4
U24,D2
DC_P1,64
)
(
N17244170
U10,10
JP1,2
)
(
BDDR_D0
U16,K8
RN25,1
)
(
PCI_P_IRDYN
U52,54
P4,B35
)
(
N19240951
R287,2
R285,2
U57,5
)
(
AD26
U1,E10
U51,21
)
(
N17244023
SW4,2
RN37,3
U10,17
)
(
BDDR_D4
U16,L1
RN29,1
)
(
N16968607
U52,33
RN3,13
)
(
EM_A02_CLE
R167,1
R168,2
RN2,4
U23,D5
U22,B3
U24,A2
DC_P1,62
)
(
N17244030
RN38,8
SW4,5
)
(
BDDR_D12
```

```
U16,G1
RN27,4
)
(
PCI_P_AD27
U51,39
P4,B23
)
(
N19219702
U58,1
U57,3
R288,1
)
(
RXDV
U1,C4
U53,6
)
(
N17244109
U10,11
SW7,2
)
(
BDDR_D13
RN23,1
U16,G9
)
(
N16968539
U52,35
RN3,14
)
(
TXEN
U1,D3
U53,2
)
(
N17244034
RN38,7
SW4,6
)
(
BDDR_D21
U17,L9
RN28,4
)
(
N16968471
U52,37
RN3,15
)
(
EM_A01_ALE
R166,1
R165,2
RN2,5
U23,C4
U22,A5
U24,C2
DC_P1,61
```

```

)
(
N17244172
JP1,3
R17,2
)
(
BDDR_D8
RN23,3
U16,F8
)
(
PCI_P_FRAMEN
U52,52
P4,A34
)
(
N17944739
SW5,BB
SW5,B
R346,1
JP4,1
)
(
RXER
U1,B3
U54,2
)
(
N17244168
RN37,8
JP1,1
)
(
BDDR_D22
RN30,2
U17,J1
)
(
PCI_P_RSTN
U12,11
R181,1
U40,1
P4,A15
)
(
N19165582
R277,1
C317,2
)
(
N17480748
U1,B17
RN2,11
)
(
N172442141
R66,2
DS3,ANODE
)
(
BDDR_D7
U16,J9

```

```
RN25,3
)
(
PCI_P_PINTAN
U12,2
P4,A6
)
(
COL
U1,C6
U53,21
)
(
N172441911
DS4,ANODE
R67,2
)
(
BDDR_D14
RN27,3
U16,E1
)
(
PCI4.1V
U51,17
U52,17
C184,1
C186,1
U12,16
C178,1
C174,1
C176,1
D5,1
R68,1
U53,17
U54,17
)
(
N19165584
U55,3
C316,1
R276,1
)
(
EM_BA1
RN2,7
R175,2
SW2,3
U22,A3
U24,E2
DC_P1,65
)
(
N172443781
R65,2
DS2,ANODE
)
(
BDDR_D27
U17,G3
RN26,1
)
(
```

```
PCI_P_C/BEN2
U52,50
P4,B33
)
(
N19239727
U56,4
C327,1
)
(
YOUT3_BOOTMODE3
RN6,12
SW1,13
RN12,4
DC_P1,39
)
(
N172442640
DS1,ANODE
R64,2
)
(
BDDR_DQS0
R82,1
U16,J7
)
(
VLYNQ_CLOCK
R333,2
R337,1
J20,16
DC_P3,2
)
(
N19165644
C313,1
R276,2
)
(
VD
R313,2
U45,23
DC_P1,19
)
(
BDDR_D28
U17,G1
RN26,4
)
(
VLYNQ_RXD0
R347,2
R339,1
J20,21
DC_P3,5
)
(
SPDIF_OUT
R356,1
U49,2
U46,2
)
(
```

N19165293
U55,16
U55,14
U55,15
L49,2
C322,1
C321,1
)
(
PCLK
R312,2
R348,1
U48,4
)
(
BDDR_DQS3
U17,E7
R81,1
)
(
N16968743
U52,29
RN3,11
)
(
RXD0
U1,C3
U54,4
)
(
BDDR_D30
U17,E1
RN26,3
)
(
N16968675
U52,31
RN3,12
)
(
N17181437
DC_P3,12
R267,2
R266,1
U15,4
)
(
AD17
U1,A8
U52,11
)
(
BDDR_D5
U16,L9
RN25,4
)
(
N25601974
R333,1
U51,53
)
(
RXD1

```
U1,B2
U54,11
)
(
BDDR_DQS1
R80,1
U16,E7
)
(
N16968403
U52,40
RN3,16
)
(
PTRDYN
U1,D6
U53,27
)
(
BDDR_D11
RN27,2
U16,G3
)
(
PCI_P_CLK
U51,54
P4,B16
)
(
N22459220
R443,1
R442,2
U105,3
)
(
RXD2
U1,D2
U54,9
)
(
BDDR_D29
U17,G9
RN24,3
)
(
PCI_P_C/BEN3
U51,30
P4,B26
)
(
N22457177
R441,1
R446,2
U104,3
)
(
N19187390
R313,1
U1,A13
)
(
BDDR_D2
U16,L7
```

```
RN25,2
)
(
VLYNQ_SCRUN
R332,2
J20,24
DC_P3,1
)
(
N18479791
L46,2
C308,1
J4,1
)
(
TXCLK
U1,A4
U53,4
)
(
BDDR_D9
U16,F2
RN27,1
)
(
PCI_P_AD24
U51,32
P4,A25
)
(
N18479734
L40,2
C301,1
J2,1
)
(
N19185627
R312,1
U1,A14
)
(
I2C.IOP4
U64,16
RN15,12
)
(
PCI_P_AD25
U51,34
P4,B24
)
(
N18479905
R243,1
U103,4
)
(
TXD0
U1,D4
U53,11
)
(
I2C.IOP3
U64,14
```



```
RN15,13
)
(
VLYNQ_RXD2
R329,2
J20,59
DC_P3,13
)
(
DAC_3V3
C294,1
L35,2
C292,2
C293,2
C404,2
U106,6
C403,2
U105,6
C402,2
U104,6
C401,2
U103,6
U103,5
U105,5
U106,5
U104,5
)
(
CI0
U1,C12
U12,4
)
(
PCI_P_AD26
U51,36
P4,A23
)
(
N18479787
L46,1
C307,1
R255,2
)
(
CI4
U1,C11
U51,4
)
(
USER_I2C_IO.A1P0
RN34,8
U13,10
DC_P2,87
)
(
VLYNQ_RXD3
R308,2
J20,61
DC_P3,15
)
(
N22461273
R448,1
```

```
R447,2
U106,3
)
(
RESET_OUT
U32,4
U44,1
)
(
RXD3
U1,C2
U54,13
)
(
N16964908
RN5,16
U51,51
)
(
N22445166
R445,1
R444,2
U103,3
)
(
TXD1
U1,B4
U53,9
)
(
PCI_P_AD28
U51,41
P4,A22
)
(
N18479768
L41,2
C304,1
J3,1
)
(
N18174540
R268,2
U1,G19
R184,2
R183,2
)
(
X00265
R252,2
SW3,2
R451,2
)
(
N19378044
C362,1
R338,2
)
(
N18479911
R245,1
U104,4
)
```

```
(
TXD2
U1,D5
U53,15
)
(
PCI_P_AD29
U51,43
P4,B21
)
(
N18479921
R250,1
U105,4
)
(
EM_BA0
RN2,8
R43,2
SW2,2
U22,H6
R75,1
R73,2
R153,1
DC_P1,63
)
(
USER_I2C_IO.A1P2
RN34,6
U13,12
DC_P2,85
)
(
PCI_P_IDSEL
U52,30
P4,A26
)
(
N18479701
L40,1
C300,1
P1,3
R245,2
)
(
PIDRDYN
U1,A6
U52,2
)
(
PCI_P_AD30
U51,45
P4,A20
)
(
N18479766
L41,1
C303,1
P1,4
R250,2
)
(
EM_CS2
```

```
RN14,1
JP2,7
JP2,5
JP2,3
JP2,1
)
(
N18479720
L37,2
C297,1
J1,1
)
(
AD16
U1,D7
U52,9
)
(
USER_I2C_IO.A1P3
RN34,5
U13,14
DC_P2,86
)
(
PCI_P_AD31
U51,47
P4,B20
)
(
N18479718
L37,1
C296,1
R243,2
)
(
PCBE2N
U1,B7
U52,6
)
(
USER_I2C_IO.A1P1
RN34,7
U13,11
DC_P2,88
)
(
N24560709
C363,1
R339,2
)
(
N18480674
R255,1
U106,4
)
(
N17480752
U1,A17
RN2,15
)
(
PCI_P_AD17
U52,45
```

```
P4,B32
)
(
GND_DAC
C403,1
C401,1
C404,1
C402,1
C296,2
C300,2
C301,2
C293,1
C303,2
C304,2
C307,2
C308,2
C292,1
C294,2
J3,2
J4,2
J2,2
P1,1
P1,6
U106,2
U105,2
U104,2
U103,2
R445,2
R441,2
R443,2
R448,2
L14,2
J3,3
J2,3
J4,3
P1,5
P1,2
C297,2
J1,2
J1,3
)
(
RXCLK
U1,A3
U54,6
)
(
I2C.IOP5
U64,17
RN15,11
)
(
N17383445
R360,1
U31,5
C150,1
R361,1
)
(
N16964737
U51,33
RN5,11
)
```

```
(  
N28750  
U50,17  
C266,1  
)  
(  
N17383368  
R138,1  
U31,8  
)  
(  
N3235851  
C361,1  
R337,2  
)  
(  
N52983  
U50,1  
C259,1  
)  
(  
N17480750  
U1,B16  
RN2,13  
)  
(  
N17383297  
U31,3  
C146,1  
R363,1  
R362,2  
)  
(  
PCI_P_AD15  
U53,45  
P4,A44  
)  
(  
N41090  
U50,7  
C264,1  
)  
(  
PPERIN  
U1,A5  
U53,23  
)  
(  
N17383292  
U31,6  
R342,1  
T1,4  
)  
(  
PCI_P_AD14  
U53,47  
P4,B45  
)  
(  
N16928953  
U50,18  
C267,1  
)
```

```
(
EM_A09
U1,D9
U52,27
)
(
USER_I2C_IO.A0P7
U11,20
RN20,1
DC_P2,82
)
(
N17383340
U31,4
R256,2
)
(
PCI_P_AD13
U53,50
P4,A46
)
(
3V3A_DEC
U50,5
U50,4
U50,21
U50,20
C284,1
C287,1
C286,1
C285,1
L27,2
)
(
CI7
U1,B10
U51,23
)
(
N17383477
T1,3
R343,1
U31,7
)
(
PCI_P_AD12
U53,52
P4,B47
)
(
N33254
L33,1
C271,1
J5,1
)
(
CI5
U1,B11
U51,13
)
(
N17383364
R140,2
```

```
C151,1
R141,2
)
(
PCI_P_AD11
U53,54
P4,A47
)
(
N84828
R230,1
R232,2
U50,37
TP58,1
)
(
YI0
RN4,8
U45,2
DC_P1,13
)
(
PCI_P_AD10
U54,54
P4,B48
)
(
N16794371
U50,23
C268,1
)
(
CI2
U1,D11
U12,7
)
(
PCI_P_AD9
U54,52
P4,A49
)
(
3V3D_DEC
R230,2
R237,2
R231,2
U50,61
U50,48
U50,38
C275,1
C277,1
C276,1
L25,2
)
(
N21164298
U1,B15
RN4,10
)
(
PCI_P_AD8
U54,50
P4,B52
```



```
)  
(  
N86936  
U50,30  
TP20,1  
)  
(  
CI3  
U1,A11  
U51,9  
)  
(  
PCI_P_AD7  
U54,45  
P4,B53  
)  
(  
N83880  
U50,51  
RN33,7  
)  
(  
N17480749  
U1,A16  
RN2,12  
)  
(  
VLYNQ_RESET  
RN20,4  
U11,16  
U67,1  
U67,2  
U15,1  
U15,2  
)  
(  
PCI_P_AD6  
U54,43  
P4,A54  
)  
(  
N83878  
U50,50  
RN33,6  
)  
(  
N21163437  
U1,D17  
RN1,6  
)  
(  
PCI_P_AD5  
U54,41  
P4,B55  
)  
(  
N83882  
U50,52  
RN33,8  
)  
(  
N21164295  
U1,C15
```

```
RN4,9
)
(
PCI_P_AD4
U54,39
P4,A55
)
(
N82097
R237,1
R240,2
U50,33
)
(
COUT7
RN1,16
U19,3
DC_P1,56
)
(
PCI_P_AD3
U54,36
P4,B56
)
(
N31997
R239,2
L34,2
C273,1
C270,1
)
(
N21164292
U1,C14
RN4,11
)
(
USER_I2C_IO.A0P6
U11,19
RN20,2
DC_P2,81
)
(
PCI_P_AD2
U54,34
P4,A57
)
(
N67649
Y4,1
C274,1
R241,2
)
(
N21163446
U1,D16
RN1,8
)
(
PCI_P_AD1
U54,32
P4,B58
)
```

```

(
N20878069
U50,9
C260,2
)
(
N21164289
U1,C13
RN4,14
)
(
PCI_P_AD0
U54,30
P4,A58
)
(
N35582
C255,1
R229,2
L30,2
C257,1
)
(
N21164286
U1,B14
RN4,12
)
(
UART_RXD
U33,7
R257,1
)
(
N20871879
U50,8
C270,2
)
(
N21164283
U1,B13
RN4,15
)
(
N16761479
U34,13
C155,1
L12,1
)
(
N35578
C256,1
L29,1
P2,4
)
(
READ_OE
RN14,6
U23,D4
U22,A2
U24,G2
DC_P1,80
)
(

```

```
N16751846
U34,4
C159,2
)
(
N33364
L34,1
L33,2
C272,1
)
(
N20091071
U1,E14
RN14,10
)
(
RS232_ENABLEN
R187,1
U33,15
R144,2
DC_P2,15
)
(
N35010
R233,2
C260,1
L32,2
C263,1
)
(
VCLK
R27,2
DC_P1,31
)
(
N16755738
U34,12
R143,2
)
(
N28888
U50,16
C265,1
)
(
EM_A07
U1,C9
U52,23
)
(
N18782118
U34,9
R257,2
)
(
1V8A_DEC
U50,25
U50,14
U50,76
U50,12
U50,11
U50,78
C278,1
```

C283,1
C282,1
C281,1
C280,1
C279,1
L26,2
)
(
N16757136
R145,2
U34,1
)
(
N310913
R241,1
R238,2
U50,75
)
(
N21164226
U1,D14
RN4,13
)
(
N16766818
P8,8
P8,7
)
(
N35018
C261,1
L31,1
P2,3
)
(
C_FIELD
RN2,1
U18,3
DC_P1,18
)
(
N16765298
L12,2
C156,1
P8,3
)
(
N66879
C269,1
Y4,2
R238,1
U50,74
)
(
N20091069
U1,D15
RN14,11
)
(
N16841390
C158,1
L13,2
P8,2

```
)  
(  
N101120  
U50,73  
R235,2  
)  
(  
HSYNC  
RN14,5  
DC_P1,36  
)  
(  
N16755387  
U34,16  
R142,2  
)  
(  
N97449  
U50,72  
R234,2  
)  
(  
AD30  
U1,E12  
U51,11  
)  
(  
UART_TXD  
U34,11  
U33,4  
)  
(  
N96139  
U50,40  
R236,2  
)  
(  
YOUT7  
RN6,16  
DC_P1,46  
)  
(  
N16752183  
U34,6  
C160,2  
)  
(  
N17166584  
U12,6  
R172,2  
)  
(  
GND_DENC  
R233,1  
R229,1  
C264,2  
C266,2  
C267,2  
C265,2  
C259,2  
C258,2  
C268,2  
R239,1
```

U50,79
U50,26
U50,6
U50,3
U50,15
U50,10
U50,22
U50,19
C273,2
C272,2
C271,2
C263,2
C262,2
C261,2
C257,2
C254,2
C256,2
U50,77
U50,13
C278,2
C282,2
C281,2
C280,2
C279,2
C284,2
C286,2
C285,2
C287,2
C283,2
P2,1
L15,1
P2,2
P2,5
U50,24
P2,6
J5,2
)
(
N16914581
U6,1
Y2,1
C78,2
)
(
N16751511
U34,2
C159,1
)
(
N17166382
U12,5
R181,2
U40,4
)
(
N53177
U50,80
C258,1
)
(
YOUT6
RN6,15
R364,1

```
DC_P1,44
)
(
N16915416
Y2,2
U6,8
C79,2
)
(
N16764087
L13,1
C157,1
U34,8
)
(
N17166396
U12,9
U36,2
R170,1
)
(
N87791
U50,71
R242,2
)
(
YOUT5
RN6,14
R258,1
DC_P1,42
)
(
PLL_PCLK
R59,1
R265,2
)
(
N16793020
U33,1
R146,2
R147,1
)
(
N17166377
U12,3
R171,2
)
(
1V8D_DEC
U50,55
U50,41
U50,67
U50,31
C288,1
C291,1
C290,1
C289,1
L28,2
)
(
N21164365
U1,A12
RN4,16
```



```
)  
(  
N16920284  
R49,1  
R48,1  
Q1,COLLECTOR  
)  
(  
N16754287  
C160,1  
U34,5  
)  
(  
N17166652  
R177,1  
U36,4  
)  
(  
N45159  
U50,2  
C255,2  
)  
(  
EM_A03  
RN2,3  
U22,B4  
U24,B2  
DC_P1,59  
)  
(  
N20292335  
R59,2  
U7,10  
R350,1  
)  
(  
N16753221  
U34,3  
C162,1  
)  
(  
N83876  
U50,47  
RN33,5  
)  
(  
YOUT4_FASTBOOT  
RN6,13  
SW1,12  
RN12,5  
DC_P1,40  
)  
(  
N20281309  
R350,2  
R60,2  
C370,2  
U6,5  
)  
(  
N16752878  
U34,7  
C161,1
```

```

)
(
N83874
U50,46
RN33,4
)
(
N19183863
R311,1
U1,A15
)
(
PLL_FS1
U7,5
RN22,2
U13,19
)
(
CAN_ENABLEN
R328,2
R188,1
U78,1
U77,1
DC_P2,26
)
(
N83872
U50,45
RN33,3
)
(
YI6
RN4,2
U45,16
DC_P1,12
)
(
N16960376
R57,1
U7,3
)
(
N17233824
U64,10
RN15,16
U42,1
U42,2
)
(
CAN_D
U31,1
R139,1
U77,4
)
(
N90096
TP19,1
U50,36
)
(
N17480771
U1,C19
RN14,16

```

```
)  
(  
N16918886  
U6,3  
TP59,1  
C87,1  
R49,2  
)  
(  
I2C.IOP6  
U64,19  
RN15,10  
)  
(  
CAN_R  
U78,4  
R256,1  
)  
(  
N83870  
U50,44  
RN33,2  
)  
(  
PFRAMEN  
U1,C7  
U52,4  
)  
(  
PLL_FS2  
U7,6  
RN22,3  
U13,17  
)  
(  
N83868  
U50,43  
RN33,1  
)  
(  
COUT6  
RN1,15  
U19,6  
DC_P1,54  
)  
(  
N16959863  
R56,1  
U7,2  
)  
(  
N17233809  
U64,11  
RN15,15  
U74,2  
)  
(  
N35574  
C254,1  
L30,1  
L29,2  
)  
(
```

YOUT2_BOOTMODE2

RN6,11

RN12,3

SW1,14

DC_P1,41

)

(

PLL_SR

U7,7

U13,16

RN22,4

)

(

N35022

C262,1

L31,2

L32,1

)

(

N17182377

C315,2

U55,18

R349,1

R344,1

)

(

YI4

RN4,4

U45,10

DC_P1,8

)

(

N16959352

R55,1

U7,18

)

(

N20736291

U73,2

U42,4

)

(

B_CANL

D2,1

R140,1

C149,1

R342,2

P7,2

TP14,1

T1,1

)

(

YOUT1_BOOTMODE1

RN6,10

SW1,15

RN12,2

DC_P1,43

)

(

N17100560

R47,1

U5,1

)

```

(
TVP5146_PCLK
U48,2
R236,1
)
(
N21163371
U1,F17
RN1,1
)
(
N16923512
R52,1
C88,1
R437,2
R51,2
)
(
TVP5146_VS
U45,22
R235,1
)
(
YI2
RN4,6
U45,6
DC_P1,9
)
(
AUDIO_CLK
R54,2
R209,2
R270,2
DC_P2,27
)
(
B_CANH
TP13,1
C148,2
D1,2
R343,2
T1,2
R141,1
P7,7
)
(
TVP5146_HS
U45,21
R231,1
R234,1
)
(
N21163434
U1,E16
RN1,5
)
(
N16922189
C88,2
R52,2
Q1,BASE
)
(

```

```

GND_E_CAN
P7,B
P7,A
C158,2
C157,2
P8,A
C155,2
C156,2
P8,B
L57,2
L58,2
)
(
DC3_PCLK
R348,2
DC_P1,3
R265,1
)
(
N17480751
U1,B18
RN2,14
)
(
N16964438
U8,2
R55,2
R56,2
R57,2
R53,2
)
(
N23789769
R224,2
R225,2
J10,1
)
(
EMU_SYS_RESETN
Embedded_USB_1,PONRSn
R314,1
)
(
N21163431
U1,E18
RN1,4
)
(
N20405305
R62,1
U9,4
)
(
TVP5146_ENABLEN
U45,1
U48,1
U45,13
R186,1
DC_P1,5
)
(
EM_A04
RN2,2

```

R176,2
SW2,4
U22,C3
U24,D3
DC_P1,60
)
(
N16953922
R50,1
U7,14
)
(
N23789792
P14,3
U49,4
)
(
N17187821
U57,1
U56,3
R284,1
R346,2
R345,1
C368,1
R314,2
D10,1
D10,3
)
(
N21163443
U1,F16
RN1,2
)
(
N17101823
R58,1
U8,1
)
(
VDD_1V8PLL
L9,2
C140,2
U30,47
C141,2
)
(
TVP5146YIN4
U45,11
RN33,13
)
(
COUT4
RN1,13
U20,6
DC_P1,50
)
(
N16958843
R53,1
U7,19
)
(
TVP5146YIN3

```
U45,8
RN33,12
)
(
COUT1
RN1,10
U19,10
DC_P1,53
)
(
X00483
U9,1
R324,1
R189,2
DC_P2,7
)
(
TVP5146YIN2
U45,7
RN33,11
)
(
N17480746
U1,C17
RN2,9
)
(
N16955436
U5,2
R50,2
)
(
N23789743
R224,1
C250,2
)
(
N21163449
U1,D18
RN1,7
)
(
N20406003
U9,2
R60,1
)
(
N17236413
SW3,3
R161,1
)
(
TVP5146YIN1
U45,4
RN33,10
)
(
YI7
RN4,1
U45,19
DC_P1,14
)
(
```



```
PLL_CSEL
U7,12
U13,20
RN22,1
)
(
TVP5146YIN0
U45,3
RN33,9
)
(
COUT5
RN1,14
U20,3
DC_P1,52
)
(
N167859310
R54,1
U8,4
)
(
N23789733
C250,1
U46,4
)
(
YOUT0_BOOTMODE0
RN6,9
RN12,1
SW1,16
DC_P1,45
)
(
N167856730
U5,4
R46,1
)
(
GND_E_ENET
P3,13
L55,2
L56,2
P3,14
P3,8
C142,1
)
(
TVP5146YIN7
U45,18
RN33,16
)
(
N21163440
U1,E17
RN1,3
)
(
TVP5146YIN6
U45,17
RN33,15
)
(
```

```
VPBECLK
R184,1
DC_P1,35
)
(
N17599875
SW2,5
R174,2
)
(
TVP5146YIN5
U45,14
RN33,14
)
(
YI5
RN4,3
U45,15
DC_P1,10
)
(
N17599895
SW2,8
R173,2
)
(
N3132944
R228,1
U49,1
)
(
COUT2
RN1,11
U20,13
DC_P1,51
)
(
N25433340
SW2,7
R44,2
)
(
N3133164
U46,1
R226,1
)
(
YI3
RN4,5
U45,9
DC_P1,7
)
(
N25433334
SW2,6
R45,2
)
(
HD
R311,2
U45,20
DC_P1,20
)
```

```

(
N25433320
RN13,12
SW1,5
)
(
WRITE_WE
RN14,7
U23,C7
U22,G5
U24,A5
DC_P1,79
)
(
N25433314
RN13,15
SW1,2
)
(
N17419945
U1,F19
RN14,12
)
(
N25433316
RN13,14
SW1,3
)
(
WAIT_BSYN
U1,E15
U23,C8
R96,2
DC_P1,67
)
(
N19095252
RN13,9
SW1,8
)
(
EPHY.LED0
R129,2
P3,12
R127,1
R375,1
)
(
YI1
RN4,7
U45,5
DC_P1,11
)
(
N19073765
RN12,7
SW1,9
)
(
CI1
U1,B12
U51,6
)

```

```
(
N25433288
RN13,16
SW1,1
)
(
N17480773
U1,E19
RN14,13
)
(
EPHY.LED2
R130,2
R373,1
P3,10
R128,1
)
(
N17480772
U1,C18
RN14,14
)
(
N25433318
RN13,13
SW1,4
)
(
VDD_1V8RX
L11,2
C131,2
U30,31
C132,2
)
(
PDEVSELN
U1,B6
U53,25
)
(
DVDD_3V3
C25,1
C30,1
C35,1
C34,1
C37,1
C39,1
C32,1
C31,1
C29,1
C24,1
C23,1
C36,1
C33,1
C26,1
C28,1
C22,1
C27,1
U1,K15
U1,K17
U1,L6
U1,M15
U1,M5
```

U1,N6
U1,P1
U1,A1
U1,A18
U1,A2
U1,E6
U1,E8
U1,F11
U1,F13
U1,F5
U1,F7
U1,F9
U1,G14
U1,G6
U1,H18
U1,H5
U1,J1
U1,J14
U1,J16
U1,J6
U1,G12
U1,G10
U1,G8
TP43,1
R317,2
C19,1
C20,1
C21,1
)
(
GND_AIC
R211,2
R210,2
U43,F2
C242,2
R203,2
C236,2
U43,H2
U43,G2
P11,1
C241,2
R197,2
C234,2
P10,1
C237,2
C229,2
U43,D2
C228,2
C239,2
R204,2
P13,1
L16,2
C226,2
C244,2
R196,2
C227,2
C224,2
R194,2
P12,1
P11,4
U43,E2
C230,2
U43,C2

```
)  
(  
AD18  
U1,C8  
U52,13  
)  
(  
N19648463  
C72,1  
C71,1  
C73,1  
TP48,1  
U1,L16  
R320,1  
)  
(  
PHY_1V8  
C130,2  
C133,1  
L9,1  
L11,1  
U30,13  
)  
(  
COUT0  
RN1,9  
U19,13  
DC_P1,55  
)  
(  
DVDD_1V8  
C65,1  
C63,1  
C64,1  
C66,1  
C61,1  
C60,1  
C67,1  
C62,1  
C70,1  
C57,1  
U1,P9  
U1,L14  
U1,P5  
U1,P7  
U1,L18  
U1,P11  
U1,P13  
U1,R12  
U1,R10  
C58,1  
C69,1  
C59,1  
TP46,1  
R319,2  
U1,W19  
U1,R14  
U1,R16  
U1,R4  
U1,R6  
U1,R8  
U1,T5  
U1,V1
```

```

U1,W18
C188,1
)
(
VDD_3V3A
P3,5
P3,4
R110,2
R109,2
R115,1
R117,1
P3,11
P3,9
L8,2
C143,1
C134,1
U30,38
C136,2
C137,2
)
(
URXD0
U1,L2
U33,5
DC_P2,17
)
(
N20372430
TP49,1
R320,2
L6,2
)
(
I2C_DATA
R15,1
TP41,1
U1,M3
U10,4
U11,4
U13,4
U64,4
U25,5
U43,D8
U50,29
DC_P2,38
)
(
N19716639
TP54,1
R323,1
L5,2
)
(
I2C_INT_ENABLEN
U3,1
R251,2
R18,1
DC_P1,90
)
(
DAC_IOUT_A
U1,P19
U103,1

```

```

)
(
TINP0L
R435,2
R62,2
DC_P2,9
)
(
N19714769
R322,1
TP52,1
L4,2
)
(
N22914378
U1,K2
R435,1
)
(
VREF_0.5V
U1,N19
R37,1
R39,1
C11,1
)
(
TINP1L
TP61,1
U1,L4
U78,2
U33,6
DC_P2,23
)
(
N19716637
U1,P17
C16,1
C17,1
C18,1
TP55,1
R323,2
)
(
CLKX0
R434,2
U65,6
U75,17
DC_P2,34
)
(
DAC_IOUT_D
U1,N17
U106,1
)
(
N22912558
U1,H1
R434,1
)
(
DAC_RBIAS
U1,N16
R444,1

```



```
R446,1
R442,1
R447,1
)
(
FSX0
R433,2
U65,1
DC_P2,30
)
(
DSP_CORE_VDD
L4,1
C42,1
C53,1
C47,1
C56,1
C55,1
C44,1
C43,1
C45,1
C46,1
C52,1
C48,1
C41,1
C49,1
C50,1
C54,1
C51,1
U1,K13
U1,J8
U1,J10
U1,J12
U1,H7
U1,H9
U1,H11
U1,H13
U1,N12
U1,N10
U1,K11
U1,K9
U1,K7
U1,L12
U1,L10
U1,L8
U1,M13
U1,M11
U1,M9
U1,M7
U1,N8
C189,1
C190,1
R279,2
TP23,1
R277,2
C325,1
R280,2
R289,1
)
(
B.CRS
U27,11
U53,37
```

```
DC_P2,61
)
(
N22908975
U1,H3
R433,1
)
(
N19884541
R41,1
U4,4
R38,2
)
(
B.COL
U27,8
U53,35
DC_P2,58
)
(
FSX1
R432,2
U75,7
DC_P2,42
)
(
N19865612
C12,1
R37,2
U4,3
R38,1
R36,1
)
(
N22903692
U1,F2
R432,1
)
(
N19714767
U1,N15
C14,1
C13,1
C15,1
TP53,1
R322,2
)
(
EPHY.TX_EN
U26,12
U30,16
)
(
TOUT1L
U1,K4
TP64,1
U77,2
U33,3
DC_P2,24
)
(
DAC_IOUT_C
U1,N18
```

```

U105,1
)
(
EPHY.TXD1
U26,9
U30,18
)
(
CLKX1
R429,2
U75,3
U75,14
DC_P2,45
)
(
DAC_IOUT_B
U1,P18
U104,1
)
(
CLKR1
R431,2
U75,4
DC_P2,44
)
(
B.GP[03]
U54,37
DC_P2,14
)
(
N22899983
U1,F1
R429,1
)
(
EPHY.TXD0
U26,7
U30,17
)
(
B.GP[01]
U54,31
DC_P2,12
)
(
N22928325
U1,G1
R431,1
)
(
DDR_D8
U1,V4
RN23,6
)
(
EPHY.TXCLK
U26,4
R111,1
)
(
B.GP[00]
U54,29

```

```

DC_P2,11
)
(
DX1
R430,2
U75,8
U75,18
DC_P2,46
)
(
DDR_CLK
R31,2
U16,M8
U17,M8
)
(
EPHY.MDC
U27,19
U30,2
)
(
B.GP[02]
U54,33
DC_P2,13
)
(
N22924752
U1,G2
R430,1
)
(
DDR_CS
R29,2
U16,P8
U17,P8
)
(
TOUT0L
R436,2
DC_P2,10
)
(
DDR_D6
U1,V3
RN29,7
)
(
ENET_ENABLEN
R367,2
U44,2
R246,2
R326,1
DC_P2,74
)
(
N22916141
U1,J4
R436,1
)
(
BDDR_A12
U1,w9
RN10,4

```

```
)  
(  
N24410968  
R73,1  
U24,G7  
)  
(  
EPHY.MDIO  
U27,20  
U30,1  
R133,1  
)  
(  
GIO001  
U1,E2  
U54,25  
)  
(  
DDR_D27  
U1,W17  
RN26,8  
)  
(  
EPHY.RXD3  
U27,16  
RN32,6  
)  
(  
PWM1  
U1,F3  
U3,4  
R51,1  
TP69,1  
DC_P1,88  
)  
(  
BDDR_A11  
RN9,1  
U1,W10  
)  
(  
DR0  
U1,H4  
U65,2  
U75,21  
DC_P2,31  
)  
(  
VREF_STL  
U1,T15  
C9,1  
R77,1  
R76,2  
C111,1  
C5,2  
C112,1  
U17,M2  
C113,1  
U16,M2  
)  
(  
EPHY.RXD2  
U27,15
```

```

RN32,5
)
(
GIO000
U1,E1
U54,27
)
(
DDR_D31
U1,T19
RN24,8
)
(
EPHY.CRS
U27,10
RN32,2
)
(
UTXD0
U1,K3
U33,2
DC_P2,19
)
(
N23788706
R34,1
U1,T13
)
(
EPHY.COL
U27,9
RN32,1
)
(
GIO003
U1,E4
U54,18
)
(
N18668482
U1,D19
R27,1
)
(
DDR_CKE
RN11,1
U16,N2
U17,N2
)
(
AIC33_ENABLEN
U74,1
U73,1
R185,1
DC_P2,39
)
(
EPHY.RX_CLK
U27,6
R116,2
R119,1
R132,1
)

```

```

(
I2C_INT
U3,2
U66,4
)
(
DDR_CAS
RN11,4
U16,P7
U17,P7
)
(
EPHY.TXD3
U27,5
U30,20
R113,2
)
(
N17481957
R14,2
U1,M1
)
(
DDR_D28
U1,U18
RN26,5
)
(
B.RXD0
U28,2
U54,51
DC_P2,54
)
(
I2C_CLK
R16,1
TP40,1
U1,M2
U11,2
U10,2
U13,2
U64,2
U25,6
U43,C8
U50,28
DC_P2,37
)
(
BDDR_A10
RN9,2
U1,U10
)
(
EPHY.TXD2
U27,2
U30,19
R112,2
)
(
DR1
U1,G3
U75,11
DC_P2,43

```

```
)  
(  
DDR_D26  
U1,T18  
RN24,5  
)  
(  
B.TXEN  
U26,14  
U53,53  
DC_P2,73  
)  
(  
N16903746  
U26,3  
RN35,1  
)  
(  
CLK_OUT  
R14,1  
R183,1  
R437,1  
TP70,1  
DC_P1,84  
)  
(  
N26201  
R35,1  
TP4,1  
C8,1  
U1,T12  
C10,1  
)  
(  
EPHY.RX_ER  
U28,12  
RN32,8  
)  
(  
N16903742  
U26,6  
RN35,3  
)  
(  
FSR1  
U1,H2  
U76,2  
U76,5  
U76,9  
DC_P2,41  
)  
(  
DDR_D25  
U1,U17  
RN26,7  
)  
(  
N16903744  
U26,10  
RN35,4  
)  
(  
B.TXD1
```



```
U26,11
U53,46
DC_P2,71
)
(
FSR0
U1,G4
DC_P2,29
)
(
BDDR_DQM2
R87,2
U1,T14
)
(
EPHY.RX_DV
U28,9
RN32,7
)
(
N16903740
U26,13
RN35,2
)
(
UCTS0
U1,L1
DC_P2,20
)
(
BDDR_A9
RN9,3
U1,U11
)
(
B.TXD0
U26,5
U53,44
DC_P2,72
)
(
N16904149
U28,3
RN36,2
)
(
URTS0
U1,L3
DC_P2,18
)
(
DDR_D17
U1,V14
RN30,6
)
(
N16904147
U28,6
RN36,3
)
(
B.TXCLK
U26,2
```

```
U53,51
DC_P2,78
)
(
GIO002
U1,E3
U54,23
)
(
BDDR_DQM1
R84,1
U1,T6
)
(
N16904145
U28,13
RN36,1
)
(
DX0
U1,J3
U65,5
U75,22
DC_P2,32
)
(
DDR_D23
U1,T17
RN28,8
)
(
N16904123
U28,10
RN36,4
)
(
EPHY.RXD1
U28,7
RN32,4
)
(
CLKR0
U1,J2
DC_P2,33
R270,1
)
(
DDR_DQS3
U1,U16
R81,2
)
(
EPHY.RXD0
U28,4
RN32,3
)
(
VCC_5V
Embedded_USB_1,5V
R363,2
R68,2
P4,B62
P4,B61
```

P4,A62
P4,A61
P4,A8
P4,A5
P4,B6
P4,B5
C204,1
C203,1
J20,103
C371,1
C405,1
C372,1
J20,97
DC_P1,100
DC_P1,99
DC_P2,3
DC_P2,4
DC_P3,19
DC_P3,20
R278,2
C320,1
L49,1
R299,1
TP31,1
D3,Cathode
F1,2
R316,2
C341,1
R295,2
L51,1
C340,1
R304,2
C354,1
L53,1
)
(
BDDR_A8
RN9,4
U1,V10
)
(
ALT_AIC33_CLK
Embedded_USB_1,CLK_12MHZ
R182,1
)
(
DDR_RAS
RN11,3
U16,N7
U17,N7
)
(
N16846358
U27,1
U27,13
U14,4
)
(
T_TDI
Embedded_USB_1,T_TDI
U41,6
R152,2
)

```
(
DDR_D22
U1,V16
RN30,7
)
(
T_EMU1
Embedded_USB_1,T_EMU1
U539,6
)
(
DDR_DQS2
U1,U14
R83,2
)
(
B.TXD2
U27,3
U53,40
DC_P2,70
)
(
XDS_TMS
U41,14
J6,1
)
(
BDDR_CLK_N
R30,1
U1,W8
)
(
B.MDIO
U27,21
U54,35
DC_P2,66
)
(
XDS_TDI
U41,5
J6,3
)
(
BDDR_A0
RN7,4
U1,W13
)
(
B.RXER
U28,14
U54,53
DC_P2,52
)
(
N17368035
C336,1
R297,1
C335,2
C332,2
U59,2
R291,2
)
(
```

```
N23909156
R8,2
U35,4
)
(
BDDR_CLK
R31,1
U1,W7
)
(
B.RXDV
U28,11
U53,48
DC_P2,59
)
(
N17368077
R294,1
U59,20
)
(
N23909112
C169,1
R148,2
)
(
BDDR_CS
R29,1
U1,T9
)
(
B.MDC
U27,18
U54,40
DC_P2,65
)
(
N17368935
C349,2
U60,18
R438,1
R439,1
)
(
DSP_EMU0
U539,4
U1,P2
R13,2
R12,1
)
(
DDR_D21
U1,W16
RN28,5
)
(
B.RXD1
U28,5
U54,44
DC_P2,53
)
(
N17367788
```

```
C334,2
U59,18
R393,1
R292,1
)
(
T_TDO
Embedded_USB_1,T_TDO
U41,3
)
(
DDR_A9
RN9,6
U17,U3
U16,U3
)
(
B.RXD3
U27,17
U54,42
DC_P2,55
)
(
N17096513
U28,15
U26,15
U14,1
R367,1
U44,4
)
(
N17368795
U60,3
C350,1
R302,1
)
(
DSP_EMU1
U539,7
U1,N2
R11,2
R10,1
)
(
DDR_D13
U1,V6
RN23,8
)
(
B.RXD2
U27,14
U54,46
DC_P2,56
)
(
N17368143
R306,1
TP37,1
C358,1
L54,2
C357,1
)
(
```

```
XDS_EMU0
U539,2
J6,13
)
(
DDR_A8
RN9,5
U17,U8
U16,U8
)
(
B.RXCLK
U27,7
U54,48
DC_P2,62
)
(
N17368903
R301,1
U60,20
)
(
N23880322
U539,12
R9,1
)
(
DDR_D11
U1,V5
RN27,7
)
(
B.TXD3
U27,4
U53,42
DC_P2,69
)
(
N17368021
C337,1
U59,5
)
(
XDS_EMU1
J6,14
U539,5
)
(
DDR_A7
RN8,8
U17,U2
U16,U2
)
(
N17831179
R71,1
U24,F7
R105,2
)
(
N17368031
R296,1
C336,2
```

```
)  
(  
XDS_TDO  
U41,2  
J6,7  
)  
(  
BDDR_DQM0  
R86,1  
U1,T4  
)  
(  
N23478955  
U24,A4  
R102,2  
)  
(  
N17367916  
F1,1  
J16,CENTER  
R316,1  
)  
(  
XDS_TCK_RTN  
U539,14  
J6,9  
)  
(  
BDDR_A1  
RN7,3  
U1,U13  
)  
(  
N19129068  
U65,7  
R309,1  
)  
(  
N17367961  
DS5,ANODE  
R299,2  
)  
(  
XDS_TCK  
U41,11  
J6,11  
)  
(  
DDR_A6  
RN8,7  
U17,T7  
U16,T7  
)  
(  
N24454621  
U24,C8  
R154,2  
R153,2  
)  
(  
N17368099  
C332,1  
R293,2
```



```

)
(
DSP_TRST#
U539,9
U1,R2
)
(
BDDR_A5
RN8,3
U1,W12
)
(
N19125941
U65,3
R310,2
)
(
N17368192
U60,16
C355,1
C356,1
L53,2
U60,15
U60,14
)
(
DSP_TCK
R8,1
U1,N1
)
(
DDR_D9
U1,W4
RN27,8
)
(
N23478990
U24,B4
R106,1
R104,2
)
(
N17367818
U59,14
U59,16
U59,15
C339,1
L51,2
C342,1
)
(
T_TRSTN
Embedded_USB_1,T_TRSTn
U539,10
)
(
DDR_A5
RN8,6
U17,T3
U16,T3
)
(
N17367951

```

```
R298,1
TP33,1
C345,1
C343,1
L52,2
C344,1
)
(
T_TMS
Embedded_USB_1,T_TMS
U41,13
)
(
DDR_D7
U1,W3
RN25,6
)
(
N17368085
C333,2
U59,17
)
(
XDS_TRST#
U539,11
J6,2
)
(
DDR_A4
RN8,5
U17,T8
U16,T8
)
(
N16837199
U22,A1
R69,1
)
(
N17368761
C352,1
U60,5
)
(
T_EMU0
Embedded_USB_1,T_EMU0
U539,3
)
(
DDR_D5
U1,U3
RN25,5
)
(
N24383970
U23,G8
R99,1
)
(
N17368765
C351,1
R305,1
C350,2
```

C347,2
U60,2
R300,2
)
(
N23878170
R148,1
U35,1
U37,1
U41,9
)
(
DDR_A3
RN7,8
U17,T2
U16,T2
)
(
B.EM_A12
U22,G4
U24,A7
RN5,8
DC_P1,78
)
(
N17368190
U60,10
U60,7
U60,8
U60,9
U60,6
C352,2
L54,1
)
(
DSP_TDI
U41,7
U1,P4
)
(
DDR_D4
U1,V2
RN29,8
)
(
B.EM_A05
U22,C4
U24,C3
RN3,1
DC_P1,71
)
(
N17368051
U59,3
C335,1
R293,1
)
(
EMU_STS
U41,1
U539,1
J6,8
R6,1

```
C165,1
U38,2
)
(
DDR_A2
RN7,7
U17,R7
U16,R7
)
(
N23142904
U23,C3
R100,2
R101,1
)
(
N17368905
C348,2
U60,17
)
(
DSP_TDO
U41,4
U1,P3
)
(
DDR_D3
U1,U2
RN29,6
)
(
B.EM_A11
U22,G3
U24,B7
RN5,7
DC_P1,76
)
(
N17368983
C347,1
R302,2
)
(
N23887850
DS502,CATHODE
U38,4
)
(
DDR_A1
RN7,6
U17,R3
U16,R3
)
(
B.EM_A10
U22,H5
U24,D6
RN3,5
DC_P1,74
)
(
N17367824
U59,8
```

```
U59,10
U59,9
U59,7
U59,6
L52,1
C337,2
)
(
DSP_TMS
U41,12
U1,R3
)
(
DDR_WE
RN11,2
U16,N3
U17,N3
)
(
B.EM_A09
U22,H4
U24,C6
RN3,6
DC_P1,72
)
(
N17219404
R204,1
P12,3
L22,2
)
(
N17368791
R303,1
C351,2
)
(
T_TCK_RET
U539,13
R150,2
Embedded_USB_1,T_TCK_RET
)
(
DDR_D2
U1,U1
RN25,7
)
(
N16837203
U22,B2
R70,1
R107,1
)
(
N17219465
TP18,1
U43,J8
R217,1
)
(
N23887846
R149,2
DS502,ANODE
```

```
)  
(  
N23788606  
TP6,1  
U1,R13  
)  
(  
B.EM_A08  
U22,H3  
U24,A6  
RN3,3  
DC_P1,77  
)  
(  
N17219396  
U43,J4  
C245,1  
)  
(  
T_TCK  
U41,10  
Embedded_USB_1,T_TCK  
)  
(  
DDR_DQM3  
R85,1  
U17,E3  
)  
(  
B.EM_A07  
U22,H2  
U24,B6  
RN3,4  
DC_P1,75  
)  
(  
N17219468  
R203,1  
P12,2  
L21,2  
)  
(  
N23909182  
R9,2  
U37,4  
)  
(  
DDR_A0  
RN7,5  
U17,R8  
U16,R8  
)  
(  
B.EM_A06  
U22,D4  
U24,A3  
RN3,2  
DC_P1,73  
)  
(  
N17219568  
R205,2  
U43,B8
```

```

R206,2
)
(
N23246255
R4,2
U2,3
)
(
BDDR_A7
RN8,1
U1,V11
)
(
N24332833
R95,1
U22,A6
)
(
B_DX1_AXR0[0]
R218,1
U76,6
U75,9
)
(
N23246245
L1,2
C3,1
U2,4
U2,1
)
(
BDDR_A3
U1,U12
RN7,1
)
(
MCASP_ONN
U74,4
U76,4
U75,13
)
(
N23142758
U23,C5
R98,2
)
(
N17943022
SW6,BB
R2,1
SW6,B
JP3,1
)
(
DDR_D1
U1,T2
RN29,5
)
(
N16837816
U22,G2
R75,2
R94,2

```

```
)  
(  
N17219452  
R213,2  
R214,2  
U43,G9  
)  
(  
RESERVED  
DC_P1,4  
)  
(  
GND  
C197,2  
C3,2  
C1,2  
SW6,A  
SW6,AA  
U2,2  
JP3,2  
R19,2  
U32,3  
C168,2  
C163,2  
U41,15  
U41,8  
C166,2  
U38,3  
C167,2  
C165,2  
Embedded_USB_1,GND  
J6,12  
J6,4  
J6,10  
C169,2  
U35,3  
U35,2  
U37,2  
U37,3  
C164,2  
U539,15  
R10,2  
R12,2  
U539,8  
U3,3  
C7,2  
R251,1  
R166,2  
R167,2  
C8,2  
R33,2  
C10,2  
R34,2  
C9,2  
TP7,1  
TP8,1  
TP9,1  
TP10,1  
C5,1  
R77,2  
U4,5  
R41,2  
U1,P15
```


U1,P16
C18,2
C16,2
C17,2
C15,2
C14,2
C13,2
R39,2
C11,2
C12,2
U1,K14
U1,K12
U1,E13
U1,L17
U1,L19
U1,L7
U1,K6
U1,L13
U1,K8
U1,L11
U1,M10
U1,L9
U1,M12
U1,M14
U1,M16
U1,M19
U1,M18
U1,M17
U1,M6
U1,N5
U1,N7
U1,N9
U1,M8
U1,N14
U1,N11
U1,N13
U1,P12
U1,P10
U1,P14
U1,P6
U1,P8
U1,R15
U1,R11
U1,R1
U1,R17
U1,E7
U1,E9
U1,F10
U1,A19
U1,B1
U1,B19
U1,F14
U1,F12
U1,F4
U1,F6
U1,F8
U1,G18
U1,G13
U1,G11
U1,G7
U1,H12
U1,H6
U1,H10

U1,G9
U1,G5
U1,H8
U1,H19
U1,H14
U1,J13
U1,J17
U1,J15
U1,J11
U1,K10
U1,J5
U1,J18
U1,J9
U1,K1
U1,J7
U1,R5
U1,R19
U1,R18
U1,R7
U1,K16
U1,R9
U1,V19
U1,W1
U1,W2
C29,2
C62,2
C61,2
C65,2
C57,2
C33,2
C35,2
C30,2
C34,2
C66,2
C24,2
C63,2
C64,2
C31,2
C32,2
C60,2
C67,2
C23,2
C36,2
C39,2
C25,2
C37,2
C26,2
C70,2
C27,2
C22,2
C19,2
C20,2
C21,2
C56,2
C48,2
C50,2
C47,2
C52,2
C44,2
C42,2
C55,2
C53,2
C49,2

C41,2
C45,2
C46,2
C43,2
C51,2
C54,2
C28,2
C73,2
C72,2
C71,2
C69,2
C58,2
C59,2
C188,2
C189,2
C190,2
RN12,16
RN12,15
RN12,14
RN12,13
RN12,10
RN12,11
RN12,12
R42,1
R43,1
R176,1
R175,1
RN12,9
R364,2
C89,2
U6,4
C85,2
Q1, EMITTER
C78,1
C79,1
C84,2
C83,2
C82,2
C81,2
U5,3
U8,3
C77,2
C86,2
R47,2
R58,2
U9,3
U7,4
U7,16
U7,17
C370,1
U7,9
C80,2
C367,2
C87,2
R189,1
U11,9
U66,3
RN38,1
C90,2
U11,15
U10,15
RN38,2
RN38,4

U10,7
U10,9
U11,7
RN38,3
R7,1
C91,1
U10,6
SW7,5
SW7,4
C187,2
R17,1
C205,2
U74,3
C170,2
C206,2
U13,9
U13,6
C92,1
U13,15
C185,1
U64,15
U64,9
U42,3
U73,3
R185,2
R161,2
C110,2
C107,2
C108,2
C113,2
C109,2
U16,U9
U16,T1
U16,H3
U16,D3
U16,M3
U16,G8
U16,J8
U16,D7
U16,J2
U16,E2
U16,L8
U16,L2
U16,E8
U16,H7
U16,G2
U16,M7
U17,U9
U17,T1
U17,H3
U17,D3
U17,M3
U17,G8
U17,J8
U17,D7
U17,J2
U17,E2
U17,L8
U17,L2
U17,E8
U17,H7
U17,G2
U17,M7

C100,2
C112,2
C101,2
C104,2
C94,2
C105,2
C95,2
C96,2
C97,2
C93,2
C106,2
C103,2
C102,2
C98,2
C99,2
R78,2
R79,2
C116,2
C115,2
C195,2
U20,8
U19,8
U18,12
RN31,16
RN31,15
RN31,14
RN31,13
RN31,12
RN31,11
RN31,10
RN31,9
R89,1
C114,2
U18,14
R248,2
U71,3
C196,2
U69,3
R247,1
C123,1
R101,2
C122,1
C118,2
C119,2
U23,K3
R98,1
R99,2
U23,K8
U22,E6
U22,D1
R69,2
R70,2
C125,2
U24,E8
U24,H2
C124,2
U25,4
U25,7
C126,2
U65,4
C366,2
U24,H7
U24,F8

R71,2
R154,1
R106,2
C183,2
C175,2
U28,8
U26,8
C129,2
U27,12
C128,2
C127,2
U14,3
U27,22
RN36,8
RN36,7
RN36,6
RN36,5
RN35,8
RN35,7
RN35,6
RN35,5
R358,2
R246,1
U44,3
C198,2
R136,1
R135,1
C199,2
R112,1
R137,1
R114,1
R113,1
R123,2
C133,2
L56,1
L55,1
U30,8
C138,1
C141,1
C137,1
U30,44
C132,1
U30,39
R376,2
U30,36
C142,2
U30,35
C143,2
R124,2
U30,23
R368,2
R369,2
R370,2
R371,2
U30,12
C134,2
R119,2
C139,1
C140,1
C130,1
C131,1
C136,1
U72,2

R130,1
R121,2
R126,2
R134,1
R129,1
D2,2
U31,2
C146,2
U78,3
D1,1
C148,1
C150,2
R360,2
R138,2
P7,3
C151,2
R188,2
U77,3
C147,2
C149,2
C208,2
R147,2
C154,2
U33,8
C152,2
P8,5
L57,1
L58,1
U33,14
R145,1
U34,14
U33,11
U33,10
U33,13
C153,2
C161,2
C162,2
R187,2
C144,2
C120,2
C184,2
C186,2
U51,8
U51,49
U51,19
U51,38
U52,8
U52,49
U52,19
U52,38
C364,2
C361,2
C362,2
C363,2
U12,12
U36,3
U40,3
U12,15
C178,2
U12,8
C121,2
C174,2
C176,2

U53,38
U54,38
U53,49
U54,49
U53,19
U53,8
U54,8
U54,19
C365,2
U39,3
D5,2
P4,A18
P4,B15
P4,B17
P4,B22
P4,B28
P4,B34
P4,B38
P4,B46
P4,B57
P4,A24
P4,A30
P4,A35
P4,A37
P4,A42
P4,A48
P4,A56
P4,B49
R179,1
R180,1
C203,2
C204,2
R164,2
C179,2
C172,2
C181,2
C182,2
C180,2
C383,2
C378,2
C372,2
C379,2
C374,2
C385,2
C380,2
C393,2
C377,2
C382,2
C371,2
C386,2
C375,2
C405,2
C392,2
C384,2
C376,2
C171,2
C373,2
C394,2
C381,2
J20,87
J20,119
J20,62
J20,37

J20,99
J20,76
J20,50
J20,90
J20,64
J20,41
J20,101
J20,78
J20,52
J20,27
J20,91
J20,66
J20,45
J20,102
J20,79
J20,54
J20,32
J20,92
J20,68
J20,42
J20,81
J20,56
J20,33
J20,94
J20,127
J20,69
J20,44
J20,83
J20,110
J20,58
J20,34
J20,95
J20,128
J20,72
J20,46
J20,25
J20,85
J20,114
J20,60
J20,35
J20,96
J20,74
J20,48
U67,3
J20,9
J20,14
J20,20
J20,23
C207,2
R217,2
C191,2
C246,2
U62,3
C219,2
U43,D9
R222,2
R212,2
C231,2
C233,2
R206,1
R221,2
U75,12
R207,1

L16,1
C222,2
U76,7
C220,2
C232,2
C221,2
C223,2
U76,13
U76,12
U45,12
U48,3
C248,2
C251,2
R226,2
C249,2
R225,1
J10,2
P14,5
P14,4
U46,3
R228,2
P14,1
C252,2
C253,2
U49,3
R186,2
R242,1
U50,81
R232,1
R240,1
U50,32
U50,27
U50,62
U50,68
U50,42
U50,39
U50,56
U50,49
C274,2
C269,2
C275,2
C277,2
C288,2
C290,2
C289,2
C276,2
C291,2
L15,2
L14,1
DC_P1,1
DC_P1,2
DC_P1,98
DC_P1,94
DC_P1,97
DC_P1,15
DC_P1,16
DC_P1,30
DC_P1,29
DC_P1,70
DC_P1,69
DC_P1,81
DC_P1,86
DC_P1,82

DC_P1,93
DC_P1,6
DC_P1,47
DC_P1,57
DC_P1,58
DC_P1,33
DC_P1,48
DC_P1,38
DC_P1,37
DC_P1,34
DC_P2,89
DC_P2,25
DC_P2,28
DC_P2,2
DC_P2,22
DC_P2,35
DC_P2,21
DC_P2,76
DC_P2,16
DC_P2,47
DC_P2,36
DC_P2,90
DC_P2,80
DC_P2,48
DC_P2,79
DC_P2,75
DC_P2,1
DC_P2,40
DC_P2,68
DC_P2,67
DC_P2,57
DC_P2,64
DC_P2,60
DC_P2,63
DC_P2,8
C177,2
DC_P3,4
DC_P3,18
DC_P3,17
DC_P3,3
DC_P3,9
DC_P3,10
U15,3
R283,1
C328,2
R290,1
C330,2
C326,2
R287,1
C319,2
C320,2
C323,2
C324,2
C331,2
C329,2
C327,2
SW5,A
C368,2
TP25,1
C314,1
R275,2
TP30,1
TP27,1

C322,2
TP29,1
U55,12
U55,1
U56,2
TP28,1
U57,2
TP26,1
U58,2
U55,13
U55,21
U55,11
C315,1
SW5,AA
C321,2
C325,2
JP4,2
C312,2
R274,1
Q4, SOURCE
C346,2
D3, Anode
J16, SHUNT
J16, SLEEVE
U59,13
U59,12
C341,2
C342,2
C339,2
U59,11
U59,21
U59,1
C343,2
C344,2
C340,2
R294,2
C333,1
C345,2
R291,1
C334,1
C338,2
C359,2
C353,2
U60,12
U60,13
C355,2
U60,11
C356,2
U60,21
C354,2
U60,1
C357,2
C358,2
C348,1
R301,2
C349,1
R300,1
DS5, CATHODE
)
(
DDR_DQM2
R87,1
U17, J3

```
)  
(  
N17219499  
R193,2  
C225,2  
R194,1  
C224,1  
)  
(  
N17365327  
TP2,1  
U1,L15  
)  
(  
DDR_DQS0  
U1,U4  
R82,2  
)  
(  
MCBSP_ONN  
U73,4  
U76,1  
U75,1  
)  
(  
N17677909  
U1,K18  
R5,1  
)  
(  
DDR_D0  
U1,T1  
RN25,8  
)  
(  
WLAN_RSTN  
J20,26  
R419,1  
R394,2  
U67,4  
)  
(  
B_FSR1_AFSR0  
R216,1  
U75,20  
U76,3  
)  
(  
N16917148  
C2,2  
R5,2  
C4,2  
R19,1  
)  
(  
DDR_DQM1  
R84,2  
U16,E3  
)  
(  
N17219408  
R199,1  
U43,A6
```

```

C225,1
)
(
N16917162
Y1,2
U1,J19
C2,1
R3,2
)
(
DDR_D29
U1,V18
RN24,6
)
(
N17219417
U43,A3
C244,1
)
(
N16965506
U1,K19
Y1,1
R4,1
C4,1
R3,1
)
(
DDR_DQM0
R86,2
U16,J3
)
(
B_CLKX1_ACLKX0
R213,1
U75,15
U75,2
)
(
RESETN
U1,M4
R2,2
R1,1
C1,1
R151,2
Embedded_USB_1,USB_DSP_RSTn
R177,2
DC_P1,87
)
(
DDR_A12
RN10,5
U17,V2
U16,V2
)
(
MEM_EM_A17
RN31,4
U18,16
U22,H1
U24,C4
)
(

```

N17219428
U43,B2
L20,2
C238,1
R197,1
C239,1
R198,1
)
(
PCIEN
U1,T3
U39,4
)
(
DDR_DQS1
U1,U6
R80,2
)
(
MEM_EM_A16
U18,10
RN31,8
U22,D3
U24,B3
)
(
N17219440
U43,D1
C240,1
)
(
SYS_RESETN
U1,N4
U24,B5
U30,48
U43,H8
U50,34
DC_P1,89
U56,1
R281,1
)
(
BDDR_BS00
RN10,3
U1,U8
)
(
N17219402
U43,B7
R200,1
C235,1
)
(
RESET_OUTN
TP1,1
U32,2
R366,1
U1,N3
U40,2
DC_P2,49
)
(
DDR_CLK_N

R30,2
U16,N8
U17,N8
)
(
MEM.EM_A18
RN31,3
U18,19
R94,1
U24,D5
)
(
N17219467
R193,1
P10,2
)
(
VCC_1V8
L1,1
L3,2
R76,1
C111,2
R32,2
L5,1
L6,1
R319,1
TP47,1
C97,1
C96,1
C95,1
C94,1
C106,1
C93,1
C105,1
C104,1
C101,1
C100,1
C99,1
C98,1
C110,1
C109,1
C108,1
C107,1
U16,M1
C103,1
U17,M1
C102,1
U16,M9
U16,H9
U16,H1
U16,R9
U16,D1
U16,V1
U16,F3
U16,F7
U16,K1
U16,K3
U16,K7
U16,K9
U16,D9
U16,F1
U16,F9
U17,M9

U17,H9
U17,H1
U17,R9
U17,D1
U17,V1
U17,F3
U17,F7
U17,K1
U17,K3
U17,K7
U17,K9
U17,D9
U17,F1
U17,F9
J20,111
C384,1
C383,1
C382,1
R419,2
U43,H9
C233,1
C232,1
L26,1
L28,1
DC_P1,92
DC_P1,91
R267,1
R282,1
TP38,1
R306,2
R305,2
C359,1
R303,2
)
(
BDDR_CAS
RN11,5
U1,T7
)
(
N17219407
U43,B4
R200,2
)
(
VCC_3V3
R1,2
R366,2
C197,1
U32,5
R151,1
U41,16
U539,16
C166,1
R6,2
C164,1
R149,1
C167,1
R150,1
R152,1
J6,5
U38,5
Embedded_USB_1,3.3V

U37,5
C168,1
C163,1
U35,5
R13,1
R11,1
R16,2
R15,2
C7,1
U3,5
R18,2
R165,1
R168,1
R36,2
R317,1
TP42,1
RN13,3
RN13,4
R45,1
R44,1
RN13,6
RN13,5
RN13,2
RN13,1
R174,1
R173,1
RN13,7
RN13,8
R258,2
R48,2
C83,1
C77,1
C89,1
U7,20
U7,13
C80,1
R324,2
U9,5
U6,6
U7,1
C84,1
U5,5
U7,8
C81,1
C82,1
U8,5
C367,1
C86,1
C85,1
RN20,6
C90,1
RN20,5
RN37,11
RN37,12
RN37,13
RN37,14
RN20,7
R64,1
RN37,9
RN37,10
RN20,8
U66,5
U11,6

R74,2
RN37,15
RN37,16
R67,1
U10,5
R66,1
U11,5
R65,1
C91,2
C187,1
RN22,5
RN22,6
U13,5
U13,7
RN22,7
RN22,8
C92,2
RN34,1
RN34,4
RN34,3
RN34,2
C185,2
U64,5
U64,7
RN15,1
RN15,8
RN15,7
RN15,6
RN15,5
RN15,4
RN15,3
RN15,2
U64,6
C170,1
U42,5
C206,1
C205,1
U73,5
U74,5
R252,1
U19,16
C115,1
R91,1
C116,1
R90,1
U18,24
C114,1
U20,16
C196,1
U71,5
U69,5
R88,1
C195,1
R96,1
R100,1
R97,1
C118,1
C119,1
U22,E1
U22,D6
R93,1
U23,H8
C122,2

R95,2
C123,2
U23,J6
R107,2
R102,1
U24,G5
C125,1
U24,F1
R103,1
R105,1
C124,1
U25,8
C366,1
R309,2
U65,8
U24,D8
R104,1
C126,1
C183,1
R310,1
U26,16
U28,16
C127,1
C129,1
C128,1
U27,24
U14,5
C175,1
R326,2
C198,1
U44,5
R133,2
R378,2
R131,2
C199,1
R192,2
R118,1
R373,2
R372,2
R375,2
R374,2
R377,1
R120,1
L8,1
R116,1
U72,4
U30,7
U30,24
C138,2
C139,2
C147,1
R139,2
R328,1
R361,2
R362,1
U77,5
U78,5
C208,1
R146,1
R142,1
R143,1
U34,15
C154,1

C152,1
U33,16
C153,1
R144,1
R170,2
U12,10
C120,1
U36,5
U40,5
C144,1
U39,5
C121,1
R163,1
J20,88
J20,70
C375,1
J20,19
J20,1
J20,63
J20,28
J20,31
C381,1
C380,1
C377,1
C376,1
C378,1
C374,1
C373,1
C386,1
C394,1
C392,1
C379,1
C385,1
J20,40
J20,89
C171,1
U67,5
C393,1
R394,1
L18,1
C219,1
C231,1
L19,2
R205,1
R208,1
C246,1
U75,24
C191,1
U62,5
U76,14
C207,1
R20,1
U45,24
U48,5
C248,1
C251,1
C249,1
U46,5
P14,2
C253,1
C252,1
U49,5
L25,1

L27,1
L35,1
DC_P1,96
DC_P1,95
DC_P2,5
DC_P2,6
DC_P3,11
C177,1
U15,5
R266,2
R288,2
R345,2
R281,2
R284,2
U58,6
C330,1
C328,1
U57,6
C326,1
U56,6
R285,1
TP34,1
R298,2
R297,2
C346,1
R296,2
)
(
BDDR_BS01
RN10,2
U1,V9
)
(
MEM_EM_A19
RN31,2
U18,20
U24,D4
)
(
N17043073
U28,1
U26,1
U14,2
R358,1
)
(
N17219587
U43,E9
R221,1
R218,2
)
(
BDDR_CKE
RN11,8
U1,V8
)
(
CI_EMA_ENABLEN
R88,2
R247,2
U71,1
DC_P1,85
)

```
(
N17219535
P11,3
L20,1
P11,2
)
(
BDDR_BS02
RN10,1
U1,U9
)
(
MEM.EM_D6
U19,7
U22,F6
U23,K7
U24,H6
)
(
N17323847
U30,30
R135,2
)
(
N17219424
U43,A2
R201,2
)
(
BDDR_DQM3
R85,2
U1,T16
)
(
MEM.EM_D7
U19,4
U22,G6
U23,J8
U24,E6
)
(
N17324090
P3,2
U30,40
R109,1
)
(
PCI_VIO
P4,A59
P4,B59
P4,A10
P4,A16
P4,B19
C172,1
C179,1
)
(
N17219451
L23,1
C245,2
)
(
BDDR_WE
```

```
RN11,7
U1,T8
)
(
MEM.EM_A20
RN31,1
U18,23
U24,C5
)
(
N17323913
R123,1
RN32,10
U30,9
R120,2
)
(
N17130578
P4,B4
R178,2
)
(
N17219497
L22,1
C243,2
)
(
DDR_D30
U1,U19
RN26,6
)
(
MEM.EM_D0
U19,12
U22,B6
U23,H4
U24,E3
)
(
N17323758
U30,15
R111,2
)
(
N17129477
P4,A4
R178,1
)
(
N17219409
U43,A4
R199,2
)
(
BDDR_RAS
RN11,6
U1,U7
)
(
X00861
U19,15
U20,15
R90,2
```



```
R248,1
DC_P1,83
)
(
N17323922
RN32,9
R121,1
U30,11
R118,2
)
(
N17248633
P4,B9
R179,2
)
(
N17219493
R201,1
C242,1
R198,2
)
(
DDR_BS02
RN10,8
U17,P1
U16,P1
)
(
MEM_EM_D1
U19,9
U22,C5
U23,J4
U24,H3
)
(
PHYAD1
U30,3
RN32,11
R368,1
)
(
N17248138
P4,B11
R180,2
)
(
N17219478
R219,2
U43,F8
)
(
DDR_BS01
RN10,7
U17,P3
U16,P3
)
(
N16826717
U18,13
U18,1
U71,4
)
(
```

N17323764
R134,2
R374,1
U30,27
)
(
PCIVCC_3V3
P4,A53
P4,A45
P4,A39
P4,A33
P4,A27
P4,B54
P4,B43
P4,B41
P4,B36
P4,B31
P4,B25
P4,A21
C180,1
C181,1
C182,1
)
(
N17219463
U43,J9
TP17,1
R212,1
)
(
DDR_D24
U1,V17
RN24,7
)
(
B.CI5
U18,8
U20,14
RN5,4
DC_P1,24
)
(
N17324253
U30,34
R114,2
)
(
PCI_P_TRDYN
U53,30
P4,A36
)
(
N17219414
U43,B5
C241,1
)
(
DDR_D18
U1,W15
RN28,6
)
(
B.CI4

U18,11
U20,11
RN5,1
DC_P1,22
)
(
N17323911
R115,2
U30,33
P3,3
)
(
PCI_P_PAR
U53,41
P4,A43
)
(
N17219392
C227,1
C228,1
C226,1
U43,C1
U43,H1
U43,B1
U43,J1
L19,1
C229,1
C230,1
)
(
BDDR_A4
RN8,4
U1,V12
)
(
N17324145
R376,1
RN32,15
R377,2
U30,22
)
(
N17219530
U43,B9
R207,2
R208,2
)
(
DDR_A11
RN9,8
U17,U7
U16,U7
)
(
MEM.EM_A13
U18,5
RN31,5
U22,F3
U24,C7
)
(
PHYAD3
R370,1

```
U30,5
RN32,13
)
(
PCI_PLUGN
U39,1
U39,2
R315,1
P4,B3
R163,2
R164,1
)
(
N17219555
R222,1
U43,E8
)
(
DDR_A10
RN9,7
U17,R2
U16,R2
)
(
MEM.EM_A14
U18,6
RN31,6
U22,F4
U24,D7
)
(
N17323861
R249,2
U30,46
R131,1
)
(
PCI_P_DEVSELN
U53,32
P4,B37
)
(
N20694081
U62,1
U76,8
)
(
DDR_D15
U1,V7
RN23,7
)
(
MEM.EM_A15
U18,9
RN31,7
U22,E4
U24,E7
)
(
PHYAD2
U30,4
RN32,12
R369,1
```

```

)
(
PCI_P_C/BEN1
U53,43
P4,B44
)
(
B_CLKR1_ACLKR0
R214,1
U75,16
U75,5
)
(
DDR_D19
U1,V15
RN30,5
)
(
N17324147
R110,1
P3,1
U30,41
)
(
VLYNQ_TXD1
R336,2
C365,1
J20,43
DC_P3,8
)
(
N17219443
R211,1
P13,3
L24,2
)
(
DDR_D16
U1,W14
RN28,7
)
(
MEM.EM_D3
U20,9
U22,D5
U23,K5
U24,H4
)
(
N17324295
R378,1
U30,25
R136,2
TP12,1
)
(
VLYNQ_TXD3
R334,2
J20,67
DC_P3,16
)
(
N17219566

```

```
U43,A9
TP16,1
)
(
DDR_PADREFP
R33,1
U1,T11
)
(
MEM_EM_D2
U20,12
U22,C6
U23,K4
U24,E4
)
(
N17323843
R117,2
U30,32
P3,6
)
(
B_FSX1_AFSX0
R215,1
U75,19
U75,6
)
(
DDR_PADREFN
R32,1
U1,T10
)
(
SRAM_CEN
JP2,4
U22,B5
R93,2
)
(
N17323863
R249,1
U72,3
)
(
N24576954
U53,29
R336,1
)
(
N17219561
U43,G8
R182,2
R209,1
)
(
N17145692
C317,1
R279,1
C316,2
C313,2
U55,2
R274,2
R273,2
```

)