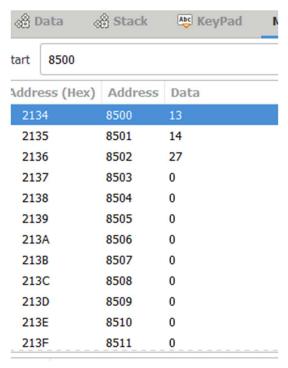# 1.Addition 8 bit;

Program:

```
Load me at
    1    LDA  2050
    2    MOV  B,A
    3    LDA  2052
    4    ADD  B
    5    STA  2054
    6    HLT
```

```
Load me at
    1    LDA  8500
    2    MOV  B,A
    3    LDA  8501
    4    ADD  B
    5    STA  8502
    6    HLT
```

Output:

tart   8500

Address (Hex)	Address	Data
2134	8500	13
2135	8501	14
2136	8502	27
2137	8503	0
2138	8504	0
2139	8505	0
213A	8506	0
213B	8507	0
213C	8508	0
213D	8509	0
213E	8510	0
213F	8511	0

Start   2050

Address (Hex)	Address	Data
0802	2050	12
0803	2051	0
0804	2052	3
0805	2053	0
0806	2054	15
0807	2055	0
0808	2056	0
0809	2057	0
080A	2058	0
080B	2059	0
080C	2060	0
080D	2061	0

Line No	Assembler Message