

COMPUTE ARCHITECTURE

CSC2002S



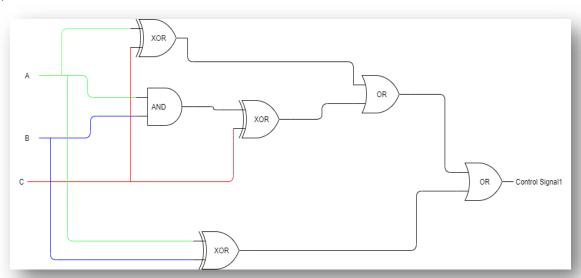
OCTOBER 10, 2020
MAFUNU LINDA
MFNLIN003

Exercise 1

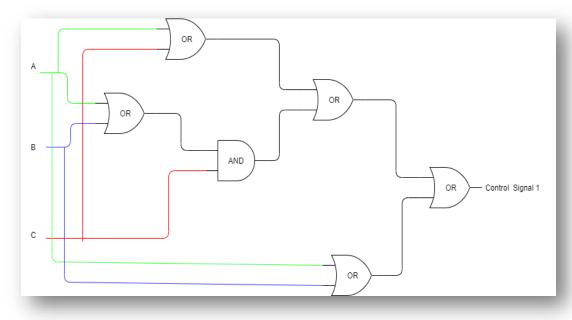
	Control Signal 1	Control Signal 2
a.	(((A AND B) XOR C) OR (A XOR C)) OR (A XOR B)	(A XOR B) OR (A XOR C)
b.	(((A OR B) AND C) OR ((A OR C) OR (A OR B))	(A AND C) OR (B AND C)

I.

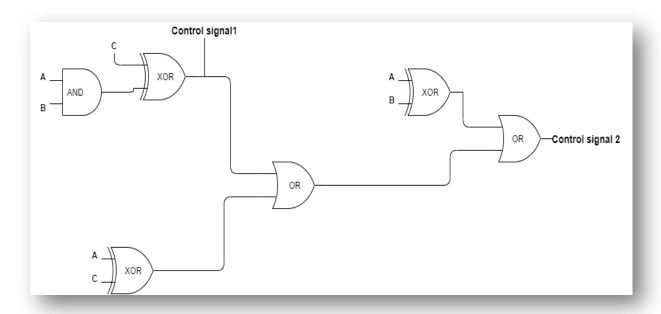
a)



b)



a)



b)

