

CSC385/CSCM85

Verification Coursework

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Question 1

Question 1 An LTS (S, α) over an alphabet A is called *trace-deterministic* if for all states $s, s_1, s_2 \in S$ and every label $a \in A$,

$$\text{if } s \xrightarrow{a} s_1 \text{ and } s \xrightarrow{a} s_2, \text{ then } s_1 =_T s_2.$$

Let (S, α) and (T, β) be trace-deterministic LTSs.

Prove: For all states $s \in S$ and $t \in T$, if $s =_T t$, then $s \sim t$.

Your proof should be a copy of the proof of part (b) of the Theorem on Page 8 of the lecture notes `verification-lts.pdf` where exactly one sentence is altered appropriately.

[30 marks]

Proof:

- we assume that the LTSs (S, α) and (T, β) are trace-deterministic. show that all states $s \in S$ and $t \in T$, if $s = Tt$, then $s \sim t$.
- using inductive proof to find bisimulation R such that whenever $s = Tt$, then $s R t$

defining $s R t \equiv s = Tt$

we aim to show that \sim is a bisimulation

- 1) $\forall a \in A \forall s' \in S (s \xrightarrow{a} s' \rightarrow \exists t' \in T (t \xrightarrow{a} t' \wedge s' = Tt'))$
- 2) $\forall a \in A \forall t' \in T (t \xrightarrow{a} t' \rightarrow \exists s' \in S (s \xrightarrow{a} s' \wedge s' = Tt'))$

- Assume $s = Tt$ means $\text{Traces}(s) = \text{Traces}(t)$
- To prove (1), assume further $s \xrightarrow{a} s'$
we must find that $t' \in T$ such that $t \xrightarrow{a} t'$ and $s' = Tt'$

for $s \xrightarrow{a} s'$ we have $\langle a \rangle \in \text{Traces}(s)$ as $\text{Traces}(s) = \text{Traces}(t)$ in the trace-deterministic case, $\langle a \rangle \in \text{Traces}(t)$. such there exists $t' \in T$ such that $t \xrightarrow{a} t'$. To complete: we need to show that $s' = Tt'$, that meaning $\text{Traces}(s') = \text{Traces}(t')$

- $\text{Traces}(s') \subseteq \text{Traces}(t')$: Assume $w \in \text{Traces}(s')$
- then $\langle a \rangle \cdot w \in \text{Traces}(s)$.
- as $\text{Traces}(s) = \text{Traces}(t)$, $\langle a \rangle \cdot w \in \text{Traces}(t)$
- there exists $t'' \in T$ such that $t \xrightarrow{a} t''$ and $w \in \text{Traces}(t'')$
- As LTS is trace-deterministic, $t' = t''$,
hence $w \in \text{Traces}(t')$

Proving $\text{Traces}(t') \subseteq \text{Traces}(s')$ is similar

Question 2

Consider the following statements about trace equivalence ($=_T$) and bisimilarity (\sim) of processes (where a is a visible event, different from the termination event, that is, $a \notin \{\tau, \checkmark\}$):

$$(1) (a \rightarrow P) \square (a \rightarrow Q) =_T a \rightarrow (P \square Q)$$

$$(2) (a \rightarrow P) \square (a \rightarrow Q) \sim a \rightarrow (P \square Q)$$

Which of these statements are true for arbitrary processes P and Q ?

In each case either prove trace equivalence respectively bisimilarity or give a concrete counterexample.

[30 marks]

Example

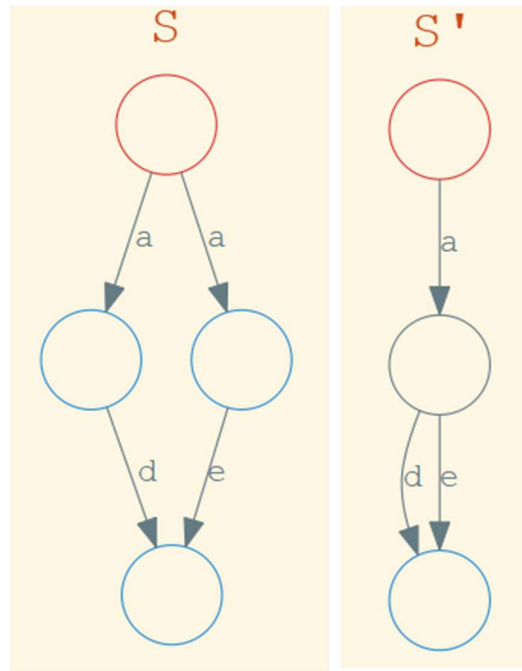
channel a, d, e

$P = d \rightarrow \text{STOP}$

$Q = e \rightarrow \text{STOP}$

$S = (a \rightarrow P) \square (a \rightarrow Q)$

$S' = a \rightarrow (P \square Q)$



$$(1) (a \rightarrow P) \square (a \rightarrow Q) =_T a \rightarrow (P \square Q)$$

LHS:

$S = (a \rightarrow P) \square (a \rightarrow Q)$

$\text{Trace}(S) = \{\langle \rangle, a, ad, ae\}$

RHS:

$S' = a \rightarrow (P \square Q)$

$\text{Trace}(S') = \{\langle \rangle, a, ad, ae\}$

$S =_T S'$ and $S' =_T S$

For all arbitrary processes P and Q

$$\text{LHS: } S = (a \rightarrow P) \square (a \rightarrow Q)$$

$$\text{Trace } (a \rightarrow P) = \{\langle \rangle\} \cup \{\langle a \rangle \wedge W \mid W \in \text{Trace}(P)\}$$

$$\text{Trace } (a \rightarrow Q) = \{\langle \rangle\} \cup \{\langle a \rangle \wedge W \mid W \in \text{Trace}(Q)\}$$

$$S = (a \rightarrow P) \square (a \rightarrow Q) = \text{Trace } (a \rightarrow P) \cup \text{Trace } (a \rightarrow Q)$$

$$\text{RHS: } S' = a \rightarrow (P \square Q)$$

$$R = \text{Trace } (P \square Q) = \text{Traces}(P) \cup \text{Traces}(Q)$$

$$S' = \text{Trace } (a \rightarrow R) = \{\langle \rangle\} \cup \{\langle a \rangle \wedge W \mid W \in \text{Trace}(R)\}$$

S and S' have equivalent traces

$$\text{Trace}(S) = \{\langle \rangle, a, a \& \text{Traces}(P), a \& \text{Traces}(Q)\}$$

$$\text{Trace}(S') = \{\langle \rangle, a, a \& \text{Traces}(P), a \& \text{Traces}(Q)\}$$

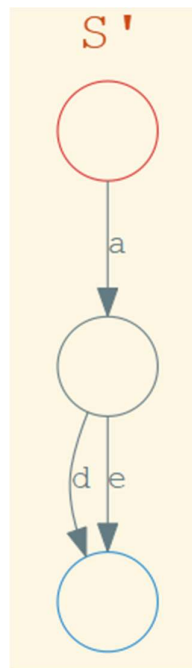
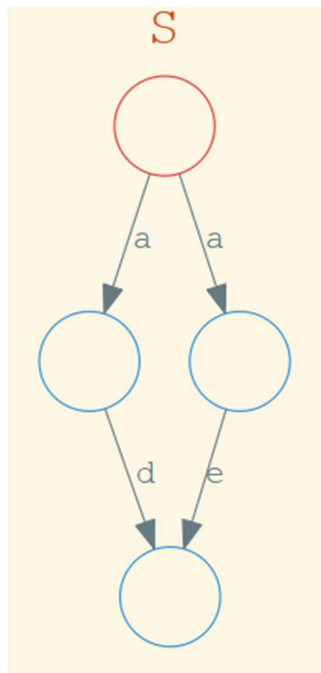
Therefore, statement (1) is true.

$$(2) \quad (a \rightarrow P) \square (a \rightarrow Q) \quad \sim \quad a \rightarrow (P \square Q)$$

(2) is false.

Trace equivalence implies bisimilarity if both systems are deterministic. S and S' are trace equivalent, however S is not deterministic (i.e., there is a difference between the traces since $\text{Trace}(S')$ branches off after $\langle a \rangle$ action compared to $\text{Trace}(S)$ it branches off from the start) therefore statement (2) is false. This can be shown using the bisimulation game shown below.

Concrete counterexample using the bi-simulation game.



S = Defender

S' = Attacker

- 1) S' chooses a $\rightarrow ((d \sqcup e) \rightarrow \text{STOP})$
 - 2) S chooses a $\rightarrow (d \rightarrow \text{STOP})$
 - 3) S' chooses e $\rightarrow \text{STOP}$
 - 4) S cannot choose e $\rightarrow \text{STOP}$
- S' has won.

Attacker S' has the winning strategy as when it picks $\langle a \rangle$, S must match $\langle a \rangle$ with either $a \rightarrow (d \rightarrow \text{STOP})$ OR $a \rightarrow (e \rightarrow \text{STOP})$. Whichever S picks, S' picks the opposite choice and so S cannot win.

As the defender cannot win, S and S' are not bisimilar.

Question 3

-- initialise min and max

min = 0

max = 5

-- set the range

Range = {min..max}

-- set the directions robot will move

datatype Direction = L | R

-- declare the channels

channel move: Direction

channel position: Range

channel work

-- implement the functions for moving robot positions

inc(x) = if $x < \text{max}$ then $x+1$ else x

dec(x) = if $x > \text{min}$ then $x-1$ else x

-- implement the process for Robot(x)

Robot(x) = position.x -> Robot(x) []

move.L -> Robot(dec(x)) []

move. R -> Robot(inc(x)) []

work -> Robot(x)

--set of any moves left or right must be in sync

x = {| move |}

--set of move left or right must be in parallel with work

y = {| move, work|}

-- robot should not hit the wall

Control(x) = x >min & move. L -> Control(x) []

x < max & move. R -> Control(x+1)

-- Uses the control system so the robot cannot move. L if in position min and cannot Move.R if in position max. Robot(x) process should be in sync with Control(x) process

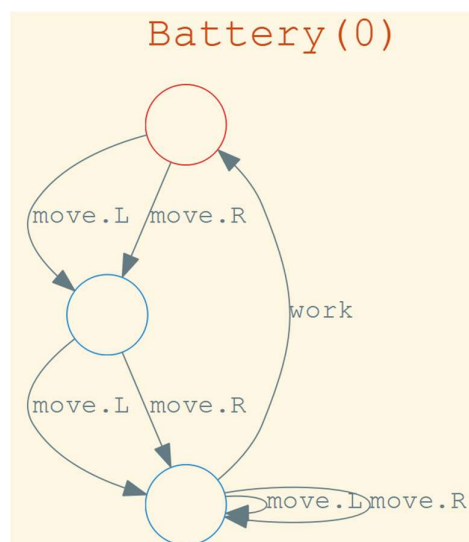
SystemC = Robot(min) [| x |] Control(min)

-- Battery does not force the robot to do work. If the battery is full the robot can move around freely and do work at any time.

-- Once work is done the battery is empty and the robot must move 2 positions to recharge the battery to do work again.

Battery(n) = (if n==2 then work -> Battery(n-2) else move?b -> Battery(n+1))

[] (n==2 & move?b -> Battery(n))



--Battery(n) process must be in sync with Robot(x) process with labels {move,work}.

-- Robot and battery without control system

SystemB = Robot(min) [| y |] Battery (0)

--Robot and battery with control system

SystemCB = SystemC [| y |] Battery(0)