

RYO Discrete TRL Boolean Logics NOR/OR

- ① Gate one input 1
- ② Gate one input 2
- ③ Gate one input 3
- ④ Gate one NOR output
- ⑤ Gate one OR output (normalised to Gate two input 1)
- ⑥ Gate two input 1
- ⑦ Gate two input 2
- ⑧ Gate two NOR output
- ⑨ Gate two OR output

[Try dif input amplitudes, waveforms and frequency rates including audio into inputs!] Width: 4 hp

