

RYO Discrete TRL Boolean Logics XNOR/ NOR/NOT

- ① Gate one input 1
- ② Gate one input 2
- ③ Gate one XNOR output
- ④ Gate one NOR Output (normalled to Gate two input 1)
- ⑤ Gate two input 1
- ⑥ Gate two input 2
- ⑦ Gate two XOR output (normalled to Gate three input 1)
- ⑧ Gate three input 1
- ⑨ Gate three NOT output

[Try dif input amplitudes, waveforms and frequency rates including audio into inputs!] Width: 4 hp

