## RYO Discrete TRL Boolean Logics NOR/OR

- Gate one input 1
- Gate one input 2
- Gate one input 3
- 4 Gate one NOR output
- Gate one OR output (normalled to Gate two input 1)
- Gate two input 1
- Gate two NOR output
- Gate two OR output

[Try dif input amplitudes, waveforms and frequency rates including audio into inputs!] Width: 4 hp

Name	OR  A+B					
Alg. Expr.						
Symbol						
Truth	В	A	X	В	A	X
Table	0	0	0	0	0	1
	0	1	1	0	1	0
	1	0	1	1	0	0
	1	1	1	1	1	0

