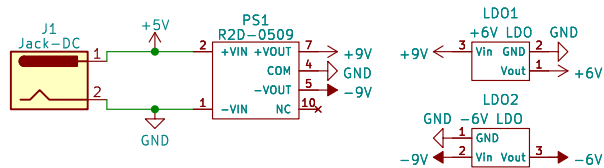
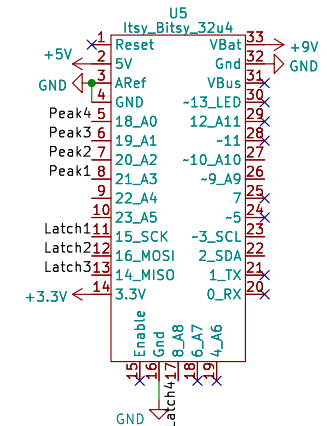


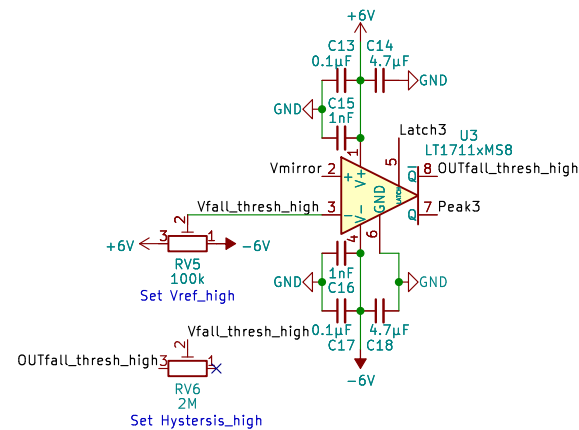
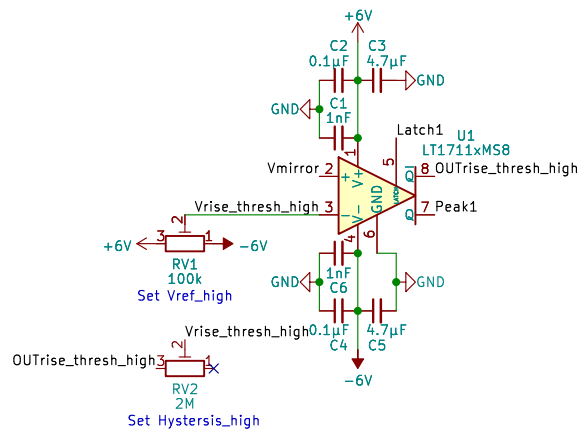
## Power Rails



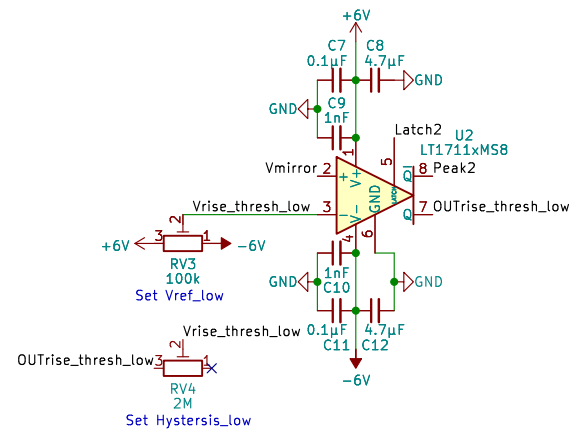
## Microcontroller



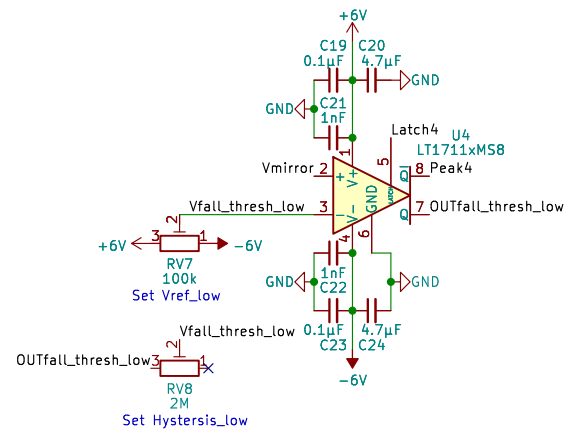
## Comparators



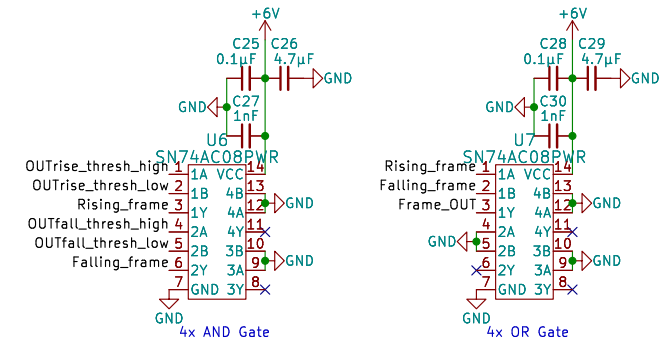
## Rising Frame



## Falling Frame



## Logic Gates to Combine Outputs



Sheet: /  
File: FACED Frame Clock.kicad\_sch

Title:

Size: A4  
KiCad E.D.A. kicad 7.0.1

Date:

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