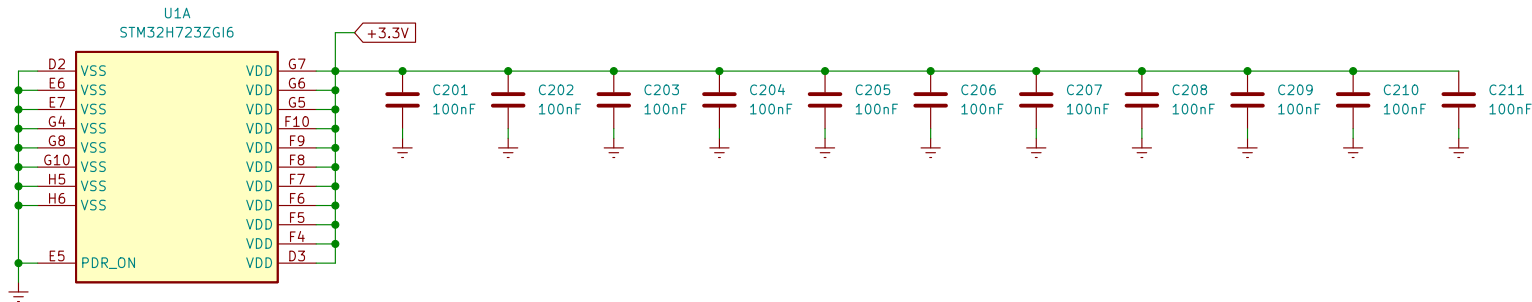
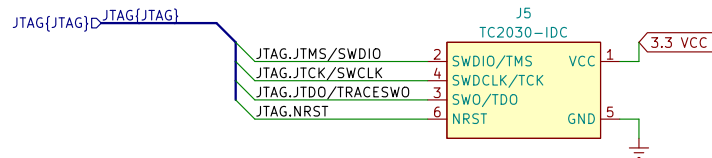


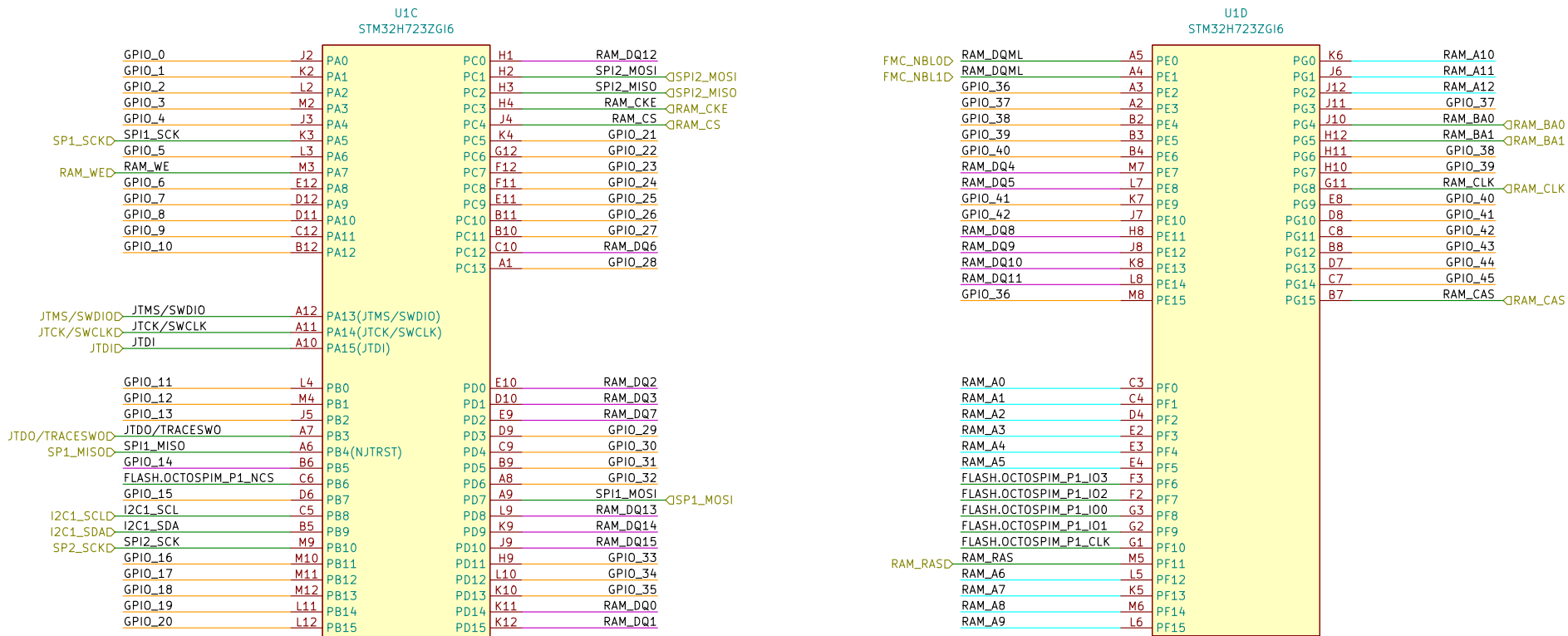
STM32H723ZGEI6 – POWER



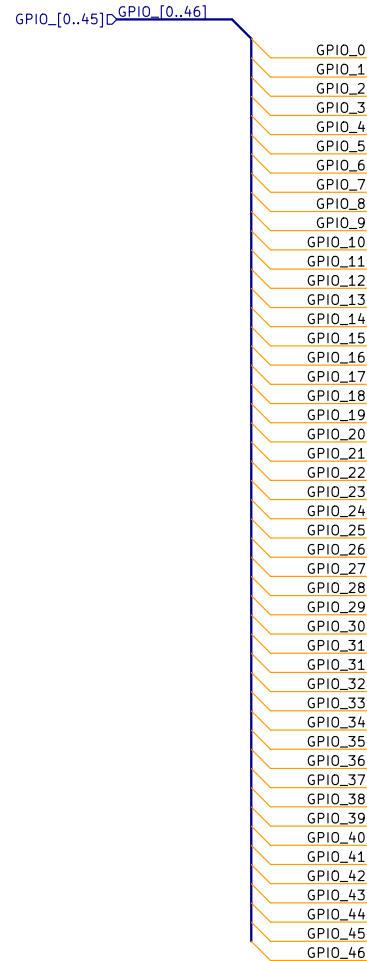
STM32H723ZGEI6 – JTAG/SWD DEBUG



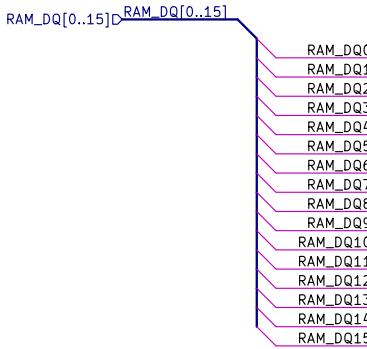
STM32H723ZGEI6 – I/O



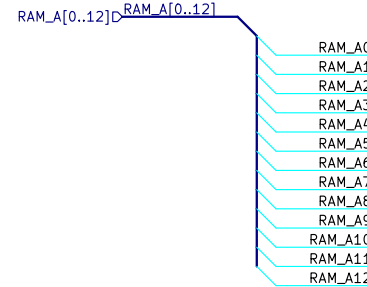
GPIO BUS



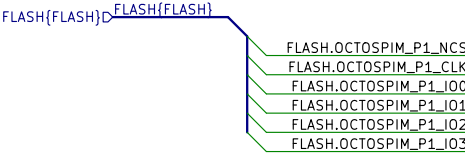
RAM DQ BUS



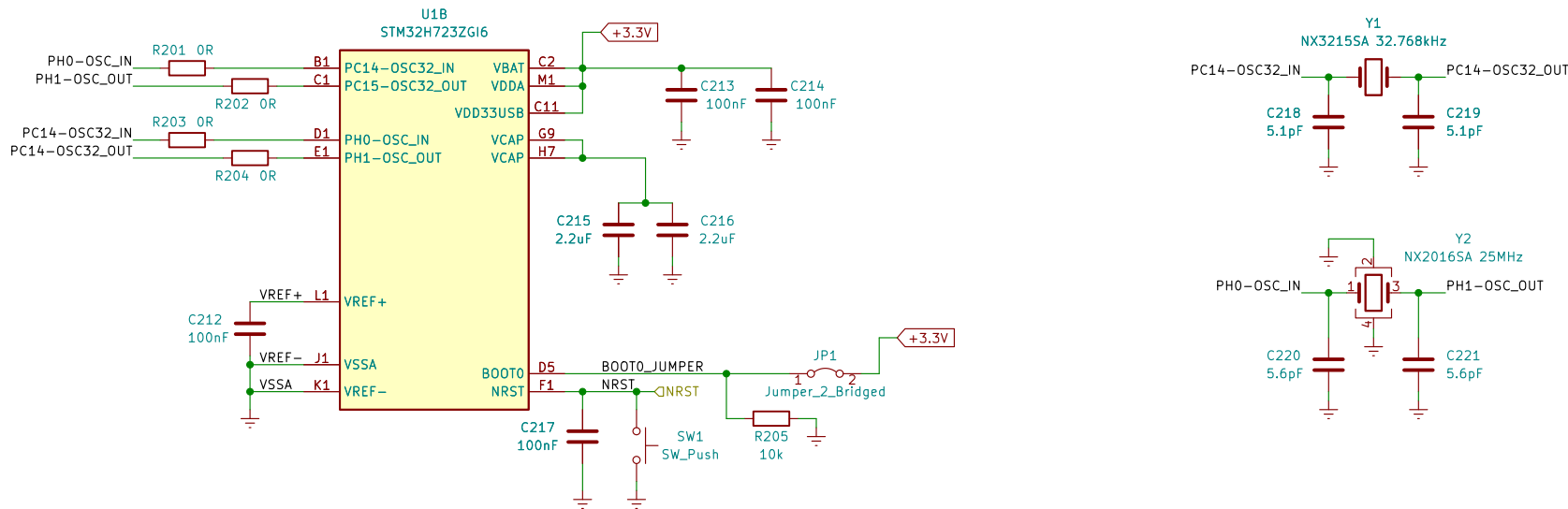
RAM ADDRESS BUS



OCTOSPI FLASH BUS



STM32H723ZGEI6 – BOOT, CRYSTALS, MISCELLANEOUS



**LEGEND:

- GPIO PINS
- RAM DQ PINS
- RAM ADDRESS PINS

**NOTES:

- Add aliases to the FLASH bus

Cerebral Systems Inc.

Sheet: /core-logic/

File: core-logic.kicad_sch

Title: Fruityfly Node

Size: User Date: 2023-09-15

KiCad E.D.A. kicad (6.0.9-0)

Rev: 1

Id: 2/6