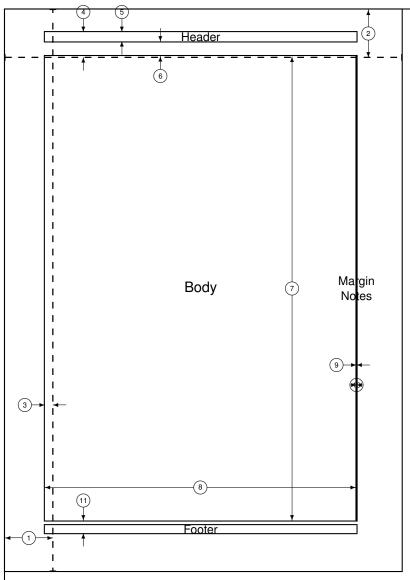
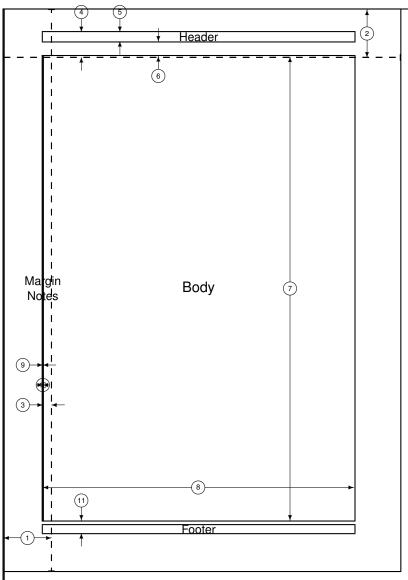
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| | CHAPTER 1. INTRODUCTION |
|-----|---------------------------|
| 1.1 | Motivation of the Project |
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| CHAPTER 2. MODELING MAGNETIC INDUCTION SYSTEM |
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| CHAPTER 3. ARCH | HITECTURE AND WPT SYSTEM | DESIGN OF THE |
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| CHAPTER 4. EXPERIMENTAL RESULTS |
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| CONCLUSIONS | |
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| CHAPTER 5. INDUCTANCE CHARACTERIZATION | |
|--|-----------------------------|
| 5.1 | Inductance Estimation Table |
| 5.2 | Equivalent coil impedance |
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| CHAPTER 6. MODEL EQUATIONS | |
|----------------------------|---|
| 6.1 | Secondary capacitor in series |
| 6.2 | Secondary capacitor in parallel |
| The s | Secondary capacitor in parallel ame steps as above are followed for obtaining the impedances Z_2 and Z_R when the secondary itor is placed in parallel: |
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| | CHAPTER 7. COILS EXPERIMENTAL RESULTS |
|-----|---------------------------------------|
| 7.1 | Inductance and Resistance |
| 7.2 | Quality Factor |
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| | CHAPTER 8. CIRCUIT SCHEMATICS |
|-----|-------------------------------|
| 8.1 | Voltage Regulator |
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