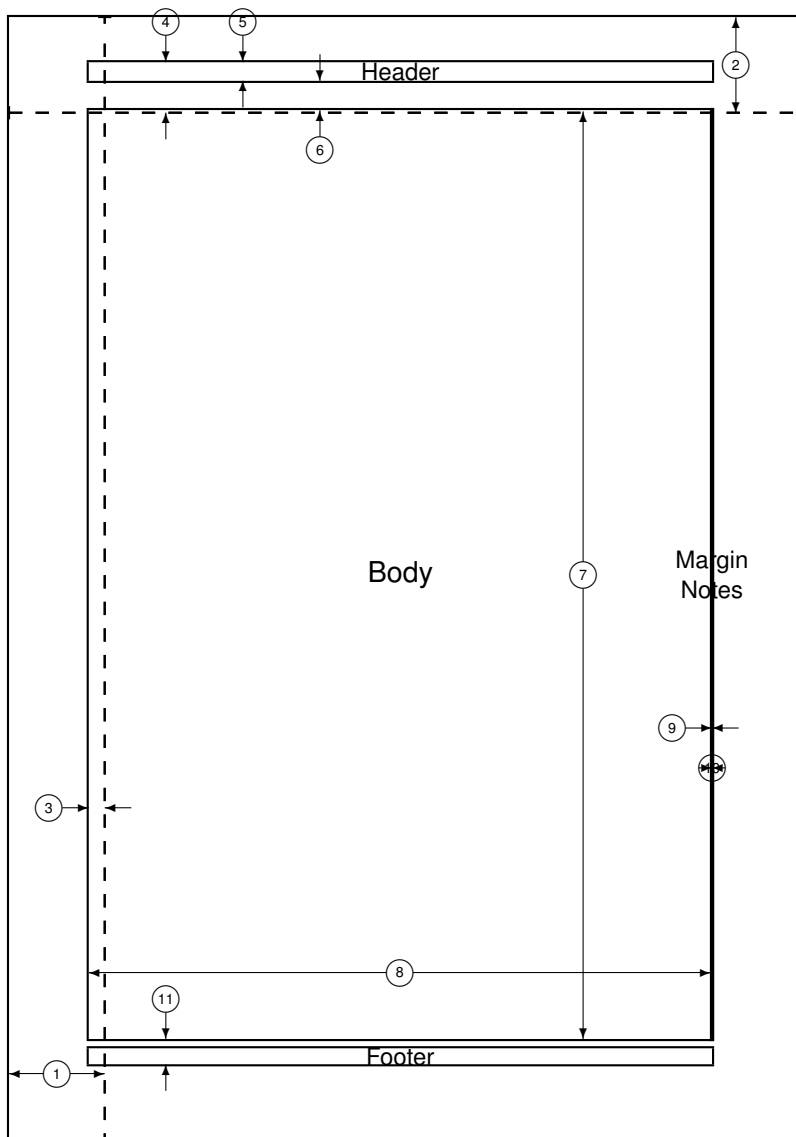
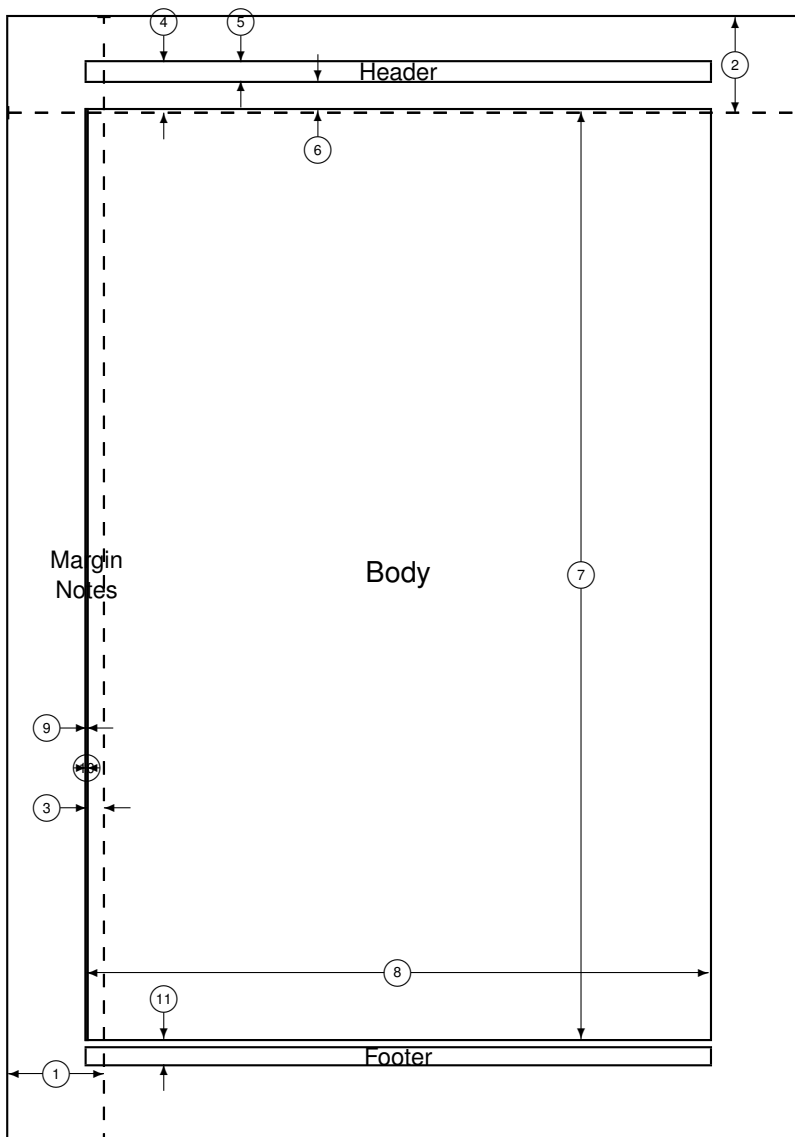




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| 5 | \headheight = 14pt | 6 | \headsep = 22pt |
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CHAPTER 1. INTRODUCTION

1.1 Motivation of the Project

CHAPTER 2. MODELING MAGNETIC INDUCTION SYSTEM

CHAPTER 3. ARCHITECTURE AND DESIGN OF THE WPT SYSTEM

CHAPTER 4. EXPERIMENTAL RESULTS

CONCLUSIONS

BIBLIOGRAPHY

CHAPTER 5. INDUCTANCE CHARACTERIZATION

5.1 Inductance Estimation Table

5.2 Equivalent coil impedance

CHAPTER 6. MODEL EQUATIONS

6.1 Secondary capacitor in series

6.2 Secondary capacitor in parallel

The same steps as above are followed for obtaining the impedances Z_2 and Z_R when the secondary capacitor is placed in parallel:

CHAPTER 7. COILS EXPERIMENTAL RESULTS

7.1 Inductance and Resistance

7.2 Quality Factor

CHAPTER 8. CIRCUIT SCHEMATICS

8.1 Voltage Regulator