

Pin Number	Pin Name
1	VI_CIF_D2_M0/MIPI_CSI_RX_CK1N/LVDS_RX_CK1N/GPI3_B2_d
2	VI_CIF_D3_M0/MIPI_CSI_RX_CK1P/LVDS_RX_CK1P/GPI3_B3_d
3	VI_CIF_D0_M0/MIPI_CSI_RX_D3N/LVDS_RX_D3N/GPI3_B0_d
4	VI_CIF_D1_M0/MIPI_CSI_RX_D3P/LVDS_RX_D3P/GPI3_B1_d
5	VI_CIF_D4_M0/MIPI_CSI_RX_D2N/LVDS_RX_D2N/GPI3_B4_d
6	VI_CIF_D5_M0/MIPI_CSI_RX_D2P/LVDS_RX_D2P/GPI3_B5_d
7	VI_CIF_D6_M0/MIPI_CSI_RX_D1N/LVDS_RX_D1N/GPI3_B6_d
8	VI_CIF_D7_M0/MIPI_CSI_RX_D1P/LVDS_RX_D1P/GPI3_B7_d
9	VI_CIF_D8_M0/MIPI_CSI_RX_CK0N/LVDS_RX_CK0N/GPI3_C0_d
10	VI_CIF_D9_M0/MIPI_CSI_RX_CK0P/LVDS_RX_CK0P/GPI3_C1_d
11	VI_CIF_CLKI_M0/MIPI_CSI_RX_D0N/LVDS_RX_D0N/GPI3_C2_d
12	VI_CIF_HREF_M0/MIPI_CSI_RX_D0P/LVDS_RX_D0P/GPI3_C3_d
13	VI_CIF_D15/PWM1_M2/GPIO3_D3_d
14	VI_CIF_D14/UART5_CTS_M2/I2C3_SDA_M2/GPIO3_D2_d
15	VI_CIF_D13/UART5_RTS_M2/I2C3_SCL_M2/GPIO3_D1_d
16	VI_CIF_D11/UART5_TX_M2/I2C4_SCL_M2/GPIO3_C7_d
17	VI_CIF_D12/UART5_RX_M2/I2C4_SDA_M2/GPIO3_D0_d
18	VI_CIF_VSYNC_M0/GPIO3_C5_d
19	VI_CIF_CLK0_M0/MIPI_CLK0_OUT/GPIO3_C4_d
20	VI_CIF_D10/PWM7_IR_M2/MIPI_CLK1_OUT/GPIO3_C6_d
21	GND
22	USB_N
23	USB_P
24	USB_VBUSDET
25	GND
26	SARADC_IN0/GPIO4_C0_z
27	SARADC_IN1/PWM1_M1/GPIO4_C1_z
28	GND
29	GND
30	CODEC_LINEOUT
31	CODEC_MICBIAS
32	CODEC_MICON
33	CODEC_MIC0P
34	CODEC_MIC1N
35	CODEC_MIC1P
36	GND
37	EMMC_D0/FSPI_D0/GPIO4_A4_u
38	EMMC_D1/FSPI_D1/GPIO4_A3_u
39	EMMC_D2/FSPI_D2/GPIO4_A2_u
40	EMMC_D3/FSPI_D3/GPIO4_A6_u
41	EMMC_D4/SPI1_CS0_M0/UART1_TX_M2/I2C2_SDA_M1/GPIO4_A5_u
42	EMMC_D5/SPI1_CLK_M0/UART1_RX_M2/I2C2_SCL_M1/GPIO4_A7_u
43	EMMC_D6/SPI1_MOSI_M0/UART0_TX_M2/I2C0_SCL_M1/GPIO4_A1_u

44	EMMC_D7/SPI1_MISO_M0/UART0_RX_M2/I2C0_SDA_M1/GPIO4_A0_u
45	EMMC_CMD/FSPI_CS0/GPIO4_B0_u
46	EMMC_CLK/FSPI_CLK/GPIO4_B1_d
47	GND
48	SDMMC_DET/GPIO3_A1_u
49	SDMMC_D0/UART2_RX_M0/PWM8_M0/GPIO3_A3_u
50	SDMMC_D1/UART2_TX_M0/PWM9_M0/GPIO3_A2_u
51	SDMMC_D2/UART5_RX_M0/JTAG_CPU_TCK_M0/JTAG_HPMCU_TCK_M1/GPIO3_A7_u
52	SDMMC_D3/UART5_TX_M0/JTAG_CPU_TMS_M0/JTAG_HPMCU_TMS_M1/GPIO3_A6_u
53	SDMMC_CMD/UART5_CTS_M0/I2C0_SDA_M2/JTAG_LPMCU_TMS_M1/PWM11_IR_M0/GPIO3_A5_u
54	SDMMC_CLK/UART5_RTS_M0/I2C0_SCL_M2/JTAG_LPMCU_TCK_M1/PWM10_M0/GPIO3_A4_d
55	GND
56	GND
57	GND
58	UART0_RX_M0/CLK_32K/CLK_REFOUT/RTC_CLK0/GPIO0_A0_z
59	UART0_TX_M0/PWM2_M0/GPIO0_A1_d
60	PWM3_IR_M0/GPIO0_A2_d
61	PWR_CTRL_M1/GPIO0_A3_u
62	PWR_CTRL_M0/PWM1_M0/GPIO0_A4_d
63	I2C1_SCL_M0/UART1_RTS_M0/PWM5_M0/GPIO0_A5_d
64	I2C1_SDA_M0/UART1_CTS_M0/PWM6_M0/GPIO0_A6_d
65	UART3_TX_M0/I2C2_SCL_M0/PWM7_IR_M0/GPIO1_A0_d
66	UART3_RX_M0/I2C2_SDA_M0/PMU_DEBUG/PWM4_M0/GPIO1_A1_d
67	PWM0_M0/CPU_AVS/VI_CIF_D0_M1/GPIO1_A2_d
68	UART1_TX_M0/I2C0_SCL_M0/GPIO1_A3_d
69	UART1_RX_M0/I2C0_SDA_M0/GPIO1_A4_d
70	UART4_RX_M0/PWM3_IR_M1/GPIO1_B0_d
71	UART4_TX_M0/PWM7_IR_M1/SPI1_CS1_M0/VI_CIF_D1_M1/GPIO1_B1_d
72	JTAG_CPU_TCK_M1/UART2_TX_M1/JTAG_HPMCU_TCK_M0/JTAG_LPMCU_TCK_M0/GPIO1_B2_d
73	JTAG_CPU_TMS_M1/UART2_RX_M1/JTAG_HPMCU_TMS_M0/JTAG_LPMCU_TMS_M0/GPIO1_B3_u
74	NPOR
75	GND
76	VCC3V3_RTC
77	VCC_1V8
78	VCC_3V3
79	VCC5V0_SYS
80	VCC5V0_SYS
81	VCC5V0_SYS
82	GND
83	GND
84	GND

85	FEPHY_RXN
86	FEPHY_RXP
87	FEPHY_TXN
88	FEPHY_TXP
89	GND
90	VO_LCDC_CLK/VI_CIF_CLK0_M1/I2C3_SCL_M1/UART5_TX_M1/PWM11_IR_M2/AUD_DSM_N/GPIO1_D3_d
91	DC_VSYNC/VI_CIF_VSYNC_M1/I2C3_SDA_M1/UART5_RX_M1/SPI0_CS1_M0/PWM0_M1/AUD_DSM_P/GPIO1
92	VO_LCDC_HSYNC/VI_CIF_HREF_M1/PWM10_M2/UART5_CTS_M1/UART3_RX_M1/GPIO1_D1_d
93	VO_LCDC_DEN/VI_CIF_CLKI_M1/PWM3_IR_M2/UART5_RTS_M1/UART3_TX_M1/GPIO1_D0_d
94	VO_LCDC_D0/VI_CIF_D9_M1/PWM11_IR_M1/UART4_CTS_M1/GPIO1_C7_d
95	VO_LCDC_D1/VI_CIF_D8_M1/PWM10_M1/UART4_RTS_M1/GPIO1_C6_d
96	VO_LCDC_D2/VI_CIF_D7_M1/PWM9_M1/UART4_TX_M1/SDIO_D2_M1/GPIO1_C5_d
97	VO_LCDC_D3/VI_CIF_D6_M1/PWM8_M1/UART4_RX_M1/SDIO_D3_M1/GPIO1_C4_d
98	VO_LCDC_D4/VI_CIF_D5_M1/PWM6_M2/I2C4_SDA_M1/SDIO_CMD_M1/SPI0_MISO_M0/GPIO1_C3_d
99	VO_LCDC_D5/VI_CIF_D4_M1/PWM5_M2/I2C4_SCL_M1/SDIO_CLK_M1/SPI0_MOSI_M0/GPIO1_C2_d
100	VO_LCDC_D6/VI_CIF_D3_M1/PWM4_M2/SPI0_CLK_M0/SDIO_D0_M1/GPIO1_C1_d
101	VO_LCDC_D7/VI_CIF_D2_M1/PWM2_M2/SPI0_CS0_M0/SDIO_D1_M1/GPIO1_C0_d
102	SDIO_D1_M0/I2S0_SCLK/VO_LCDC_D8/UART1_CTS_M1/I2C4_SDA_M0/GPIO2_A0_d
103	SDIO_D0_M0/I2S0_LRCK/VO_LCDC_D9/UART1_RTS_M1/I2C4_SCL_M0/GPIO2_A1_d
104	SDIO_CLK_M0/I2S0_MCLK/VO_LCDC_D10/GPIO2_A2_d
105	SDIO_CMD_M0/I2S0_SD03_SDI1/VO_LCDC_D11/GPIO2_A3_d
106	SDIO_D3_M0/I2S0_SD00/VO_LCDC_D12/UART1_TX_M1/GPIO2_A4_d
107	SDIO_D2_M0/I2S0_SDIO/VO_LCDC_D13/UART1_RX_M1/GPIO2_A5_d
108	UART0_RTS_M1/I2S0_SD02_SDI2/VO_LCDC_D14/PWM2_M1/I2C3_SCL_M0/FLASH_TRIG_OUT/GPIO2_A6_d
109	UART0_CTS_M1/I2S0_SD01_SDI3/VO_LCDC_D15/PWM4_M1/I2C3_SDA_M0/PRELIGHT_TRIG_OUT/GPIO2_A7_d
110	UART0_RX_M1/I2C1_SCL_M1/VO_LCDC_D16/PWM5_M1/GPIO2_B0_d
111	UART0_TX_M1/I2C1_SDA_M1/VO_LCDC_D17/PWM6_M1/GPIO2_B1_d
112	GND