

Vivado synthesis is encountering a setback as the getBDDsOfInputs() function triggers a crash

Hello,

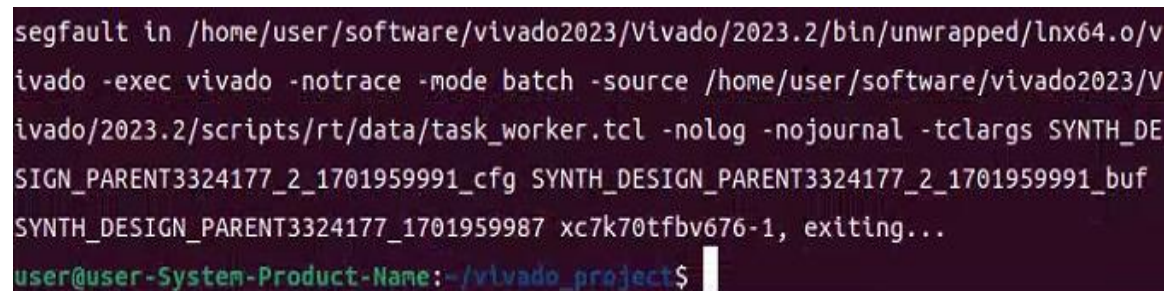
I encountered a crash while using Vivado for synthesis on my Linux system. The version of Vivado I am using is 2023.2. I performed the following commands in the Ubuntu terminal:

```
vivado -mode tcl
```

```
read_verilog rtl.v
```

```
synth_design -top top
```

During the synthesis process, Vivado crashed and generated a log file containing information about the crash.



```
segfault in /home/user/software/vivado2023/Vivado/2023.2/bin/unwrapped/lnx64.o/v  
ivado -exec vivado -notrace -mode batch -source /home/user/software/vivado2023/V  
ivado/2023.2/scripts/rt/data/task_worker.tcl -nolog -nojournal -tclargs SYNTH_DE  
SIGN_PARENT3324177_2_1701959991_cfg SYNTH_DESIGN_PARENT3324177_2_1701959991_buf  
SYNTH_DESIGN_PARENT3324177_1701959987 xc7k70tfbv676-1, exiting...  
user@user-System-Product-Name:~/vivado_project$
```

I have attached our design file rtl.v and the log file from Vivado's output.

I am looking forward to your response.

Thank you!