

## Question [1]

**micro-processor:** General purpose processor contains no RAM, ROM & No I/O ports, can't operate without adding them externally

**micro-controller:** Single specific purpose chip contains CPU, fixed amount of I/O ports, RAM, ROM used to control embedded systems

**Embedded sys:** special purpose computer

**Mechatronic sys:** Sys in which Mechanical hardware are integrated with information

**n-bit processor:** processor works only on n-bit of data at time  
- Data larger than n-bit should be broken into n-bit pieces

## Question [2]

### Micro-processor

- General purpose
- contains no RAM, no ROM, No I/O ports
- RAM, ROM, I/O ports Added externally
- it's sys are versatile

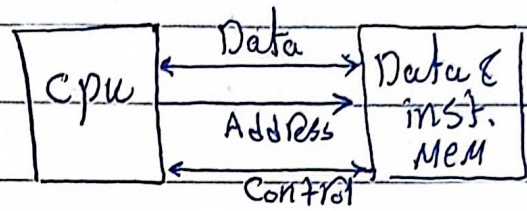
### Micro-controller

- special purpose
- contains I/O ports, ROM, RAM
- can't add any external peripherals to it
- ideal for critical app

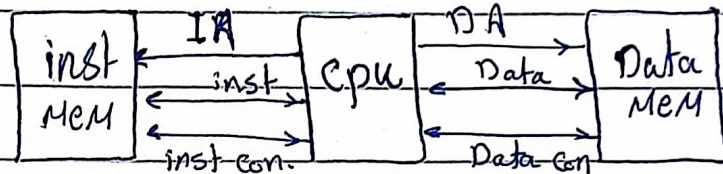
### Question 3]

#### Von-Neumann:

- Single Memory store Data & inst.
- Single bus fetch inst. or store data.



#### Harvard:



- Separate Mem for inst. & another for data
- one bus connects CPU to RAM & another connects CPU to ROM

### Question 4]

1. **PROM**: user programmable, information burned into it once

2. **EPROM**: Can be programmed & Erased by UV Ray

3. **E<sup>2</sup>PROM**: erasable with electrical, can erase 1 byte.

4. **Flash**: Enhanced version of E<sup>2</sup>PROM, mostly used

5. **Mask ROM**: not user programmable, programmed by manufacture  
- cheaper kind of OTP



### Question [5]

1) **SRAM**: cells Made from flip-flops, don't require refresh  
- Fast - require 6T<sub>N</sub>

2) **DRAM**: cells Made from capacitors, require refresh  
- Cheaper than SRAM

3) **NV-RAM**: allow cpu to Read & write  
the contents not loss due to power off  
- Another type combine between E<sup>2</sup>prom & SRAM

Question [6] ROM is Read only Mem since cpu doesn't have the capability to write on it

- it May be written by external device.

### Question [7]

Type	Volatile?	Writable?	Erase size	Speed
SRAM	Yes	Yes	Byte	Fast
DRAM	Yes	Yes	Byte	Moderate
Masked	No	No	-	Fast
PROM	No	once	-	Fast
EPROM	No	Yes	Entire chip	Fast
E <sup>2</sup> prom	No	Yes	Byte	Fast to read, slow to write
Flash	No	Yes	sector	" " " " " "
NVRAM	No	Yes	Byte	Fast