Question[1]	6
Micko processor. General purpose processor contains no RAM, ROM & No Ilo ports, can't operate without adding them externally	6
1.0 J.561.15 J. CO.C. J. O. D. G. T. T. D. C. T.	1
micro controller: single specific purpose chip contains cpu, fixed amount of I lo ports, RAM, ROM used to controll combeded systems	8
amount of I/o ports, RAM, ROM used to control	1
embeded systems	8
	1
Embedded sys: special purpose computer	8
Mechtronic sys: sys in which Mechanical hardware are integrated with information	-6
with information	—©
	_0
N-bit processor processor works only on n-bit of data at time - Data larger than n-bit should be broken into n-bit pieces	_6
- Data larger than n bit should be broken into	2
n bit pieces	1
	0
Question [2]	0
Micro processor Micro controller	0
General purpose special purpose	1
_ Contains no Ram, no Rom, No Ilo ports _ Contains Ilo ports, Rom, RAM	4 0
_ RAM, Rom, I lo ports Added externally can't add any external	1
_it's sys are versatile peripherals to it	(I)
ideal for critical app	10
	•
	<u>C</u>
	0
	art.
	6

auestion (3)
Von neumann:
Single MeMory Store Data Cinst. Cpu Data &
Single MeMory Store Data Cinst. Cpu Address inst. Single bus letch inst. or store Control Mem
Con From
$\frac{\partial \mathcal{Q}(\alpha)}{\partial \alpha}$
Horvard: inst cpu Data Duta Mem Mem
Took and Dots and
separate MeH for inst lanother to data
one bus connects cpu to RAM Canother connects cpu to RAM
E ONE DUS CONNECTS CYLL TO KNY CANOINEL CONNECTS CYLL TO KOY
Question [4]
1 mon: user programmable, information burned into it once
2 Epron. Can be programmed & Erabed by UV ray
C
3 E2 prou erasable with electrical, can erase 1 byte.
4 Jlash: Enhanced Verson of E2 prom, Mostly used
5 Mask Rou. not user programmable, programmed by Manufacture
-cheaper Kind of OTP

CS CamScanner

Questi	on[5]				6	
1) SRAM: Cells Made from Slip Slops, don't require resposh						
			2011.0		6	
2) DRAM: cells Made from capacitors, require refresh						
Cheaper than SRAM						
- Careapor Ina. S. S. A.						
3) NV RAM. allow epu to Read Ewrite						
the contents not loss due to power off						
Another type combine between E2pvom & 3RAM						
		wei igpe		1) 1000 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1		
On dr. M. D. His sol of M. M. cross con I do the the						
Question B BoM is Read only Mell since cpu doesn'thate the						
Capability to write on 11						
it is a color of the color						
it May be written by external device.						
Questi	·M	-				
_Questi	ox(-) ;[]		-		0	
Type	Volatile?	writable?	Frase	Speed	00	
	Yes	Yes		[ast	0	
SRAM		Yes	Byte	Moderate	0	
DRAM	<u> Yes</u>		Byte_		W	
Masked	No	No.		Jast Jast	<u> </u>	
prou	_No	_once	Entirechip		(M)	
Eprom	No	Yes		Fast		
E2 plom	_No	Yes	Byte_	Jast to read, slow to write.	OT.	
Flash	_No	<u> </u>	Sector		(M)	
NURAM	No	Yes	Byte	-Jast	(a)	
					40	