

## Lab objectives:

Objectives	status
Download and install the Xilinx tools	DONE
Understand the different steps of the toolchain	DONE
First Tutorial	DONE
First Project	DONE

## First Lab

- Xilinx account created
  - Xilinx installer downloaded
  - Software Installed
  - Vivado 2021.2 is used during the lab and project with necessary board files and IP library files for the Nexys A7-100T board.
  - Inspected the different tools such as creating microblaze block design and adding related IP blocks, validating process, creating bitstream process
  - Get started with the first design
- Created a small design with microblaze and added some IPs and ran the bitstream