

# Software Synthesis for Embedded Processors

(References, sources.bib file's PDF version)

```
@online{cite01,  
  author = {Embedded Artistry},  
  title = {Lesson: Compilers},  
  url = {https://embeddedartistry.com/lesson/compilers/},  
  urldate = {2023-06-04},  
  note= {Used to get a clear understanding of compilers and how the compilation process is  
handled in software synthesis as well as in normal scenarios. This resource used related  
illustrations and diagrams in this research paper}  
}
```

```
@online{cite02,  
  author = {Scaler},  
  title = {Compilation Process in C},  
  url = {https://www.scaler.com/topics/c/compilation-process-in-c/},  
  urldate = {2023-05-14},  
  note= {This resource is used to get a clear idea of how the compiling process happens in C  
programming language. The resource clearly explains the process step by step with  
examples.}  
}
```

```
@online{cite03,  
  author = {GeeksforGeeks},  
  title = {Microarchitecture and Instruction Set Architecture},  
  url = {https://www.geeksforgeeks.org/microarchitecture-and-instruction-set-architecture/},  
  urldate = {2023-05-14},  
  note= {This resource is used to get a clear understanding of Instruction set architecture. It  
explains the basics of ISA. It also explains microarchitecture and the a comparison between  
two architectures}  
}
```

```
@book{cite04,  
  author = {Andrew S. Tanenbaum and Todd Austin},  
  title = {Structured Computer Organization},  
  publisher = {Pearson},  
  year = {2012},  
  note= {This resource is used to understand the Instruction Set Architecture and its  
implementation, introduction and uses. }  
}
```

```
@article{cite05,
  author = {D. A. Patterson},
  title = {Reduced Instruction Set Computers},
  journal = {Communications of the ACM},
  volume = {Volume 28},
  year = {1985},
  note= {This resource is used to get a clear understanding and introduction to Reduced
instruction set Architecture. The resource explains all the important factors of the Reduced
Instruction set Architecture including introduction, implementation, and comparison between
other architectures}
}
```

```
@misc{cite06,
  author = {L. Ryzhyk},
  title = {The ARM Architecture},
  year = {2006},
  note= {This resource is used to get a clear understanding of ARM architecture. The resource
explains all important factors of ARM architecture including introduction, history,
implementation, uses and comparison with other architectures. }
}
```

```
@book{cite7,
  title={Hard Real-Time Computing Systems: Predictable Scheduling Algorithms and
Applications},
  author={Buttazzo, Giorgio C},
  year={2011},
  publisher={Springer Science \& Business Media},
  note= {this resource is used to get a clear understanding of Embedded systems in general
and in detail about software synthesis in embedded systems. In this resource, all the
important details about the software synthesis embedded are discussed including a proper
introduction, implementations, coding examples, figures explaining the process and required
equations}
}
```