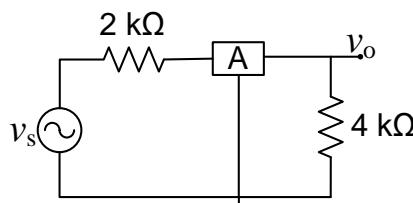
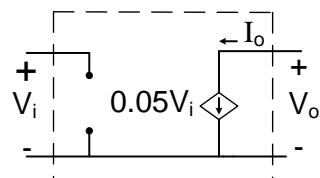
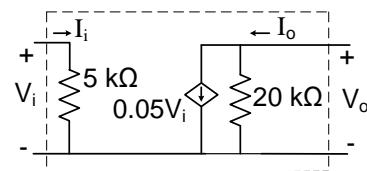


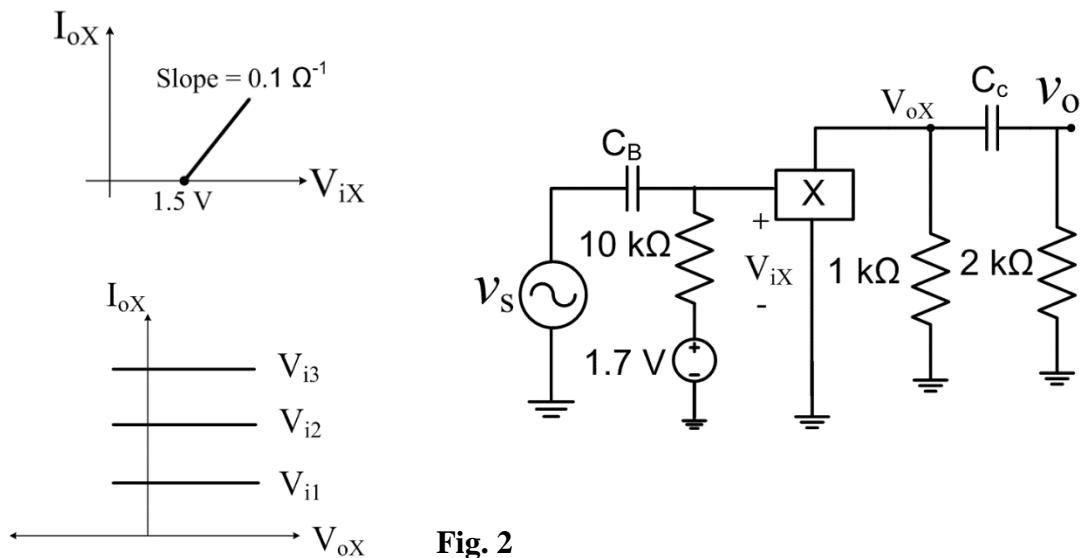
22<sup>nd</sup> September, 2016**Home Assignment – 7**

[ **Note:** DC Voltage =  $V_i$  (all capital letters); ac Voltage =  $v_i$  (all small letters); Net voltage =  $V_i$  (base is in capital and subscript is in small letter; device (A, X, Y or Z) is always in capital letter ]

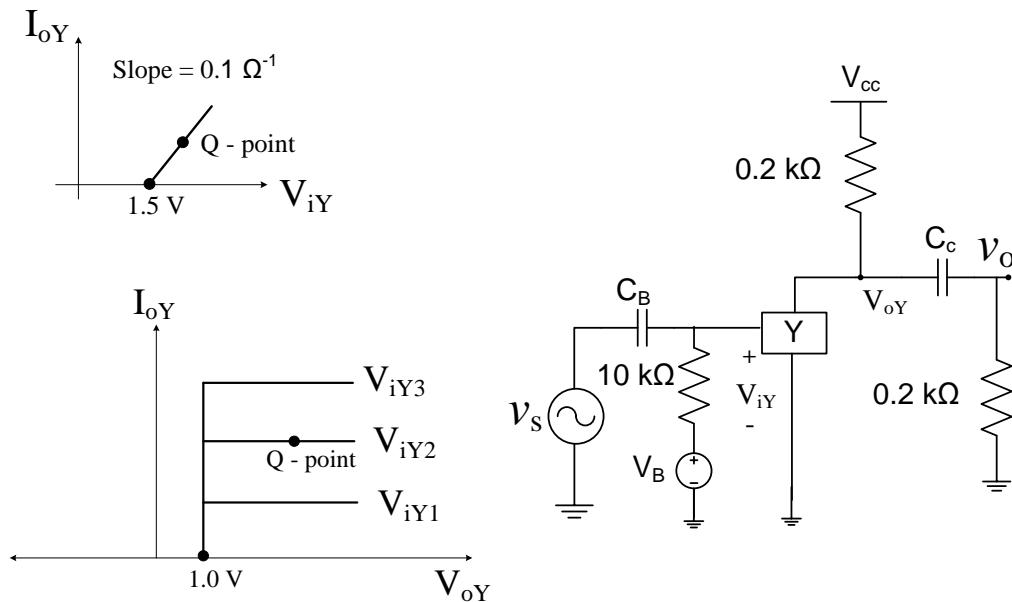
- Determine the ac **voltage gain** of the amplifier ‘A’ as shown in **Fig. 1(a)** using the **transistor model** shown in **Fig. 1(b)** and **Fig. 1(c)** as dotted region. Assume that the device ‘A’ is biased properly.

**Fig. 1(a)****Fig. 1(b)****Fig. 1(c)**

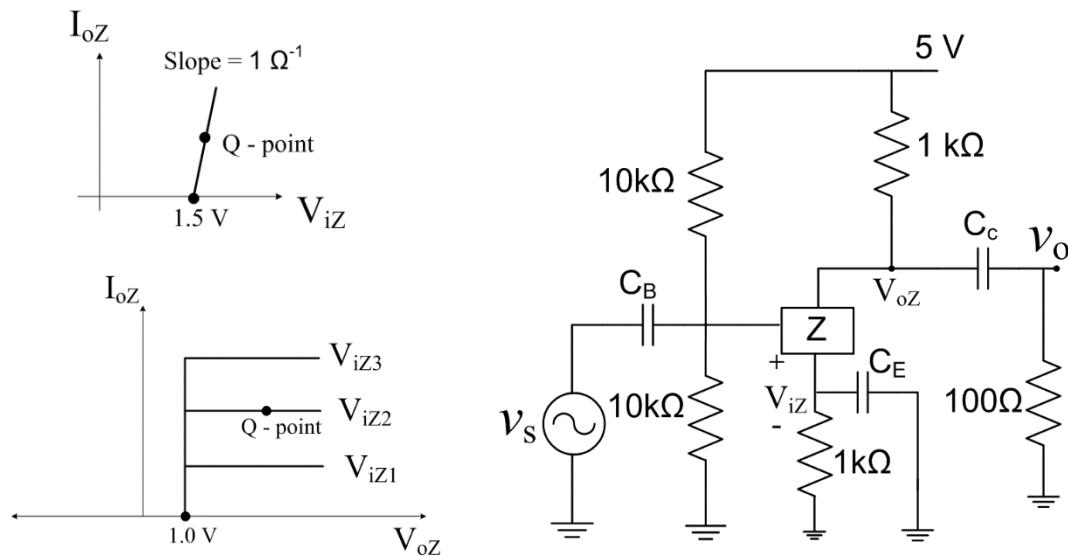
- Carry out DC and AC analysis of the amplifier circuit, with amplifier ‘X’, whose device characteristics are shown in **Fig. 2**. Also, sketch  $V_{iX}$ ,  $V_{oX}$  and  $v_s$  for  $v_s = 0.2 \sin(\omega t)$ .

**Fig. 2**

3. Determine appropriate **Q point** (DC value of  $V_{iY}$  and  $V_{oY}$ ) so that amplifier shown in **Fig. 3** would properly amplify an input voltage of  $v_s = 0.2 \sin(\omega t)$ . Also, determine minimum supply voltage  $V_{cc}$  for which the amplifier would work properly.

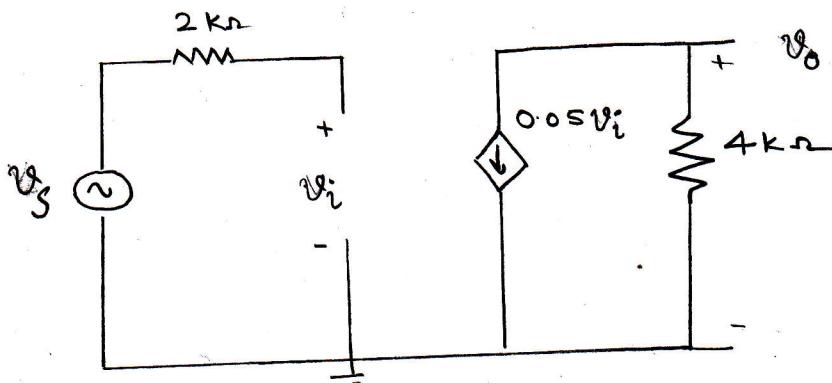
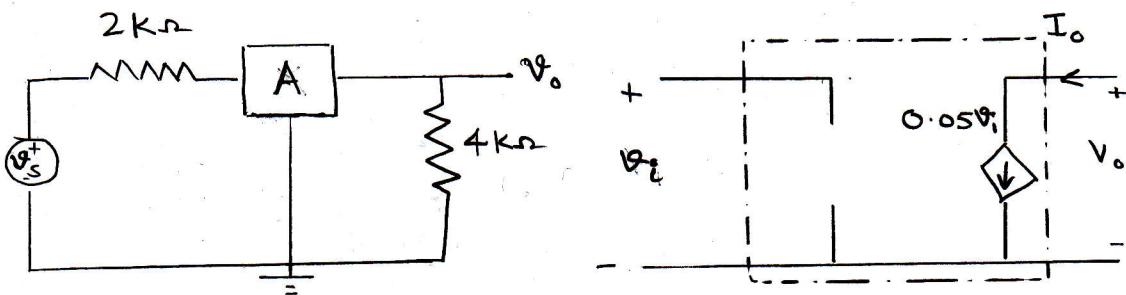
**Fig. 3**

4. Carry out DC and AC analysis of the amplifier circuit shown in **Fig. 4** to determine **bias** or **Q point** (DC value of  $I_{oZ}$  and  $V_{oZ}$ ) and **ac voltage gain**.

**Fig.4**

SOLN. ①

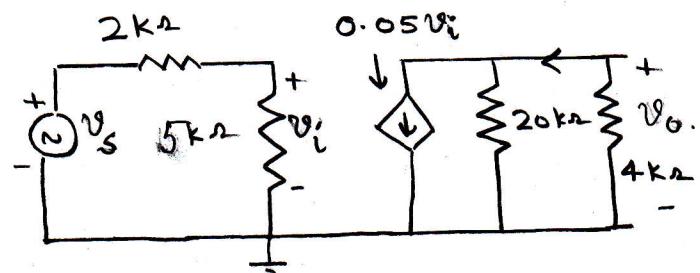
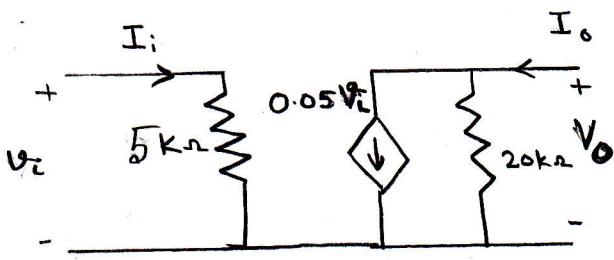
P-1



$$\therefore v_i = V_s \quad v_o = -0.05 v_i \times 4 \times 10^3$$

$$\Rightarrow v_o = -0.05 V_s \times 4 \times 10^3$$

$$\Rightarrow \text{Voltage Gain} = \frac{v_o}{V_s} = -200$$



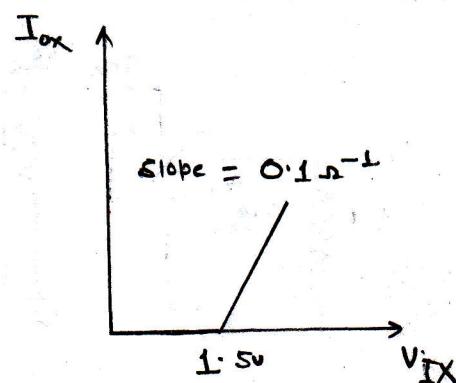
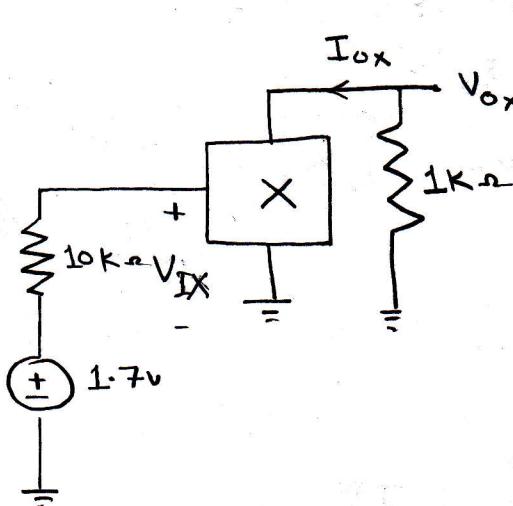
$$\Rightarrow \therefore v_i = V_s \times \frac{5 \times 10^3}{5 \times 10^3 + 2 \times 10^3} = \frac{5 V_s}{7}$$

$$\Rightarrow \therefore v_o = -0.05 v_i \times R_{eq} \quad R_{eq} = 20 k\Omega || 4 k\Omega \\ = 3.33 k\Omega$$

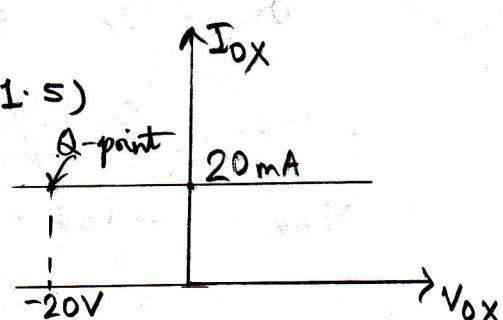
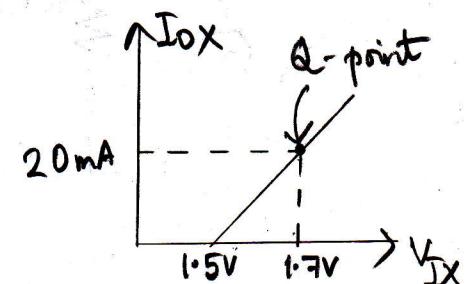
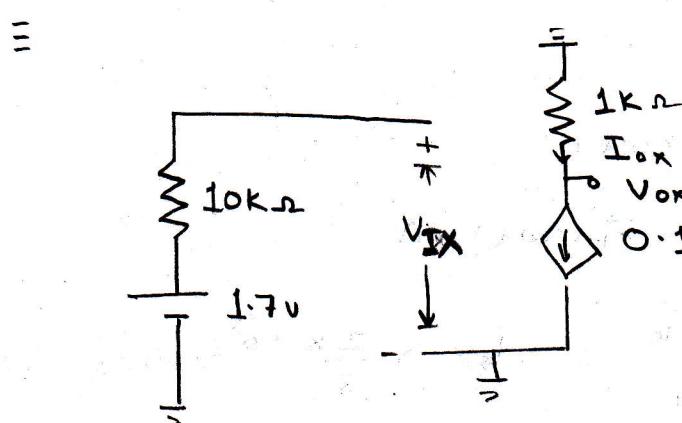
$$\Rightarrow v_o = -0.05 \times \frac{5 V_s}{7} \times 3.33 \times 10^3$$

$$\therefore \text{Voltage gain} = \frac{v_o}{V_s} = -0.05 \times \frac{5}{7} \times 3.33 \times 10^3 = -119.04$$

1. DC Analysis:- for DC circuit analysis  
capacitors are open circuited.



$$\therefore \boxed{X} = \frac{V_{IX}}{0.1(V_{IX} - 1.5)}$$



$$\therefore V_{IX} = 1.7 \text{ V.} \quad \therefore I_{OX} = 0.1 \times (1.7 - 1.5) \\ = 20 \text{ mA}$$

$$\therefore V_{OX} = -I_{OX} \times 1 \times 10^3 = -20 \times 10^{-3} \times 1 \times 10^3 \text{ V.}$$

$$\therefore V_{OX} = -20 \text{ V.}$$

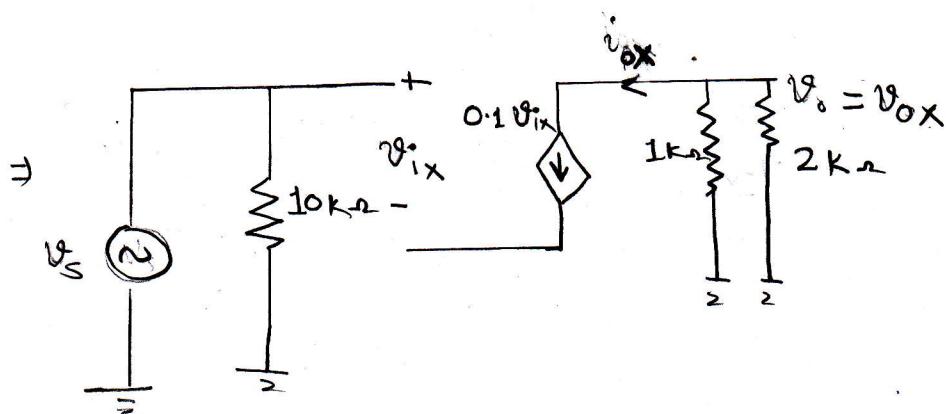
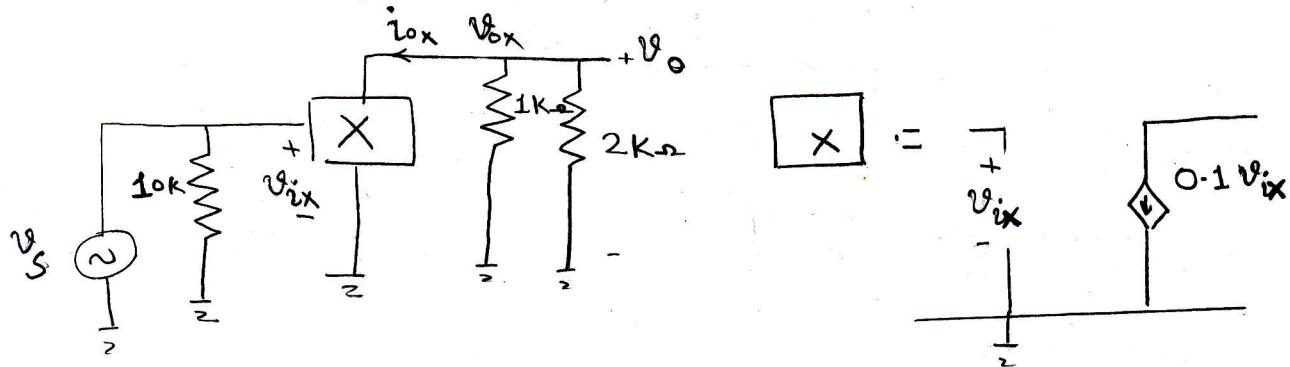
[ dc voltage:  $V_I$  ; ac voltage:  $V_i$  ; Net voltage:  $V_i$  ]

↓                    ↓                    ↓

All Capital letters      All small letters      Subscript is small letters

AC Analysis:- All capacitors & DC voltage sources are short circuited.

Sources are short circuited.



$$\Rightarrow \therefore V_{ix} = V_s = 0.2 \sin(\omega t)$$

$$\therefore i_{ox} = 0.1 V_{ix} = 20 \sin(\omega t) \text{ mA}$$

$$\Rightarrow V_{ox} = -i_{ox} \times \frac{2k \times 1k}{2k + 1k} = -i_{ox} \times \frac{2 \times 10^3}{3} = -20 \times \frac{2}{3} \sin(\omega t)$$

$$\Rightarrow V_{ox} = V_o = -13.34 \sin(\omega t) \text{ V}$$

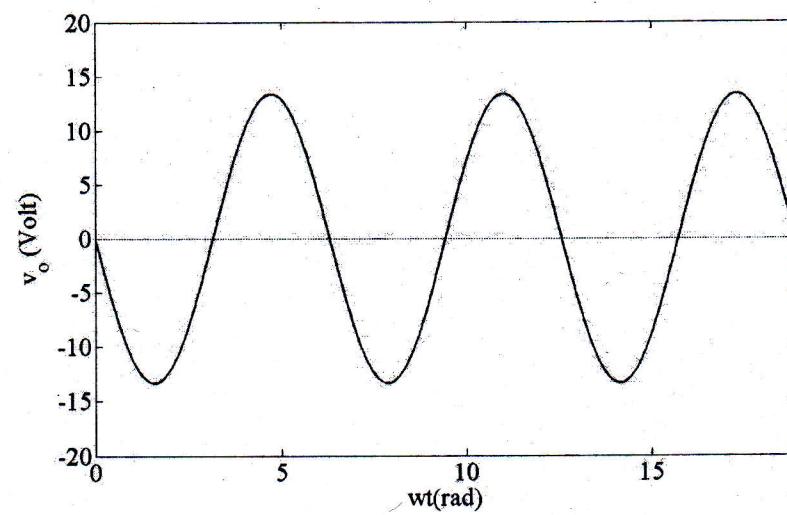
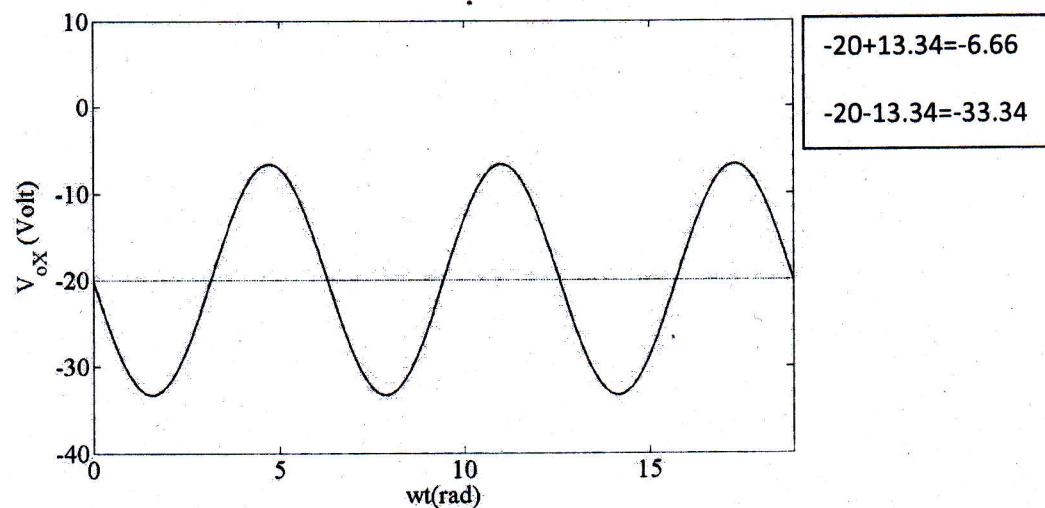
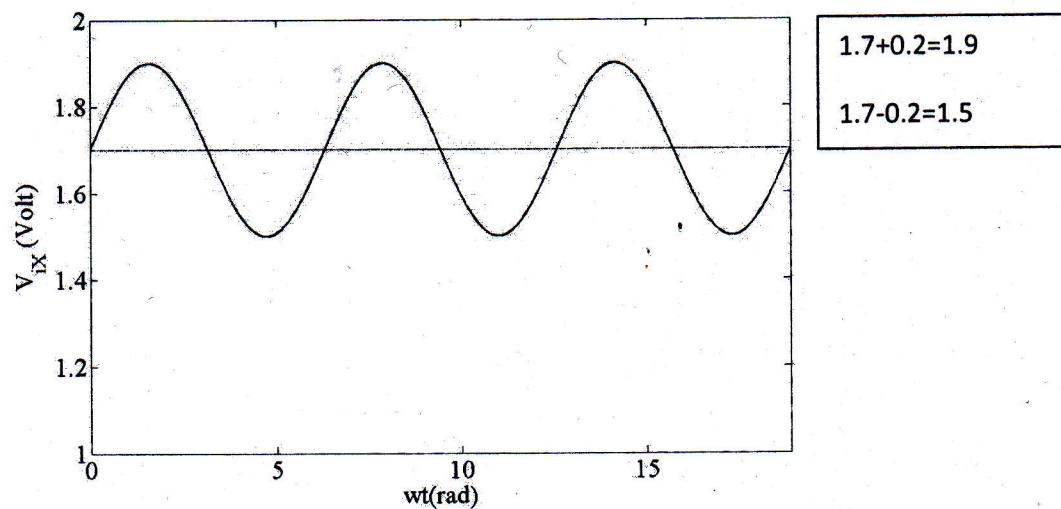
So, total  $V_{ix}$ ,  $I_{ox}$ ,  $V_{ox}$  can be written as,

$$\Rightarrow V_{ix} = V_{IX} + V_{ix} = 1.7 + 0.2 \sin(\omega t)$$

$$\Rightarrow I_{ox} = I_{ox} + i_{ox} = 20 \text{ mA} + 20 \sin(\omega t) \text{ mA}$$

$$\Rightarrow V_{ox} = V_{ox} + V_{ox} = -20 - 13.34 \sin(\omega t) \text{ V}$$

$$\Rightarrow V_o = -13.34 \sin(\omega t) \text{ V} \quad (\text{due to capacitor } C_o) \\ \text{DC portion is blocked.}$$

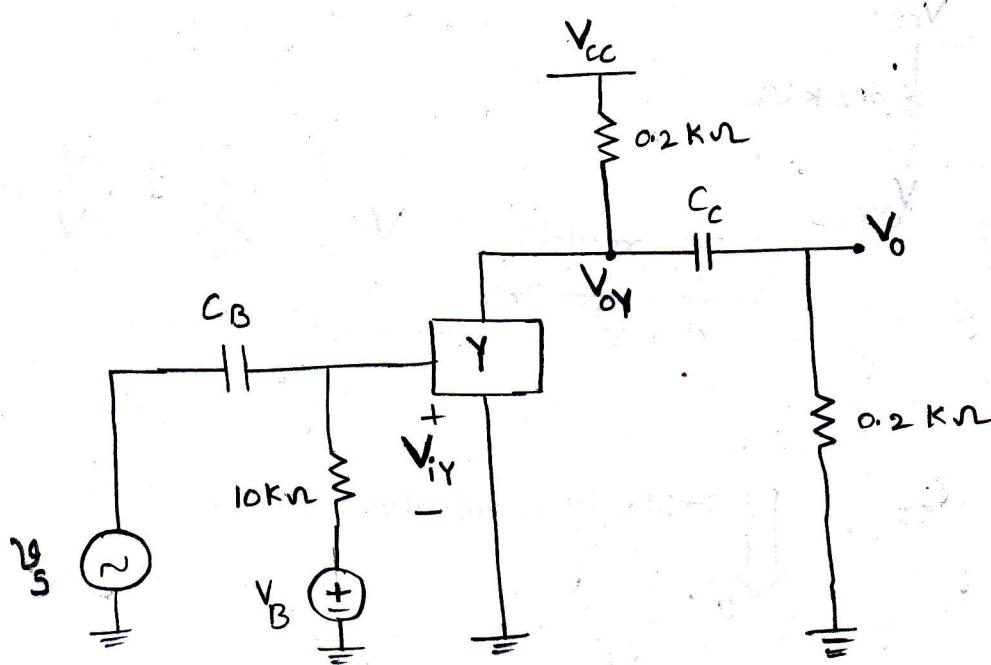
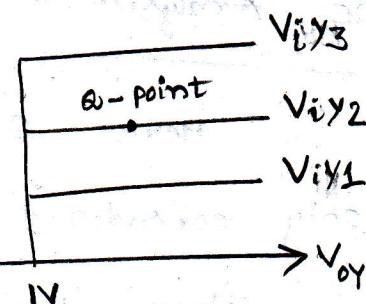
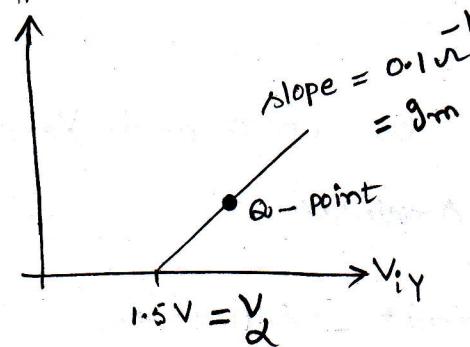


$I_{BY}$

SOLN. ③

$I_{BY}$

P-5



From the device characteristics given above, we can see that transistor works properly,

when

$$V_{iY} \geq 1.5 \text{ V} \text{ and } V_{OY} \geq 1 \text{ V} \quad \left[ \begin{array}{l} V_{iY} = V_{iY} + v_{iY} \\ V_{OY} = V_{OY} + v_{OY} \end{array} \right]$$

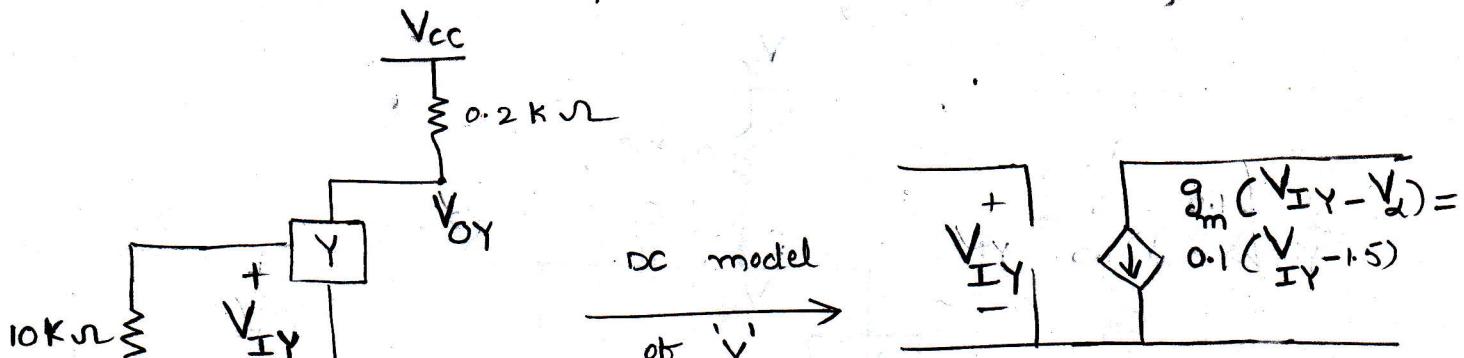
Now to bind out desired voltages we carry out 'DC' and 'AC' analysis as depicted below.

DC Analysis :-

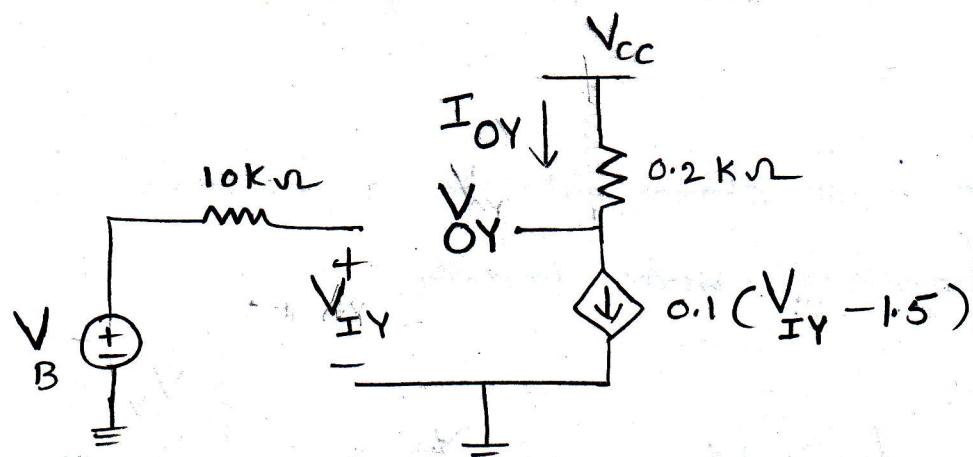
P-6

Here  $C_B$  and  $C_C$  will be open and  $V_B$  is only considered for further analysis.

Then the Amplifier circuit reduces to,



↓ This is equivalent to



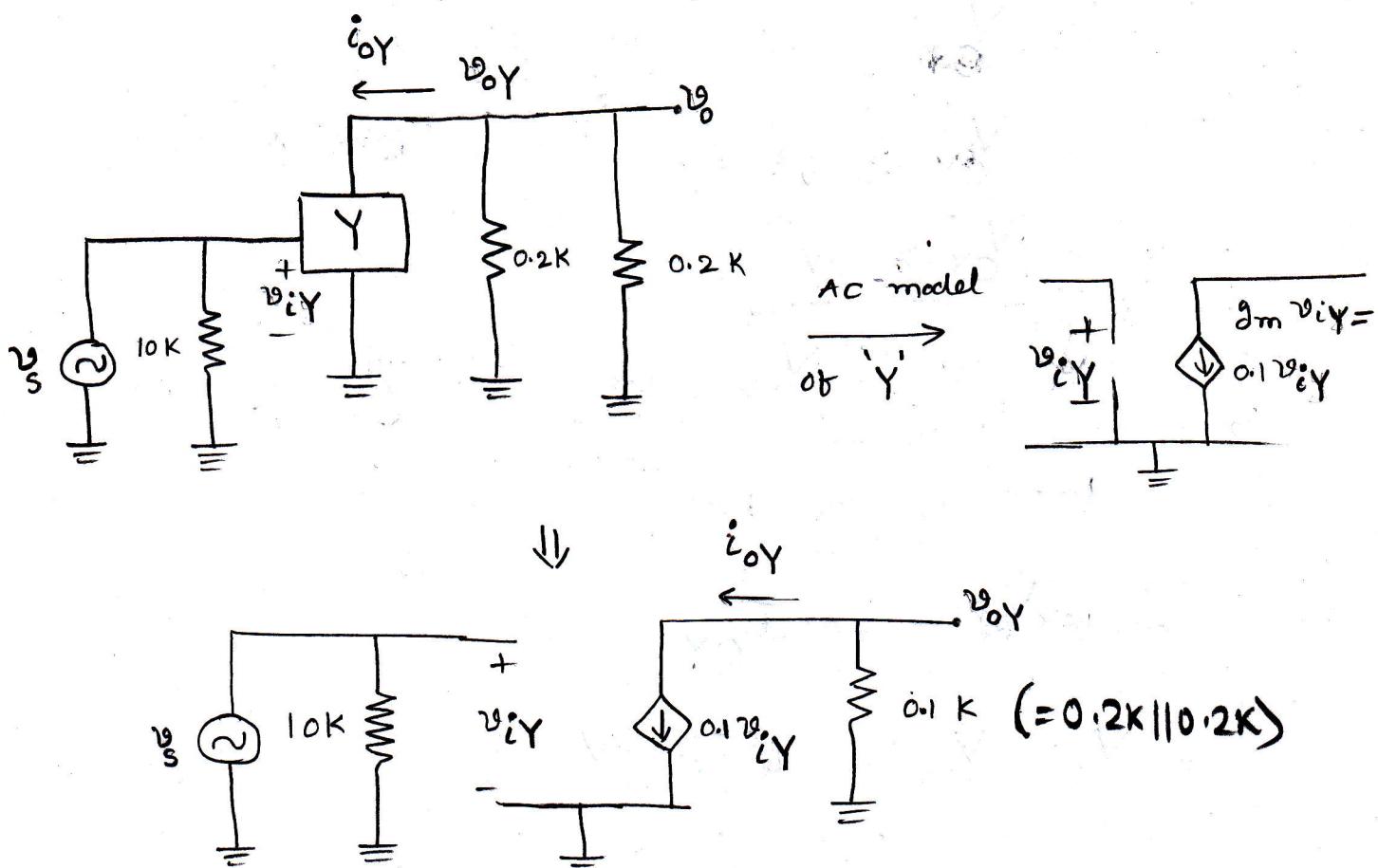
From the figure,  $V_{IY} = V_B$

$$I_{OY} = 0.1(V_{IY} - 1.5) \quad \text{--- (1)}$$

$$V_{OY} = V_{CC} - 200I_{OY} \quad \text{--- (2)}$$

Ac analysis:

Here ' $v_B$ ', ' $C_B$ ' and ' $C_C$ ' are shorted.



$$v_{iY} = v_s = 0.2 \sin(\omega t) \text{ Volts}$$

$$i_{oY} = 0.1 v_{iY} = 0.02 \sin(\omega t) \text{ Amp.}$$

$$4 \quad v_{oY} = -0.1K \times i_{oY} = -2 \sin(\omega t) \text{ Volt}$$

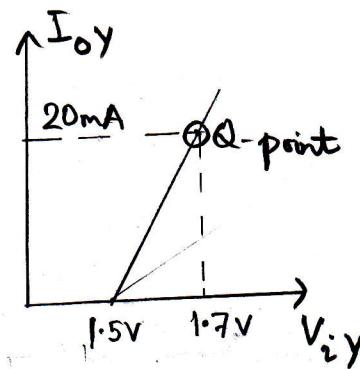
Determination of Q-point :-

$$V_{iY} = V_{IY} + v_{iY} \geq 1.5 \text{ V}$$

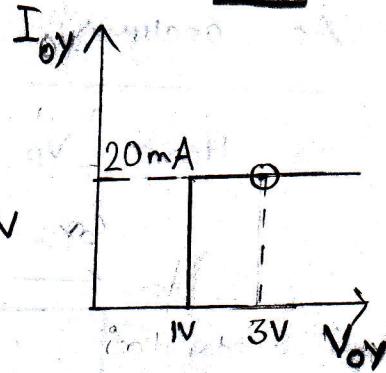
$$\Rightarrow V_{IY} + 0.2 \sin(\omega t) \geq 1.5 \text{ V}$$

$$\Rightarrow V_{IY} + 0.2 \times 1 \geq 1.5 \text{ V}$$

$$\Rightarrow V_{IY} \geq 1.7 \text{ V}$$



$$V_{OY} = V_{OY} + v_{OY} \geq 1V$$



$$\Rightarrow V_{OY} - 2 \sin(\omega t) \geq 1V$$

$$\Rightarrow V_{OY} \geq (2+1)V$$

$$\Rightarrow \boxed{V_{OY} \geq 3V}$$

Let us choose  $V_{IY} = 1.7V$  and  $V_{OY} = 3V$  (since we have to find minimum  $V_{CC}$ )  
Then, from ② we have,

$$V_{OY} = V_{CC} - 200 I_{OY}$$

$$\Rightarrow V_{CC} = V_{OY} + 200 [0.1(V_{IY} - 1.5)] \quad [\text{From ①}]$$

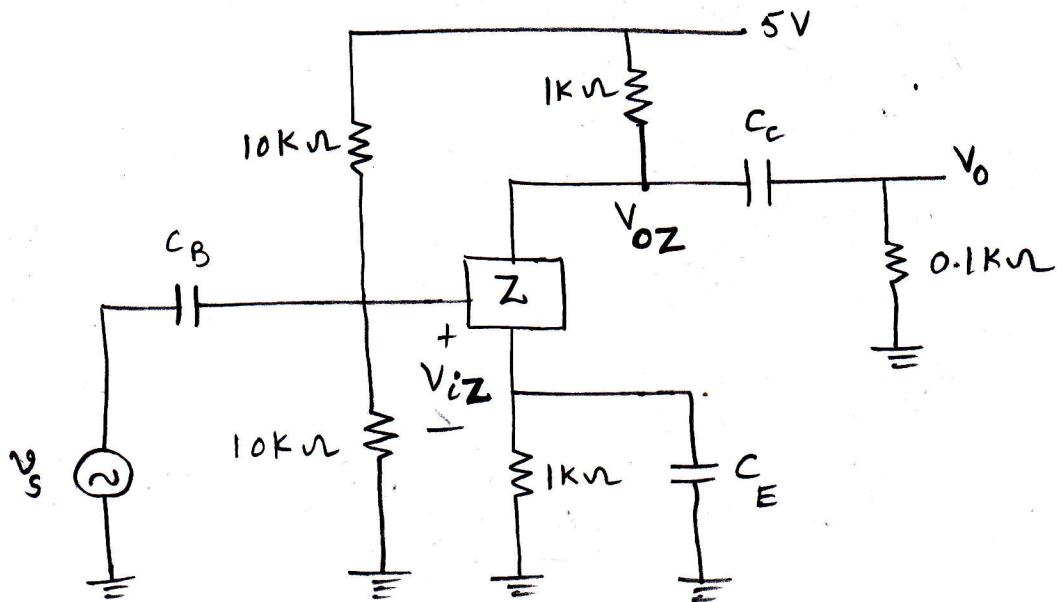
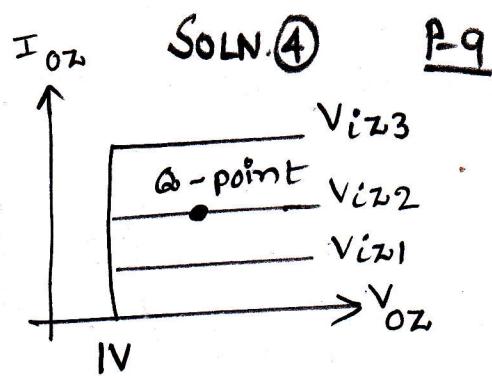
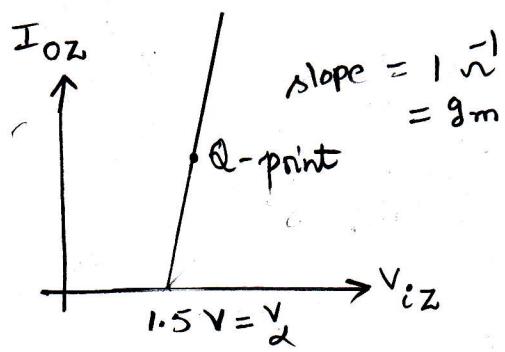
$$= 3 + 200 \times 0.1 \times (1.7 - 1.5)$$

$$= 3 + (200 \times 0.1 \times 0.2)$$

$$= 3 + 4 = 7V$$

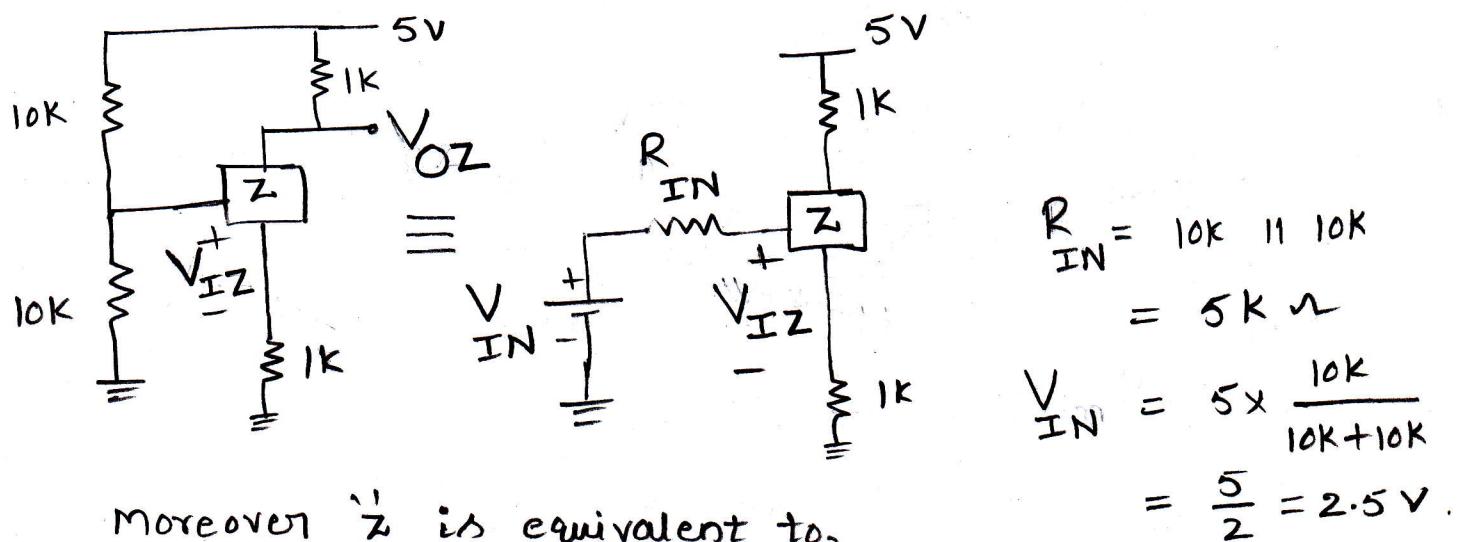
$$\therefore \boxed{V_{CC} = 7V}$$

Minimum  $V_{CC}$  of 7V is needed for proper working of Amplifier.

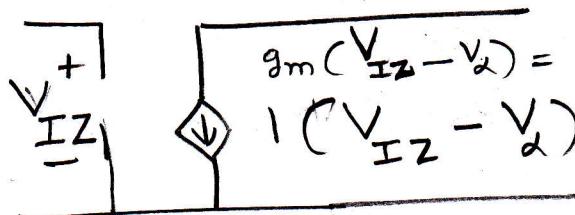


DC Analysis :-

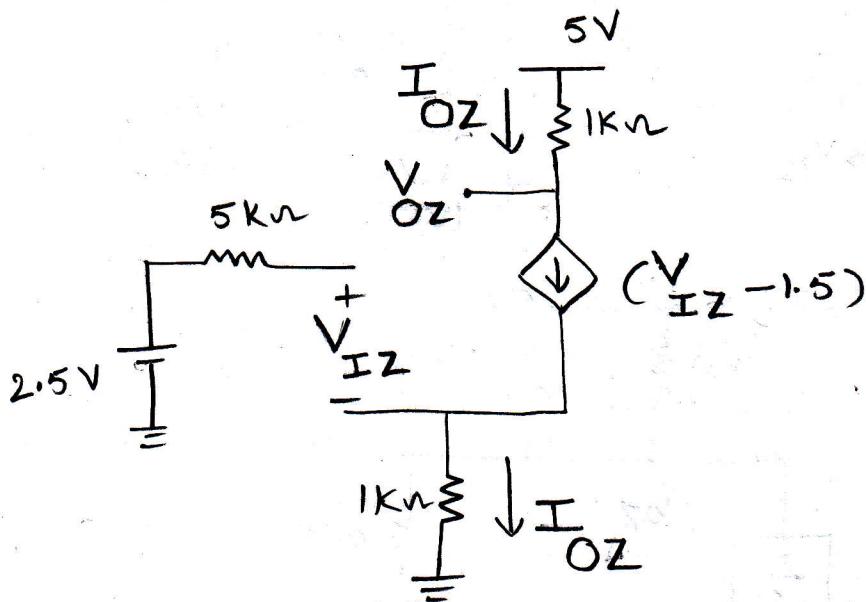
The equivalent circuit, after capacitors being opened, is



Moreover ' $Z$ ' is equivalent to,



Now the actual circuit looks like,



From the figure we can write that,

$$I_{OZ} = \frac{V_{IZ}}{1000} - 1.5$$

and

$$-2.5 + \frac{V_{IZ}}{1000} + 1000 I_{OZ} = 0$$

$$\Rightarrow -2.5 + \frac{V_{IZ}}{1000} + 1000 (\frac{V_{IZ}}{1000} - 1.5) = 0$$

$$\Rightarrow -2.5 + 1001 \frac{V_{IZ}}{1000} - 1500 = 0$$

$$\Rightarrow 1001 \frac{V_{IZ}}{1000} = 1502.5$$

$$\Rightarrow \frac{V_{IZ}}{1000} = \frac{1502.5}{1001} = 1.501 \text{ Volts}$$

$$\therefore \boxed{\frac{V_{IZ}}{1000} = 1.501 \text{ V}}$$

Now,

$$V_{OZ} = 5 - 1000 I_{OZ}$$

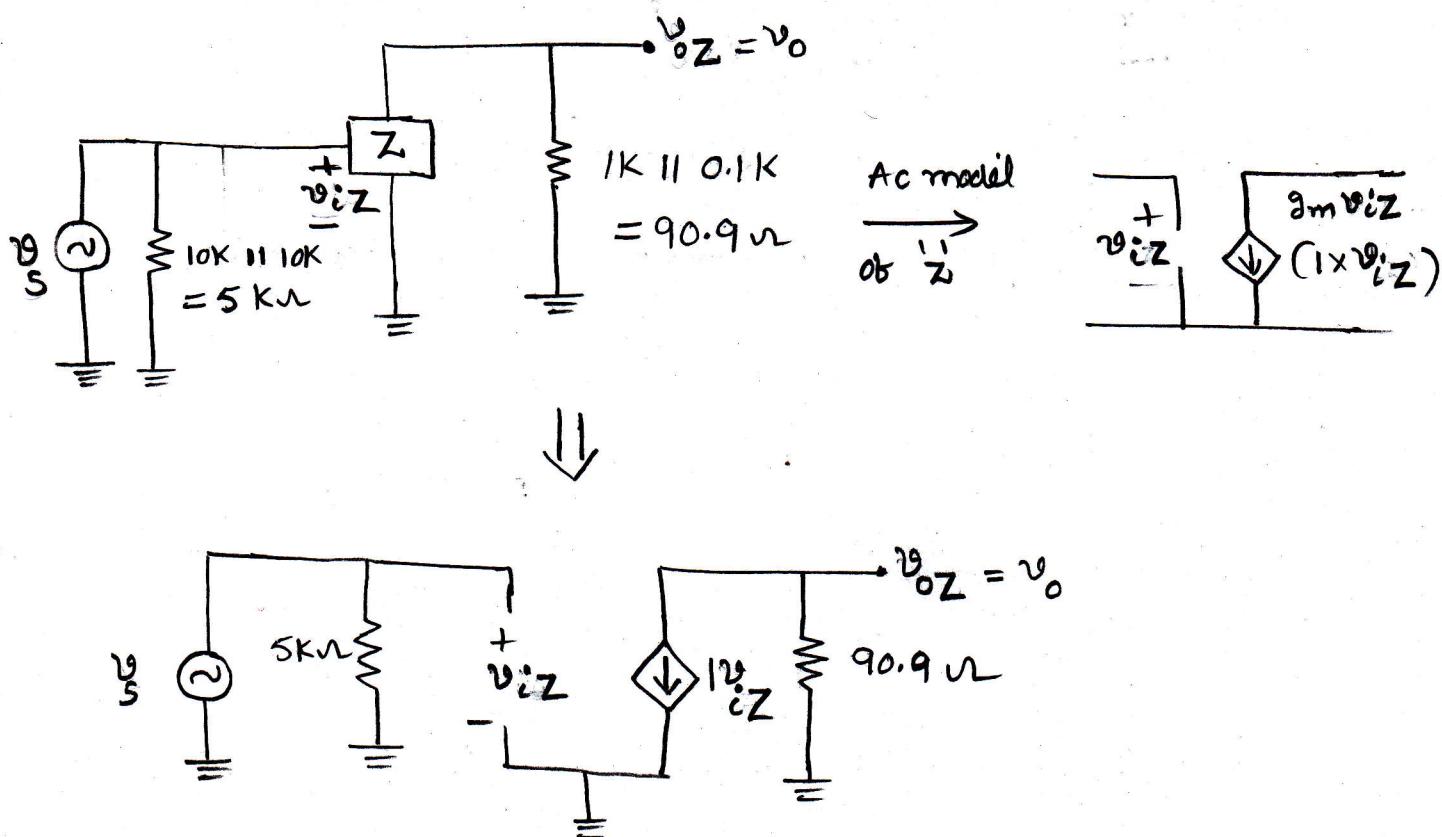
$$= 5 - 1000 [\frac{V_{IZ}}{1000} - 1.5]$$

$$= 5 - 1000 [1.501 - 1.5]$$

$$= 5 - 1000 (0.001) = 5 - 1 = 4 \text{ V}$$

$$\therefore \boxed{V_{OZ} = 4.0 \text{ V}}$$

The equivalent circuit after shorting capacitors and dc sources is,



From this figure,

$$v_{iZ} = v_s$$

$$\text{and } v_o = v_oZ = -90.9 \times v_{iZ}$$

$$\therefore \text{AC voltage gain, } A_V = \frac{v_oZ}{v_{iZ}} = -\frac{90.9 v_{iZ}}{v_{iZ}}$$

$$\boxed{\therefore A_V = -90.9}$$