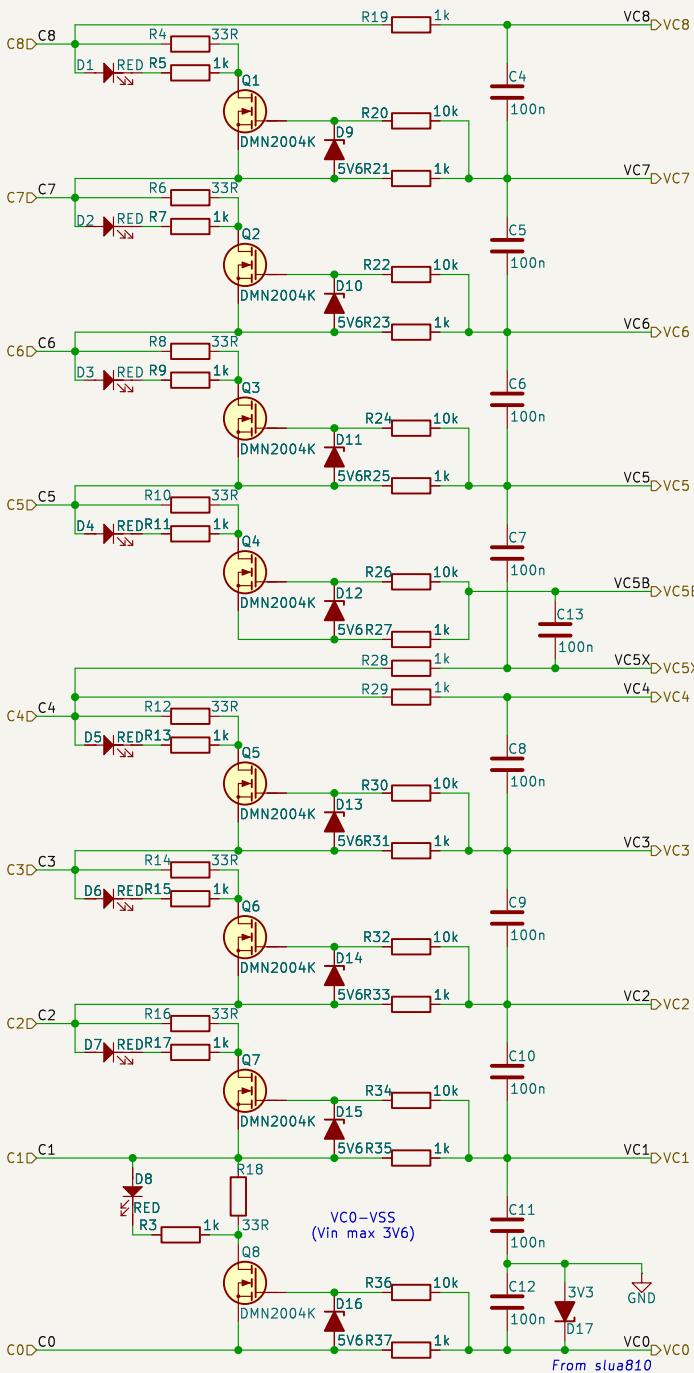


Passive Balancing Circuit



TP41	VC8
TP42	VC7
TP43	VC6
TP44	VC5
TP45	VC5B
TP46	VC5X
TP47	VC4
TP48	VC3
TP49	VC2
TP50	VC1
TP51	VC0

Sheet: /BMS Protection Board/Passive Balancing Circuit/
File: Passive Balancing Circuit.kicad_sch

Title:

Size: A4 Date:

KiCad E.D.A. 9.0.4

Rev:
Id: 3/33

A

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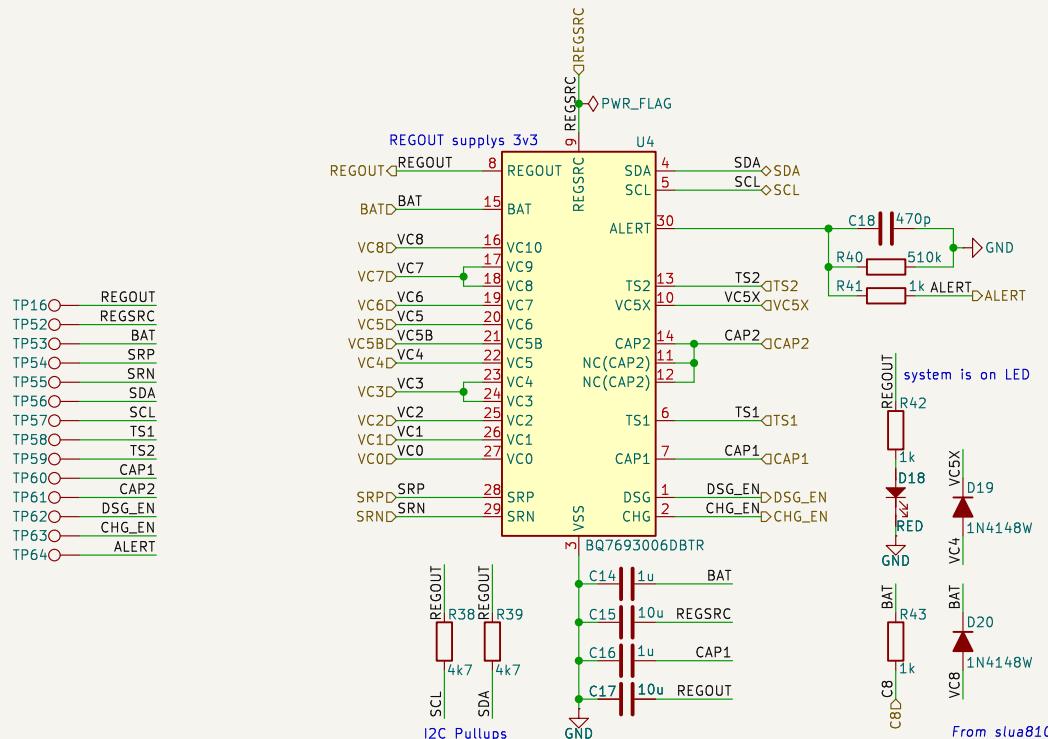
B

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Balancing & BMC (AFE)

Sheet: /BMS Protection Board/Balancing - BMC (AFE)/
File: Balancing - BMC.kicad_sch

Title:

Size: A4 Date:
KiCad E.D.A. 9.0.4

Rev:
Id: 4/33

A

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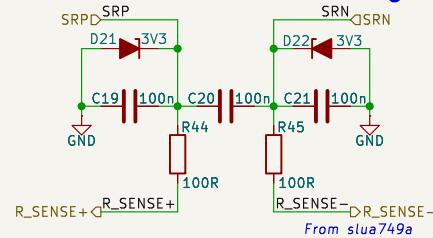
C

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Current Sense Filtering



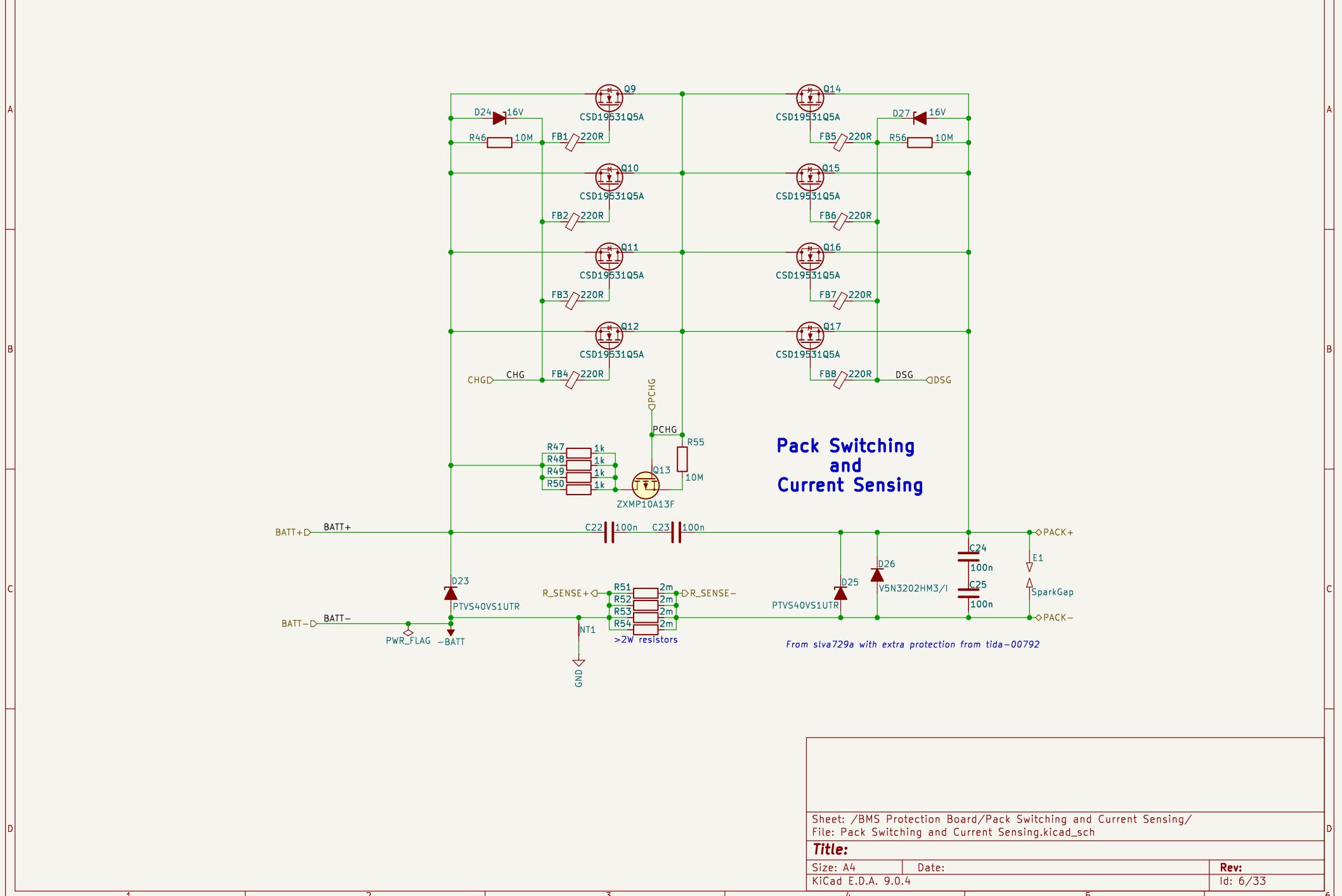
TP65 — R_SENSE+
TP66 — R_SENSE-

Sheet: /BMS Protection Board/Current Sense Filtering/
File: Current Sense Filtering.kicad_sch

Title:

Size: A4 | Date:
KiCad E.D.A. 9.0.4

Rev:
Id: 5/33



A

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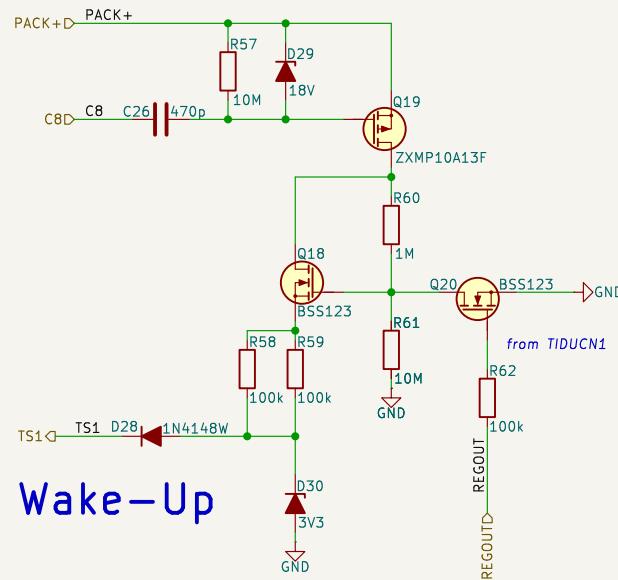
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Sheet: /BMS Protection Board/Wake Up/
File: Wake Up.kicad_sch

Title:

Size: A4 | Date:
KiCad E.D.A. 9.0.4

Rev:
Id: 7/33

A

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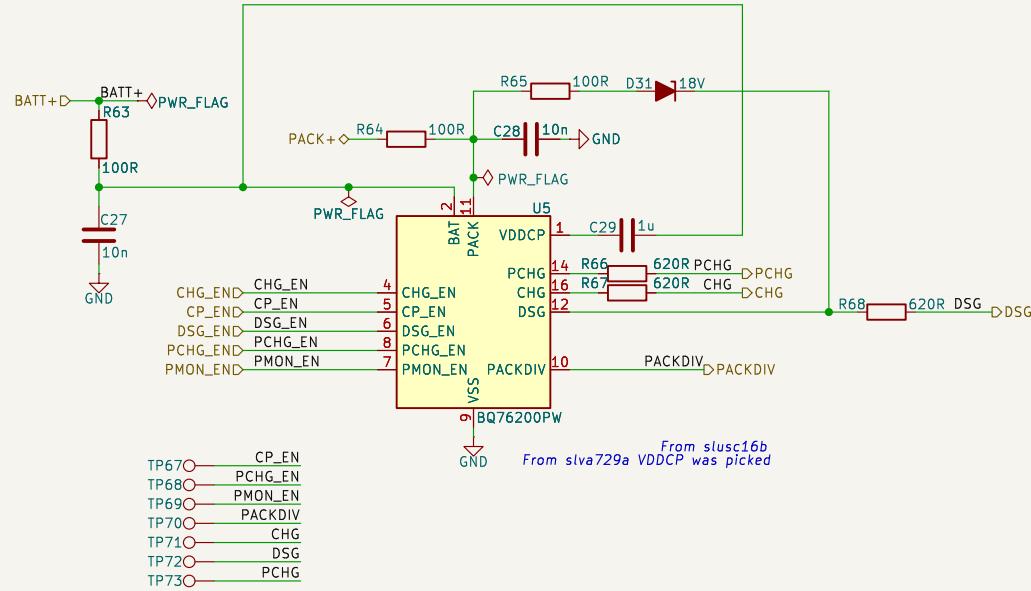
C

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High-Side Gate Driver



Sheet: /BMS Protection Board/High Side Gate Driver/
File: High Side Gate Driver.kicad_sch

Title:

Size: A4 | Date:
KiCad E.D.A. 9.0.4

Rev:
Id: 8/33

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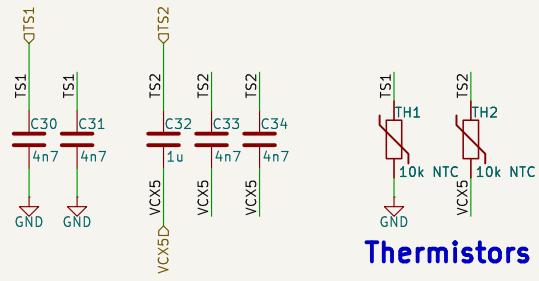
D

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Thermistors

Sheet: /BMS Protection Board/BMS Thermistors/
File: BMS Thermistors.kicad_sch

Title:

Size: A4 | Date:
KiCad E.D.A. 9.0.4

Rev:
Id: 9/33

A

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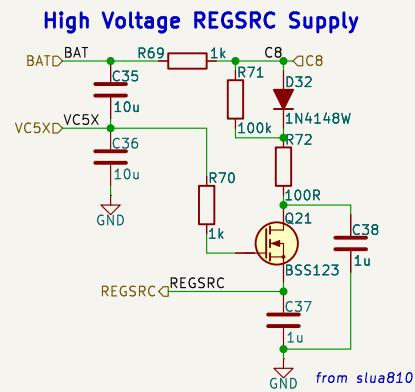
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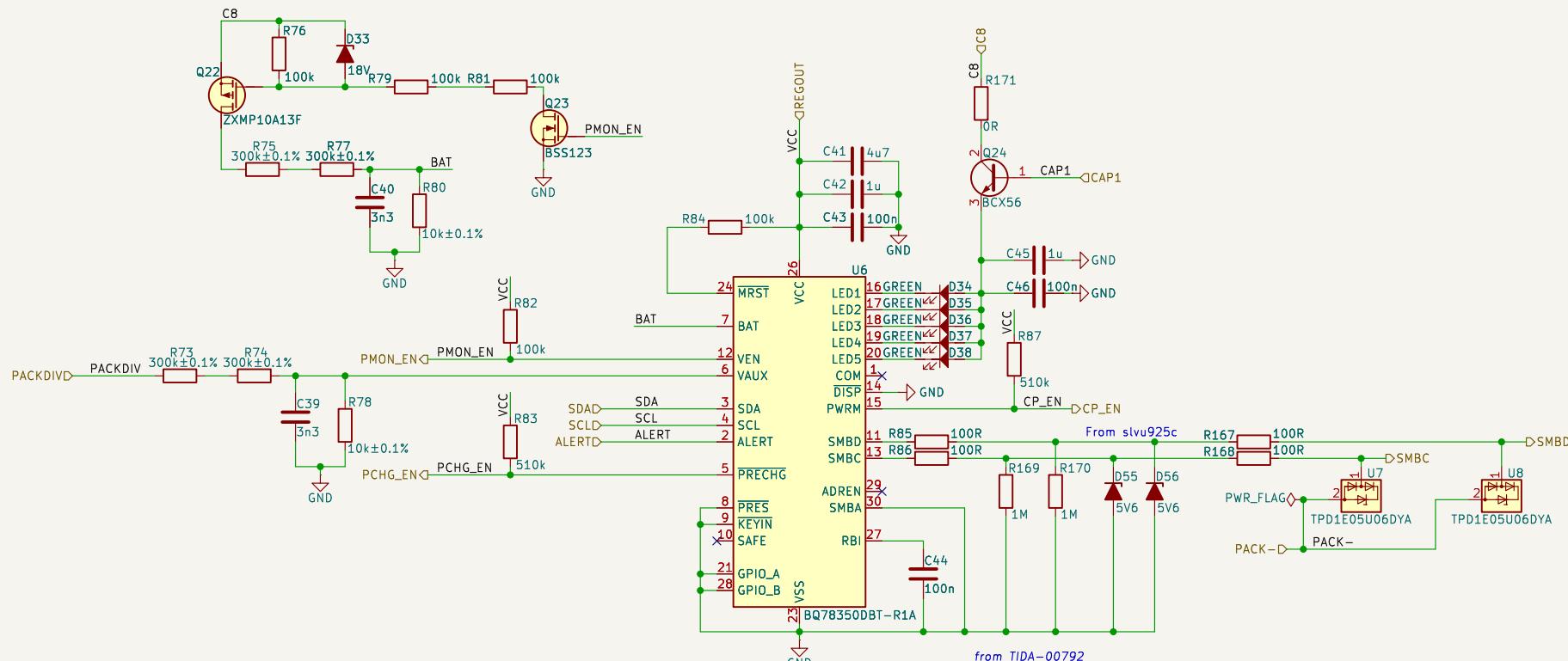
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File: High Voltage REGSRC Supply.kicad_sch

Title:

Size: A4 | Date:
KiCad E.D.A. 9.0.4

Rev:
Id: 10/33

Fuel Gauge

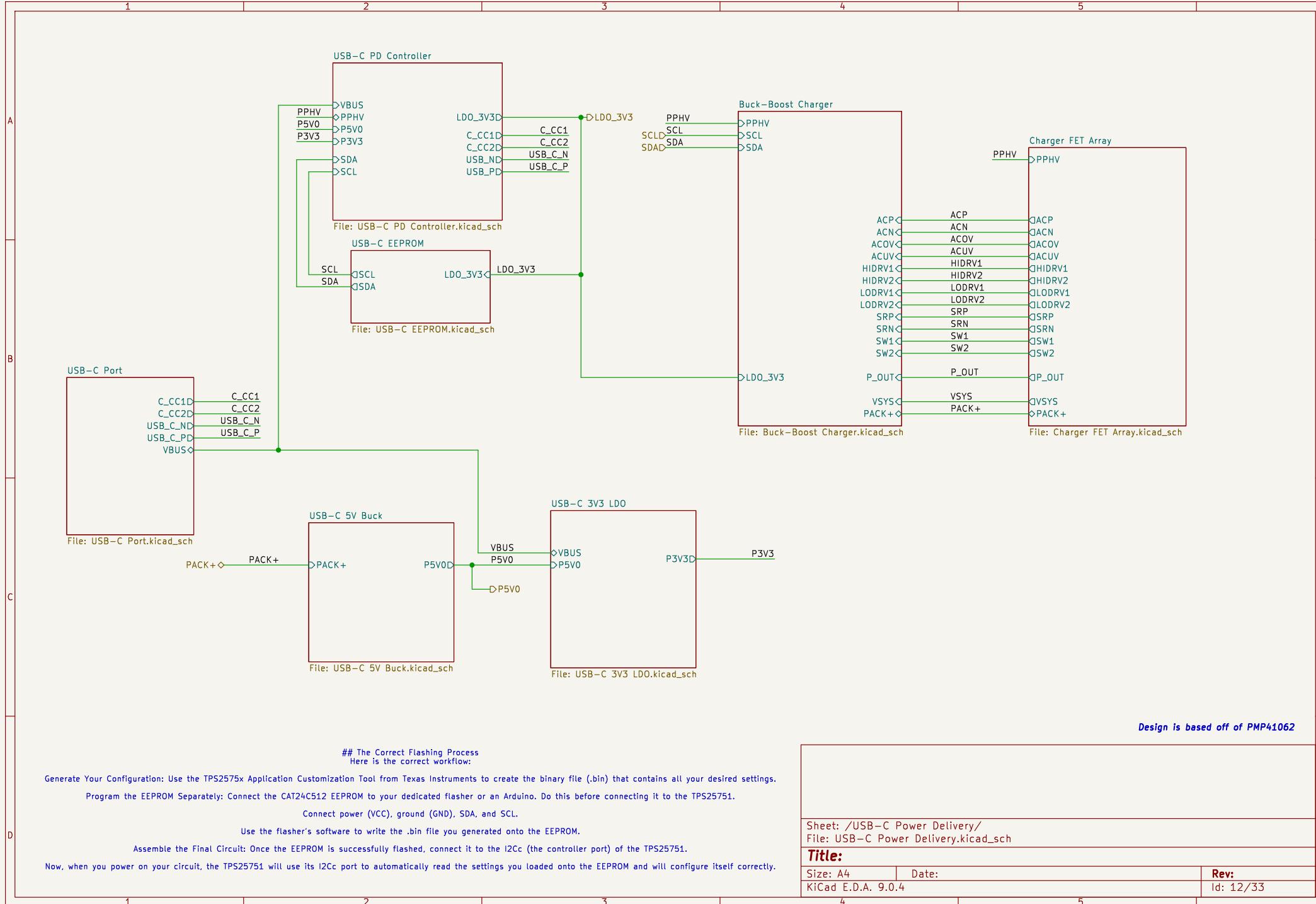


Sheet: /BMS Protection Board/Fuel Gauge/
File: Fuel Gauge.kicad_sch

Title:

Size: A4 | Date:
KiCad E.D.A. 9.0.4

Rev:
Id: 11/33



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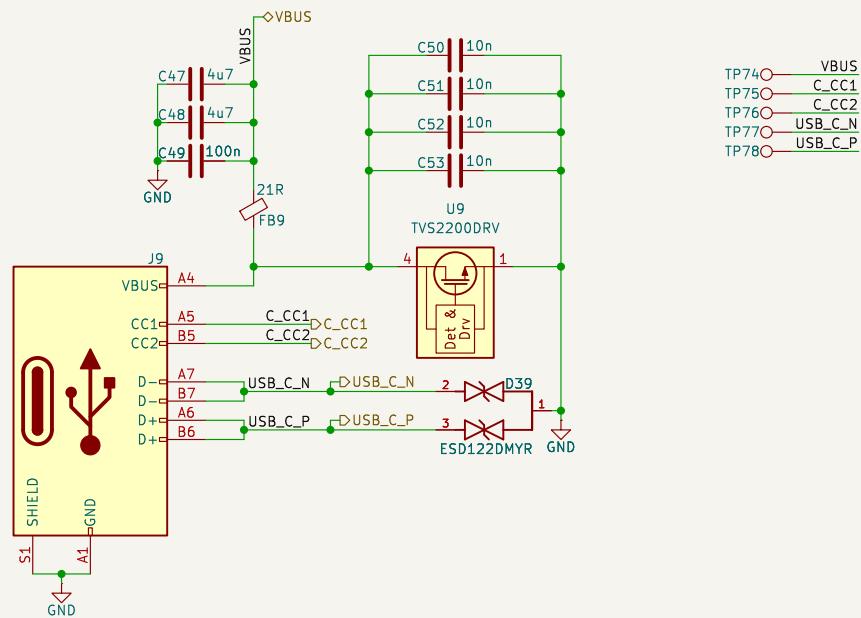
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Sheet: /USB-C Power Delivery/USB-C Port/
 File: USB-C Port.kicad_sch

Title:

Size: A4 | Date:
 KiCad E.D.A. 9.0.4

Rev:
 Id: 13/33

A

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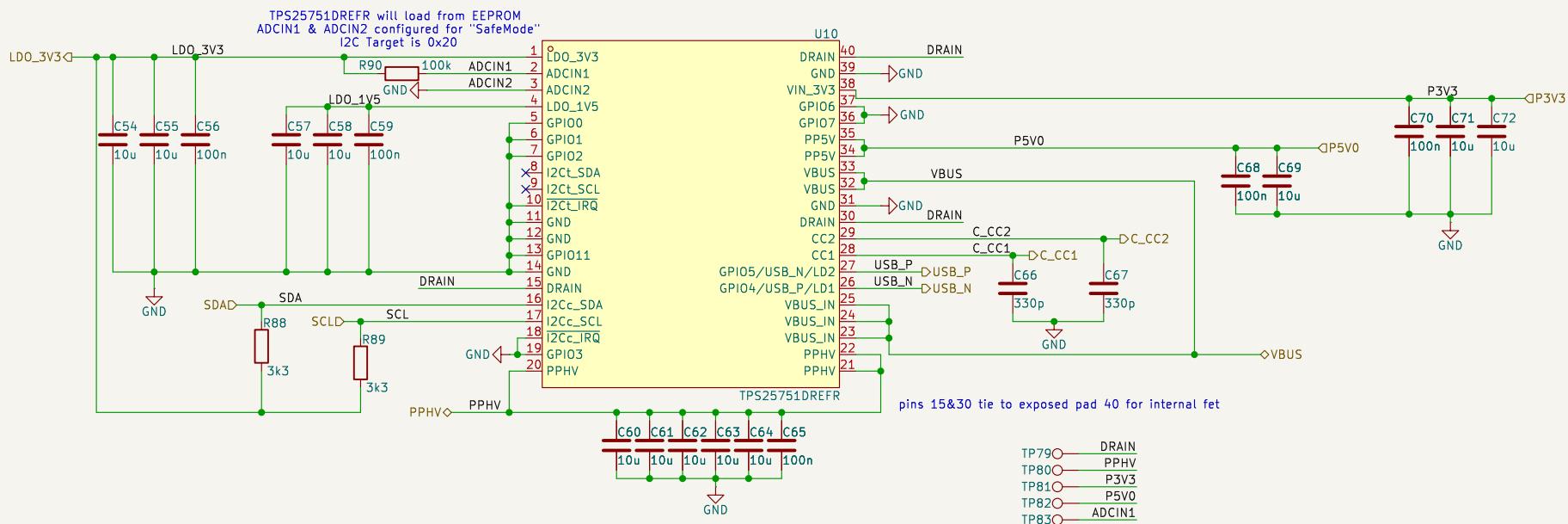
B

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Sheet: /USB-C Power Delivery/USB-C PD Controller/
File: USB-C PD Controller.kicad_sch

Title:

Size: A4 | Date:
KiCad E.D.A. 9.0.4

Rev:
Id: 14/33

A

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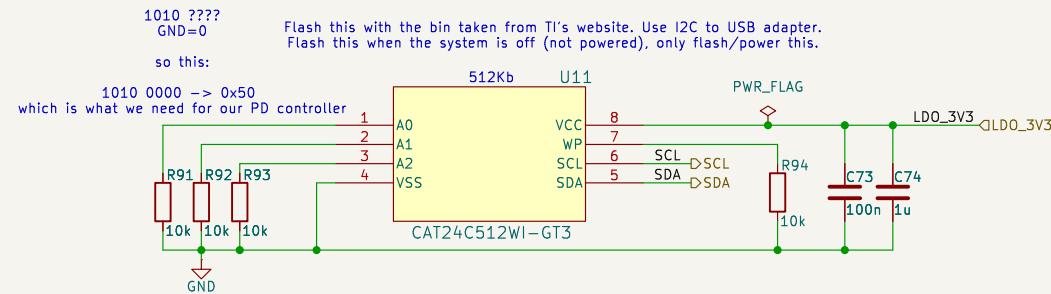
D

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Sheet: /USB-C Power Delivery/USB-C EEPROM/
File: USB-C EEPROM.kicad_sch

Title:

Size: A4 Date:
KiCad E.D.A. 9.0.4

Rev:
Id: 15/33

A

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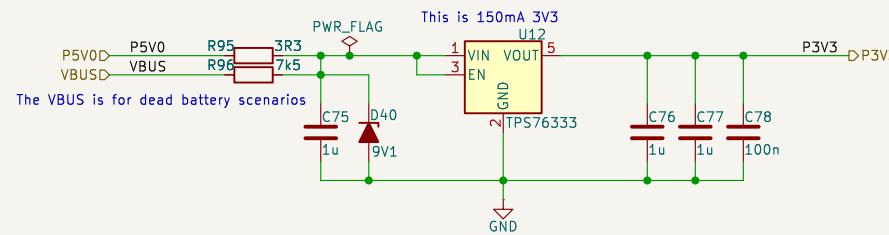
B

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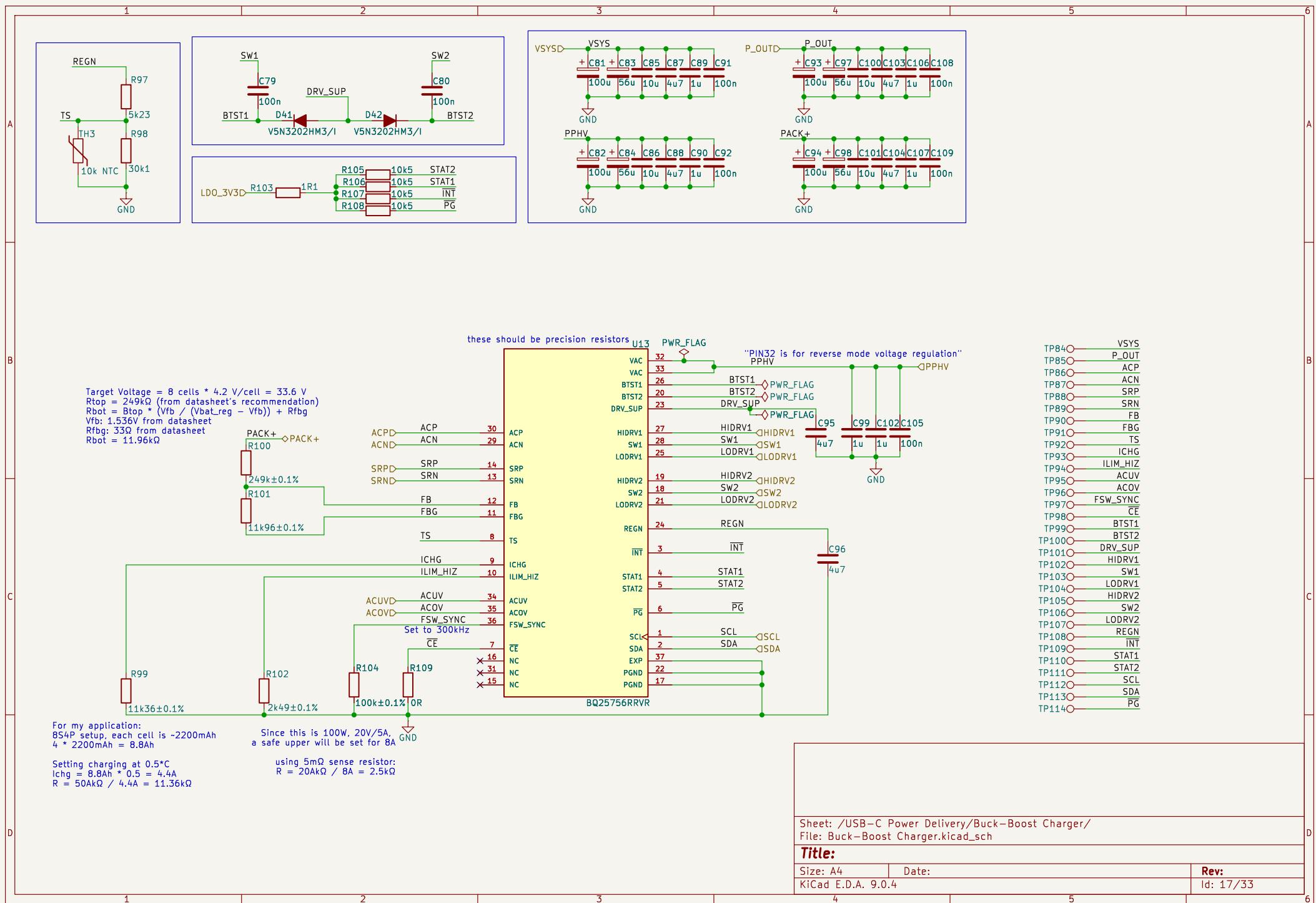


Sheet: /USB-C Power Delivery/USB-C 3V3 LDO/
File: USB-C 3V3 LDO.kicad_sch

Title:

Size: A4 Date:
KiCad E.D.A. 9.0.4

Rev:
Id: 16/33



Switching Frequency = 400kHz
 $V_{in} = 25.6V \rightarrow 33.6V$ (29.6V nominal)
 $V_{out} = 5V$
 $I_{out} = 5A$
Ripple = $1\% * 5V = 50\text{ mV}$

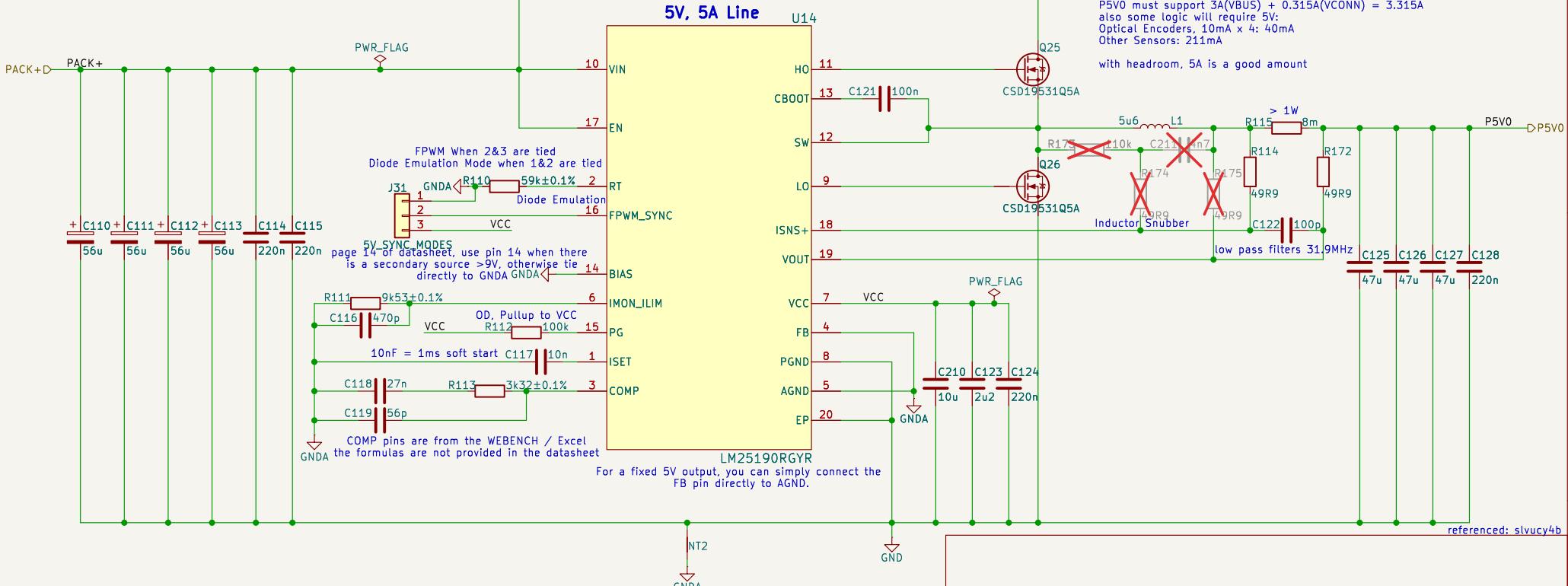
* From Datasheet: Δ_{LL} should be between 30% and 50%:
* If we do 40%, 40% * 5A = 2A
 $L_o = V_{out} / (\Delta_{LL} * F_{sw}) * (1 - V_{out} / V_{in_nom}) = 5 / (2 * 400k) * (1 - 5 / 29.6) = 5.19\mu\text{H} \rightarrow 5.6\mu\text{H}$

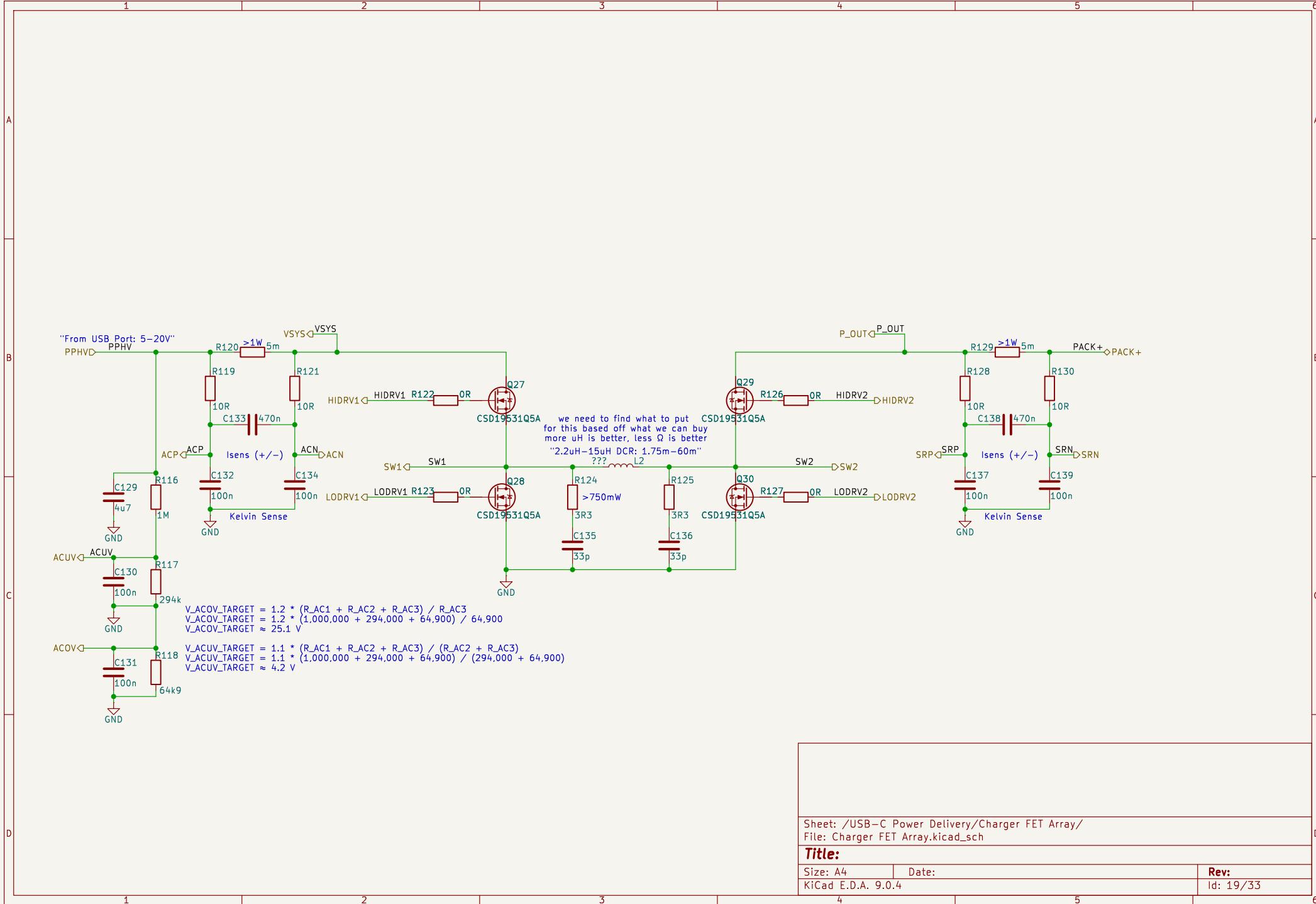
A * "The inductor's saturation current rating must be higher than the maximum peak current."
 $\Delta_{LL} = V_{out} / (L_o * F_{sw}) * (1 - V_{out} / V_{in_MAX}) = 5 / (5.6\mu\text{H} * 400k) * (1 - 5 / 33.6) = 1.89\text{A}$
* Verify with the peak current
 $L_{LO}(PK) = L_o + \Delta_{LL} / 2 = 5 + 1.89 / 2 = 5.945$
*** The inductor selected must have a saturation current rating above 6A

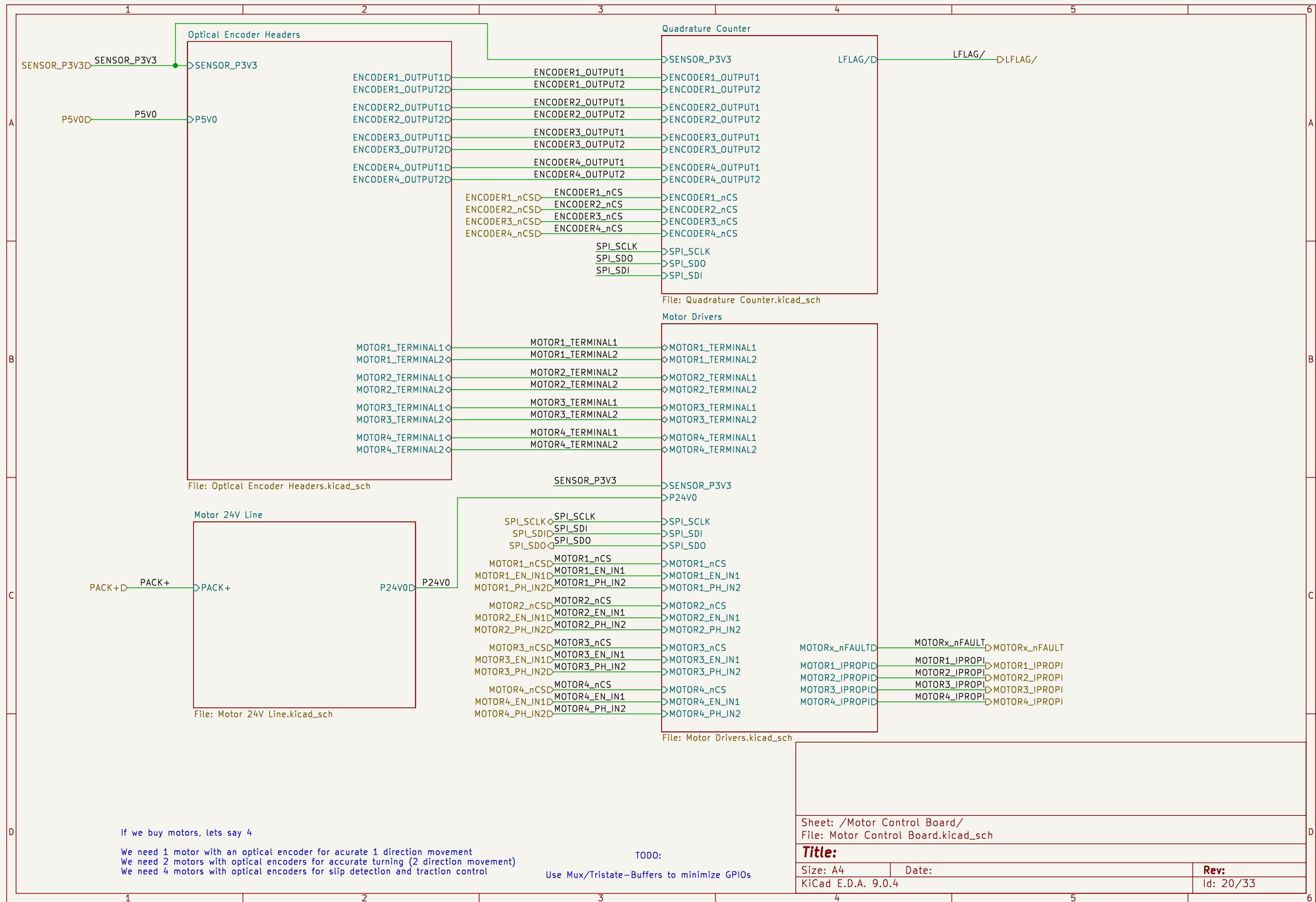
A 20% Safety margin: $5.95 * 1.2 = 7.14\text{A}$
The switching frequency is programmed by a single resistor from the RT pin to AGND.
 $R_{RT}(\text{k}\Omega) = ((10^6 / F_{sw}\{\text{kHz}\}) - 59) / 41 = ((10^6 / 400) - 59) / 41 = 59.54\text{k}\Omega = 59\text{k}$

(pin 6)
* From datasheet:
* $V_{refi} = 1\text{V}$
* $I_{IMON_OFFSET} = 25\mu\text{A}$
* $gm_{IMON} = 2\mu\text{A}/\text{mV}$
 $R_{IMON} = V_{refi} / ((R_{CS} * G_{mIMON} * I_{CC}) + I_{IMON_OFFSET}) = 1 / ((8\text{m} * 2\mu\text{A} * 5) + 25\mu\text{A}) = 9.53\text{k}\Omega$

$F_C = F_{sw} / 10 = 400\text{k} / 10 = 40\text{k}$
 $C_{IMON} = 1 / (2 * \pi * R_{IMON} * F_C) = 1 / (2 * \pi * 9.54\text{k} * 40\text{k}) = 417\text{pF} = 470\text{pF}$







Switching Frequency = 400kHz
 $V_{in} = 25.6V \rightarrow 33.6V$ (29.6V nominal)
 $V_{out} = 24V$
 $I_{out} = 20A$
Ripple = $1\% * 24V = 240\text{ mV}$

* From Datasheet: Delta_{LL} should be between 30% and 50%:
* If we do 30%, $30\% * 20A = 6A$
 $Lo = V_{out} / (\Delta_{LL} * F_{sw}) * (1 - V_{out} / V_{in_nom}) = 24 / (6 * 400k) * (1 - 24 / 29.6) = 1.9\mu H \rightarrow 2.2\mu H$

* The inductor's saturation current rating must be higher than the maximum peak current.
 $\Delta_{LL} = V_{out} / (L_{LL} * F_{sw}) * (1 - V_{out} / V_{in_MAX}) = 24 / (2.2\mu H * 400k) * (1 - 24 / 33.6) = 7.8A$
* Verify with the peak current
 $L_{LL}(PK) = Lo + \Delta_{LL} / 2 = 20 + 7.8 / 2 = 23.9A$
*** The inductor selected must have a saturation current rating above 24A

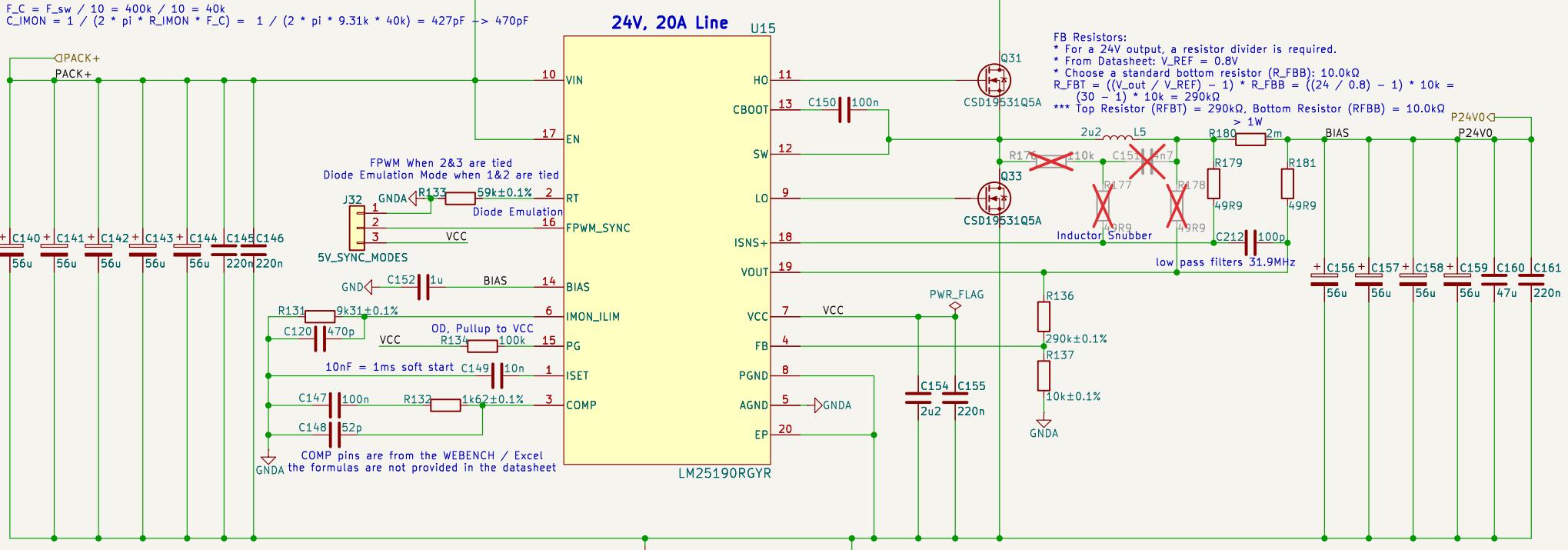
A 20% Safety margin: $23.9 * 1.2 = 28.7A$

The switching frequency is programmed by a single resistor from the RT pin to AGND.
 $R_{RT[k\Omega]} = ((10^6 / F_{sw}[\text{kHz}]) - 59) / 41 = ((10^6 / 400) - 59) / 41 = 59.54k\Omega \rightarrow 59k\Omega$

(pin 6)

* From datasheet:
* $V_{refl} = 1V$
* $I_{IMON_OFFSET} = 25\mu A$
* $gm_{IMON} = 2\mu A/\text{mV}$
 $R_{IMON} = V_{refl} / ((R_{CS} * G_{mIMON} * I_{CC}) + I_{IMON_OFFSET}) = 1 / ((2m * 2\mu A * 20) + 25\mu A) = 9.26k\Omega \rightarrow 9.31k\Omega$ (standard E96)

$F_C = F_{sw} / 10 = 400k / 10 = 40k$
 $C_{IMON} = 1 / (2 * \pi * R_{IMON} * F_C) = 1 / (2 * \pi * 9.31k * 40k) = 427\text{ pF} \rightarrow 470\text{ pF}$



Current Sense Resistor:
* From Datasheet $V_{CS_TH} = 60\text{ mV}$
 $R_s = V_{CS_TH} / I_{PEAKCL} = 60mV / 28.7A = 2.09m\Omega \rightarrow 2.0m\Omega$ is a standard resistor

* C_{out}
Transient response will dominate capacitor selection for a high-current design.
A practical choice for a 20A output would be at least 220μF effective capacitance. $\rightarrow 220\mu F$
* $I_{CO(RMS)} = \Delta_{LL} / 12(1/2) = 7.8 / 3.46 = 2.25\text{A(RMS)}$
*** The Capacitor Bank must be rated for > 2.25A for its ripple current

C_{in} :
 $D_{max} = V_{out} / V_{in(MIN)} = 24 / 25.6 = 0.938$
 $\Delta_{LL}(@V_{in_MIN}) = 24 / (2.2\mu H * 400k) * (1 - 24 / 25.6) = 1.7A$
 $I_{CIN(RMS)} = \sqrt{D * ((1 - D) * I_{out}^2 + \Delta_{LL}^2 / 12)} = \sqrt{0.938 * ((1 - 0.938) * 20^2 + 1.7^2 / 12)} = 4.88A(\text{RMS})$
 $\Delta_{LL} / V_{Supply} = 1\% * 25.6V = 0.256V = 0.25V$
 $C_{in} \geq (I_{out} * D * (1 - D)) / (F_{sw} * \Delta_{LL} / V_{Supply}) = (20 * 0.938 * (1 - 0.938)) / (400k * 0.25) = 11.6\mu F \rightarrow 22\mu F$ effective

Soft Start:
* From datasheet: I_{ISET} (internal) is 10μA
* $\Delta_{LL} / V = 1V$ (voltage ramp from 0 to 1)
For a slower start on this high-power line, let's select $C_{ISET} = 22nF$
 $T_{SSCC} = (C_{ISET} * \Delta_{LL} / V) / I_{ISET} = (22n * 1) / 10\mu A = 2.2ms$

FB Resistors:
* For a 24V output, a resistor divider is required.
* From Datasheet: $V_{REF} = 0.8V$
* Choose a standard bottom resistor (R_{FB}): $10.0k\Omega$
 $R_{FB} = ((V_{out} / V_{REF}) - 1) * R_{FB} = ((24 / 0.8) - 1) * 10k = 30k - 10k = 290k\Omega$
*** Top Resistor (R_{FBT}) = $290k\Omega$, Bottom Resistor (R_{FBB}) = $10.0k\Omega$

Sheet: /Motor Control Board/Motor 24V Line/
File: Motor 24V Line.kicad_sch

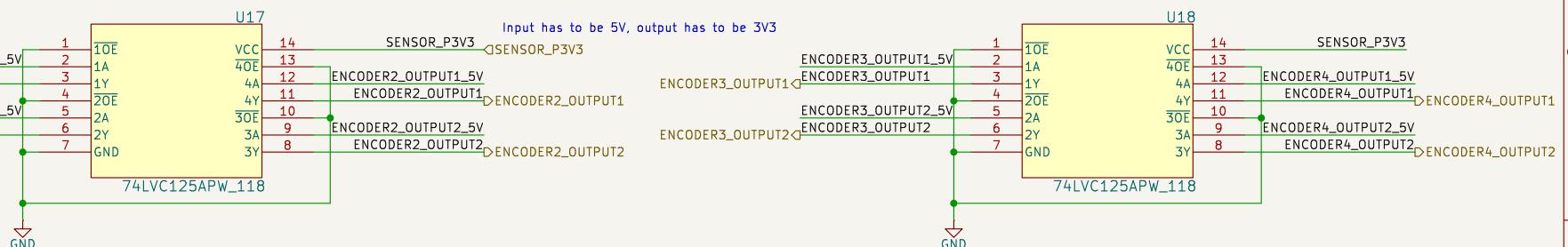
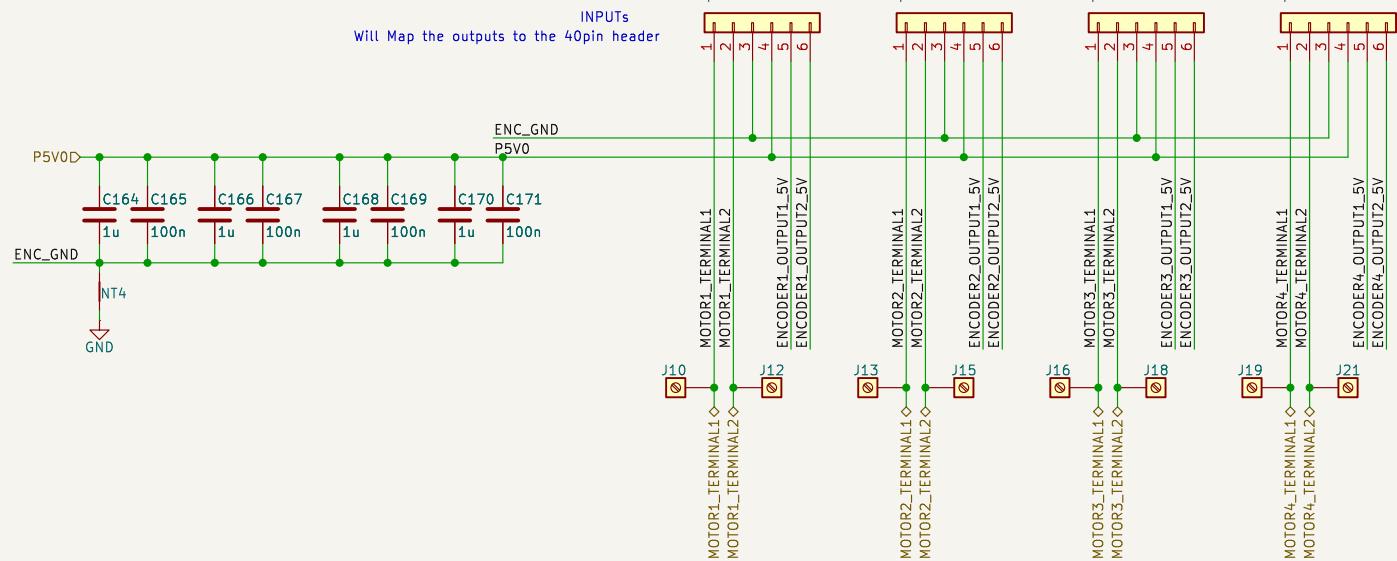
Title:

Size: A4 Date:
KiCad E.D.A. 9.0.4

Rev:
Id: 22/33

(1) Red	motor power (connects to one motor terminal)
(2) Black	motor power (connects to the other motor terminal)
(3) Green	encoder GND
(4) Blue	encoder Vcc (3.5 – 20 V)
(5) Yellow	encoder A output
(6) White	encoder B output

TP115	SENSOR_P3V3
TP116	ENC_GND
TP117	MOTOR1_TERMINAL1
TP118	MOTOR1_TERMINAL2
TP119	ENCODER1_OUTPUT1_5V
TP120	ENCODER1_OUTPUT2_5V
TP121	MOTOR2_TERMINAL1
TP122	MOTOR2_TERMINAL2
TP123	ENCODER2_OUTPUT1_5V
TP124	ENCODER2_OUTPUT2_5V
TP125	MOTOR3_TERMINAL1
TP126	MOTOR3_TERMINAL2
TP127	ENCODER3_OUTPUT1_5V
TP128	ENCODER3_OUTPUT2_5V
TP129	MOTOR4_TERMINAL1
TP130	MOTOR4_TERMINAL2
TP131	ENCODER4_OUTPUT1_5V
TP132	ENCODER4_OUTPUT2_5V
TP133	ENCODER1_OUTPUT1
TP134	ENCODER1_OUTPUT2
TP135	ENCODER2_OUTPUT1
TP136	ENCODER2_OUTPUT2
TP137	ENCODER3_OUTPUT1
TP138	ENCODER3_OUTPUT2
TP139	ENCODER4_OUTPUT1
TP140	ENCODER4_OUTPUT2



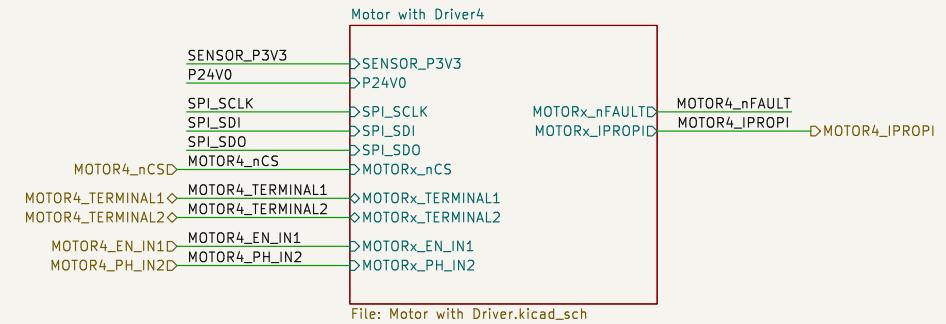
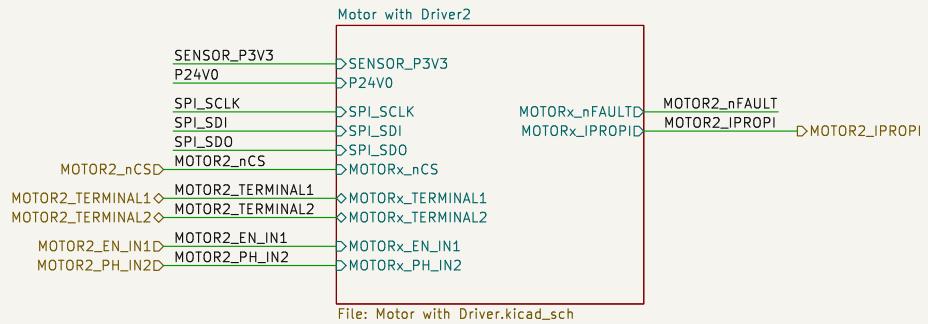
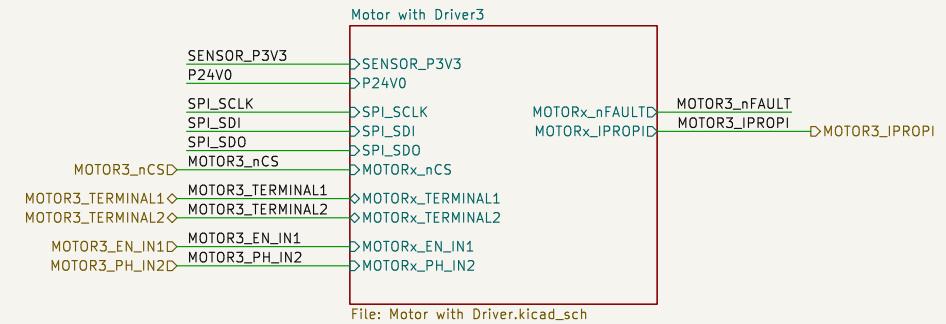
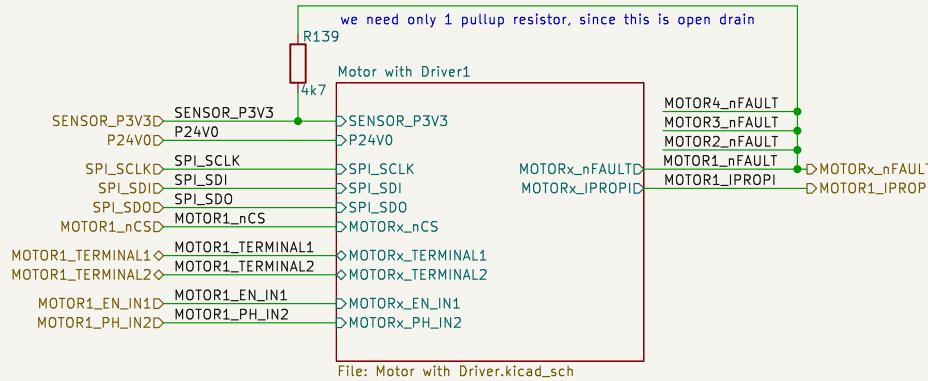
Sheet: /Motor Control Board/Optical Encoder Headers/
File: Optical_Encoder_Headers.kicad_sch

Title:

Size: A4 Date:
KiCad E.D.A. 9.0.4

Rev:
Id: 24/33

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Sheet: /Motor Control Board/Motor Drivers/
File: Motor Drivers.kicad_sch

Title:

Size: A4	Date:
KiCad E.D.A. 9.0.4	Rev: Id: 25/33

A

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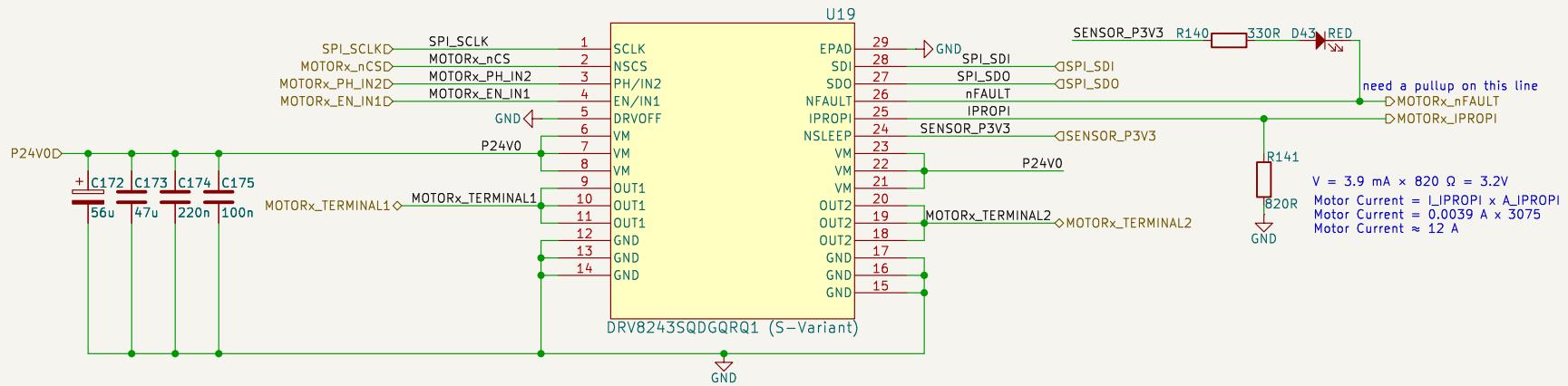
C

D

D

For the SPI (P) variant, you can set the SCLK frequency up to 10 MHz.

For the SPI (S) variant, the maximum is 10 MHz, but you should reduce it to 8 MHz if you have a 20 pF load on the SDO line.



Sheet: /Motor Control Board/Motor Drivers/Motor with Driver1/
File: Motor with Driver.kicad_sch

Title:

Size: A4 | Date:

KiCad E.D.A. 9.0.4

Rev:

Id: 26/33

A

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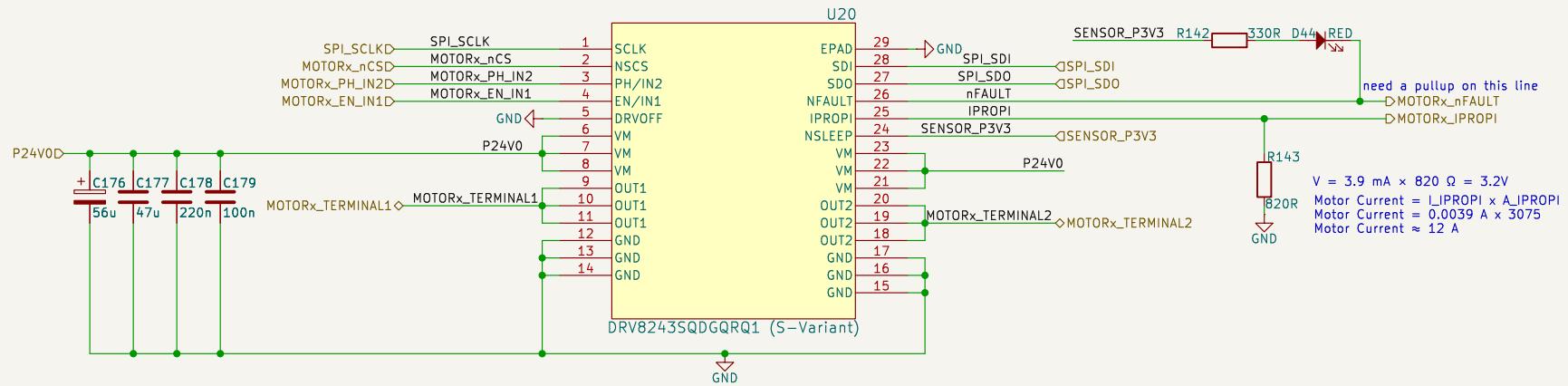
C

D

D

For the SPI (P) variant, you can set the SCLK frequency up to 10 MHz.

For the SPI (S) variant, the maximum is 10 MHz, but you should reduce it to 8 MHz if you have a 20 pF load on the SDO line.



Sheet: /Motor Control Board/Motor Drivers/Motor with Driver2/
File: Motor with Driver.kicad_sch

Title:

Size: A4 | Date:

KiCad E.D.A. 9.0.4

Rev:

Id: 27/33

A

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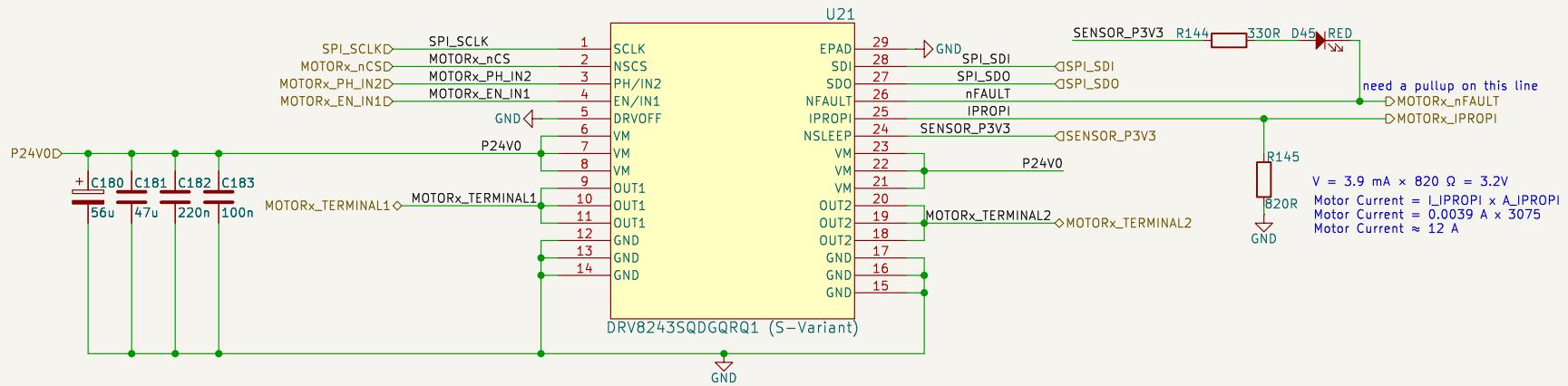
C

D

D

For the SPI (P) variant, you can set the SCLK frequency up to 10 MHz.

For the SPI (S) variant, the maximum is 10 MHz, but you should reduce it to 8 MHz if you have a 20 pF load on the SDO line.



Sheet: /Motor Control Board/Motor Drivers/Motor with Driver3/
File: Motor with Driver.kicad_sch

Title:

Size: A4 | Date:

KiCad E.D.A. 9.0.4

Rev:

Id: 28/33

A

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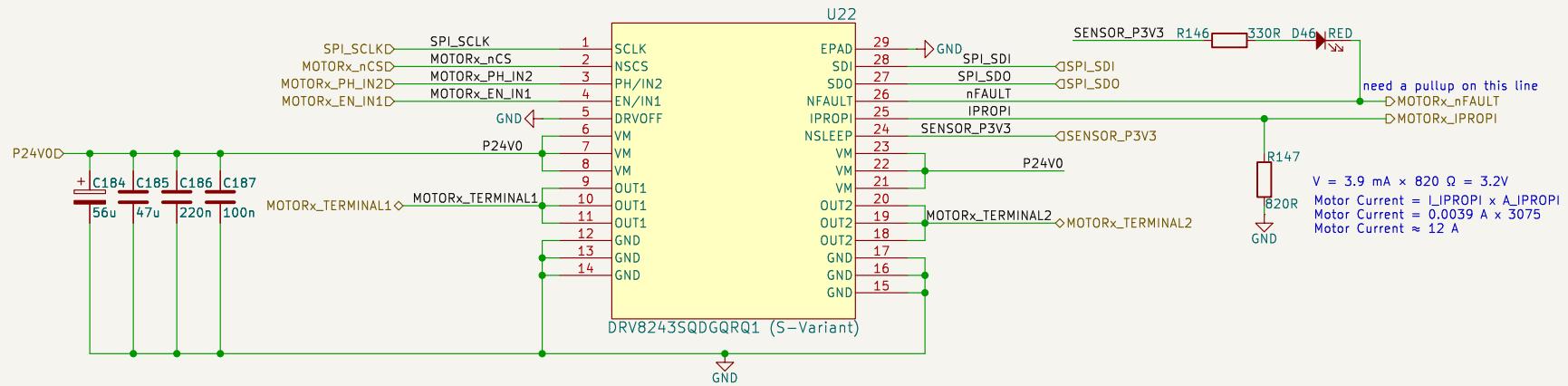
C

D

D

For the SPI (P) variant, you can set the SCLK frequency up to 10 MHz.

For the SPI (S) variant, the maximum is 10 MHz, but you should reduce it to 8 MHz if you have a 20 pF load on the SDO line.



Sheet: /Motor Control Board/Motor Drivers/Motor with Driver4/
File: Motor with Driver.kicad_sch

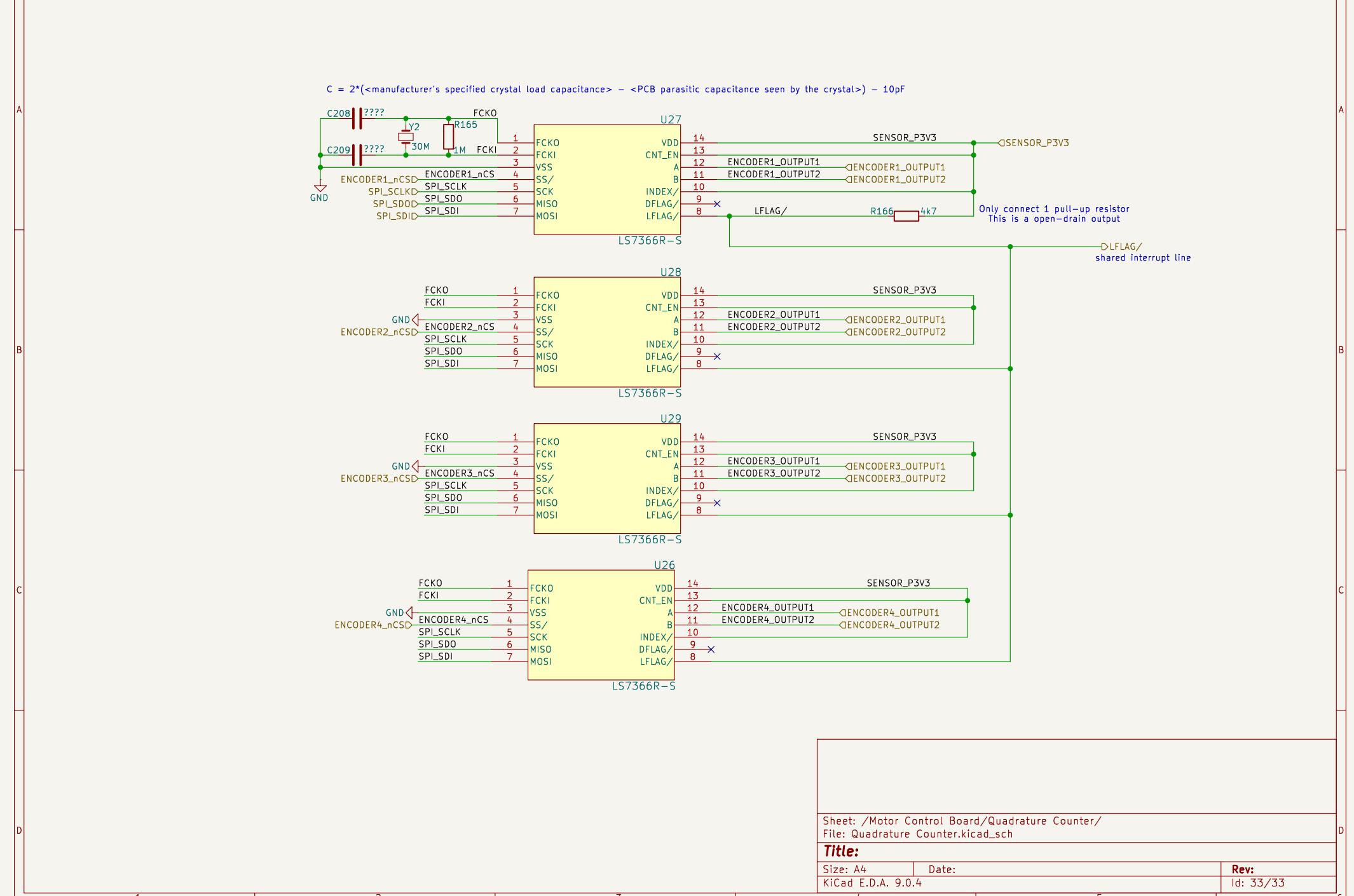
Title:

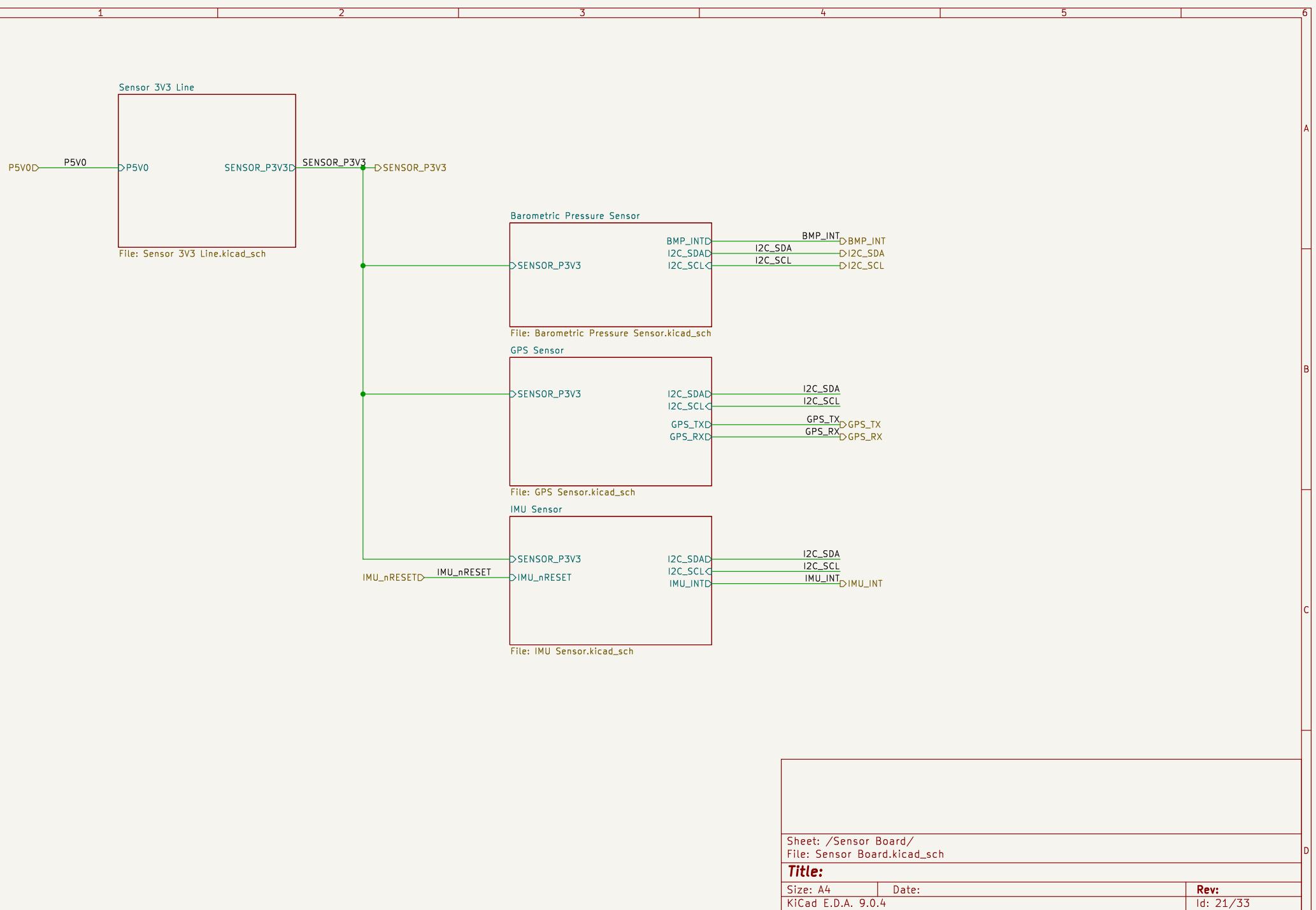
Size: A4 | Date:

KiCad E.D.A. 9.0.4

Rev:

Id: 29/33





A

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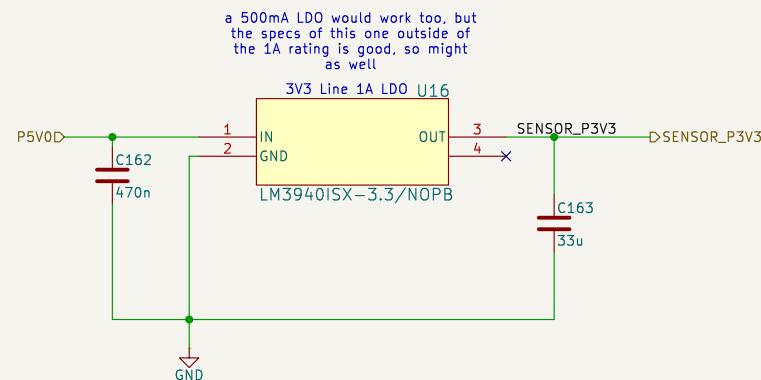
B

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C

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D



Sheet: /Sensor Board/Sensor 3V3 Line/
File: Sensor 3V3 Line.kicad_sch

Title:

Size: A4 | Date:
KiCad E.D.A. 9.0.4

Rev:
Id: 23/33

A

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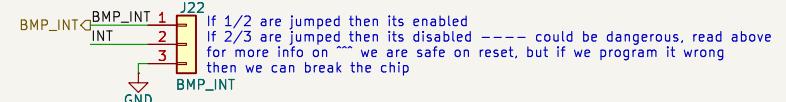
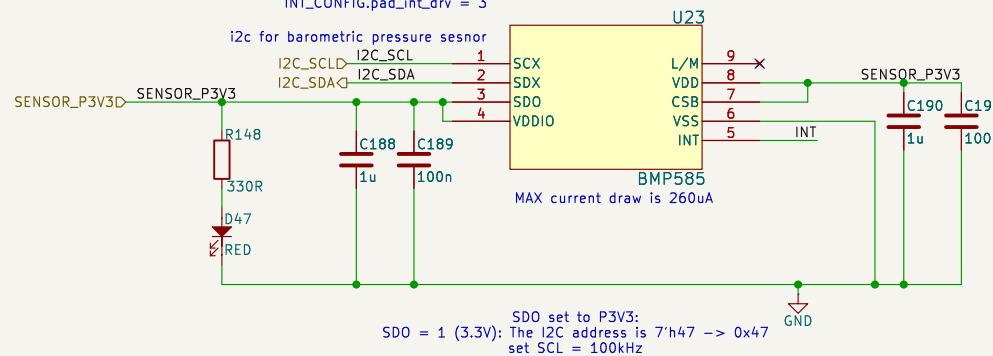
C

D

D

Per the datasheet, the unused INT pin is tied to GND to prevent a floating input.
CRITICAL: The interrupt pin must be disabled in software to prevent a short circuit.
 Ensure the 'int_en' bit in the INT_CONFIG register (0x14) remains disabled (set to 0).

Datasheet sets the IRQ to 0 on start up, so never turn it on
 PAGE49: <int_en 2bits> <int_od 2bits> <int_pol 2bits> <int_mode 2bits LSB>
 PAGE51: 8.5, setup for 0x14, <0> <1> <0> <1>
 this means: int_mode = latched, int_pol = active low, int_od = open_drain, int_en = disabled
 INT_CONFIG.int_en = 0
 INT_CONFIG.od = 1
 INT_CONFIG.pol = 0
 INT_CONFIG.mode = 1
 INT_CONFIG.pad_int_drv = 3

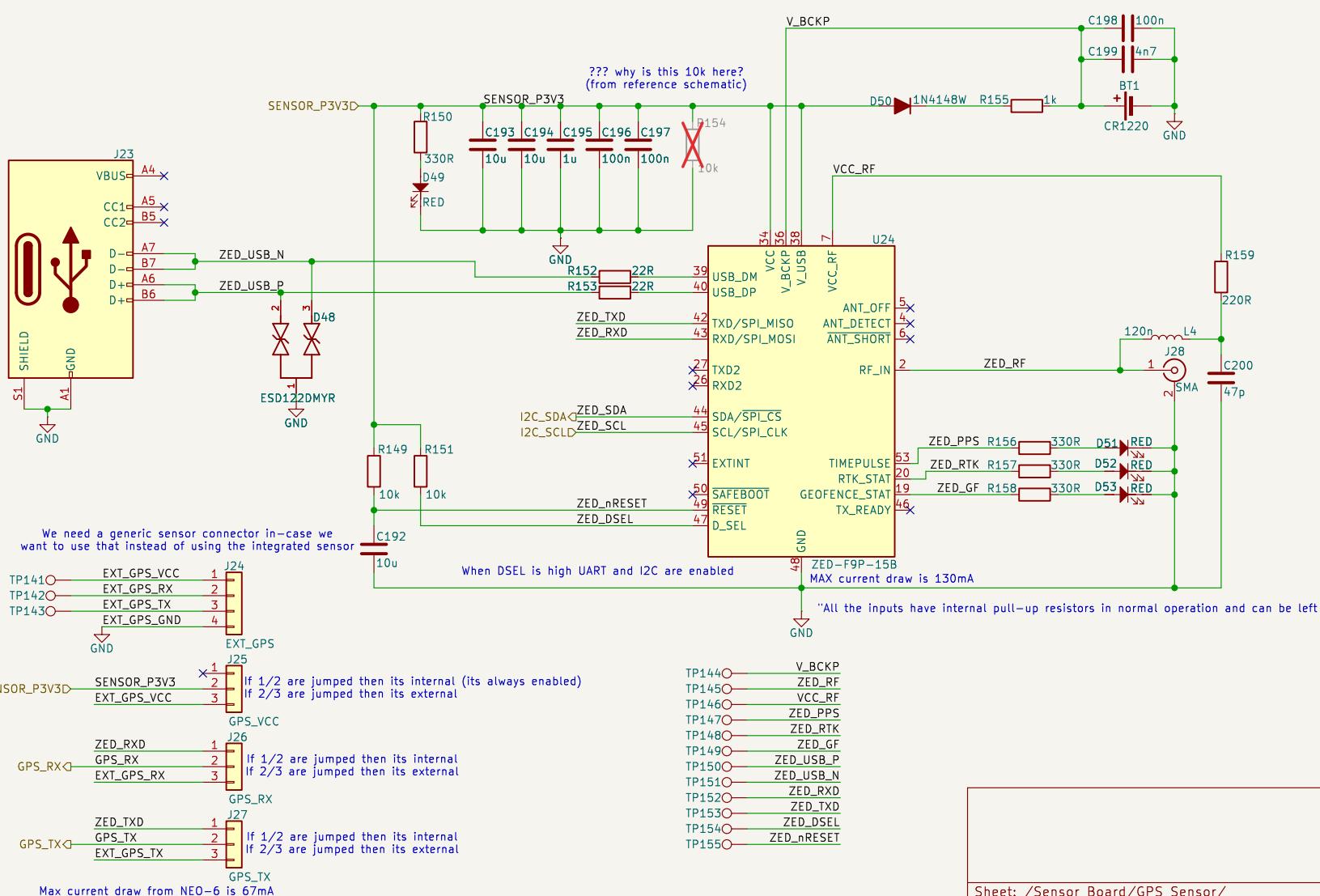


Sheet: /Sensor Board/Barometric Pressure Sensor/
 File: Barometric Pressure Sensor.kicad_sch

Title:

Size: A4 | Date:
 KiCad E.D.A. 9.0.4

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 Id: 30/33



Sheet: /Sensor Board/GPS Sensor/
File: GPS Sensor.kicad_sch

Title:

Size: A4 | Date:
KiCad E.D.A. 9.0.4

Rev:
Id: 31/33

A

A

B

B

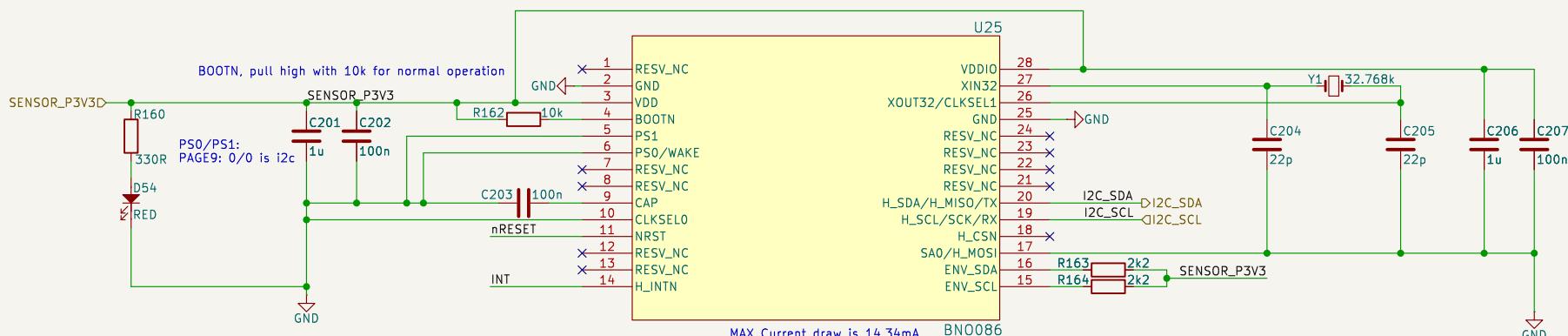
C

C

D

D

PAGE55: reflow soldering with a peak temperature up to 260°C



Sheet: /Sensor Board/IMU Sensor/
File: IMU Sensor.kicad_sch

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Rev:
Id: 32/33