

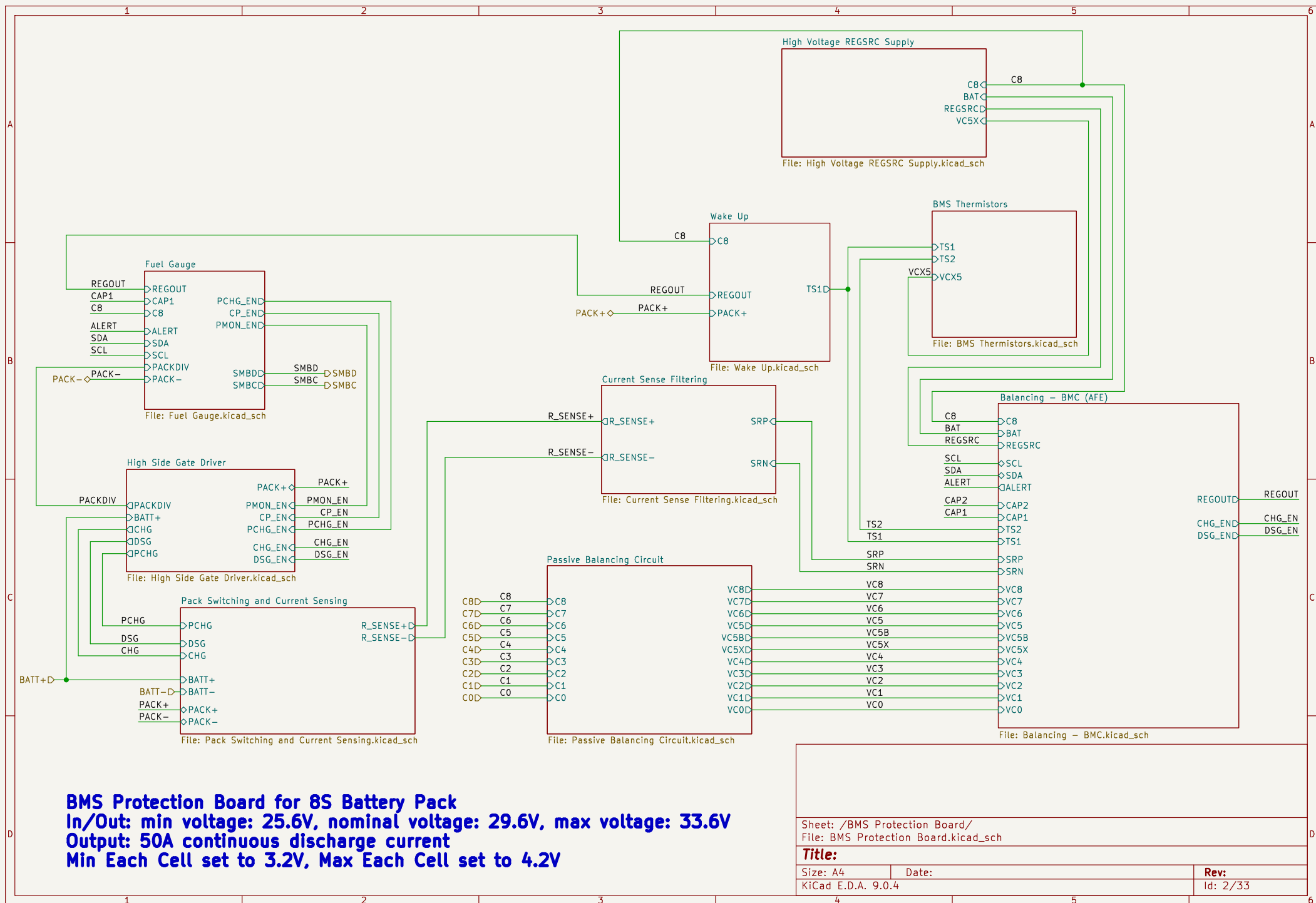
Sheet: /
File: STAR.kicad_sch

Title:

Size: A4
KiCad E.D.A. 9.0.4

Date:

Rev:
Id: 1/33



Sheet: /BMS Protection Board/
 File: BMS Protection Board.kicad_sch

Title:

Size: A4

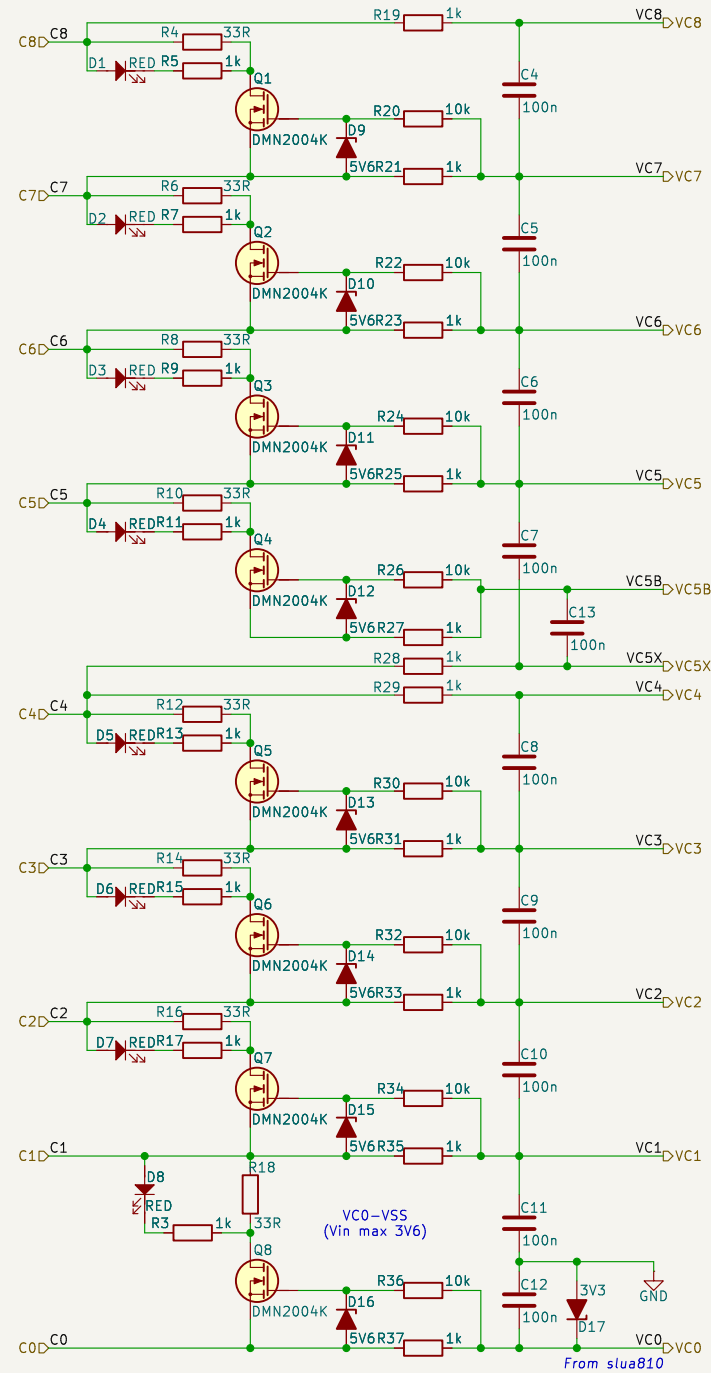
Date:

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Passive Balancing Circuit



Sheet: /BMS Protection Board/Passive Balancing Circuit/
File: Passive Balancing Circuit.kicad_sch

Title:

Size: A4

Date:

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Sheet: /BMS Protection Board/Current Sense Filtering/		D
File: Current Sense Filtering.kicad_sch		
Title:		
Size: A4	Date:	Rev:
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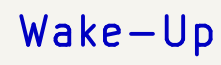


Sheet: /BMS Protection Board/Pack Switching and Current Sensing/
File: Pack Switching and Current Sensing.kicad_sch

Size: A4	Date:
KiCad E.D.A. 9.0.4	

Date:

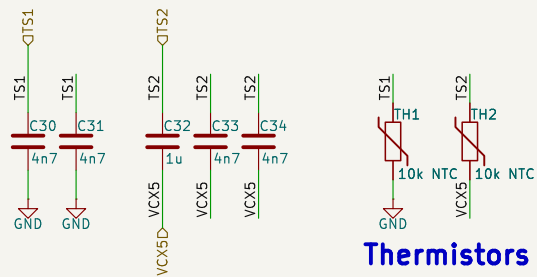
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Thermistors

Sheet: /BMS Protection Board/BMS Thermistors/
File: BMS Thermistors.kicad_sch

Title:

Size: A4

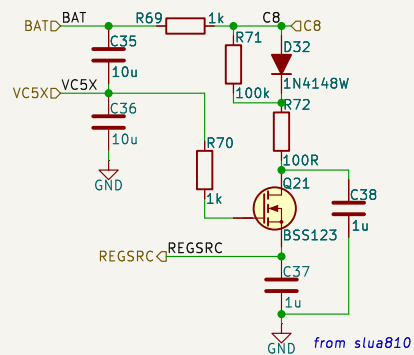
Date:

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High Voltage REGSRC Supply



Sheet: /BMS Protection Board/High Voltage REGSRC Supply/
File: High Voltage REGSRC Supply.kicad_sch

Title:

Size: A4

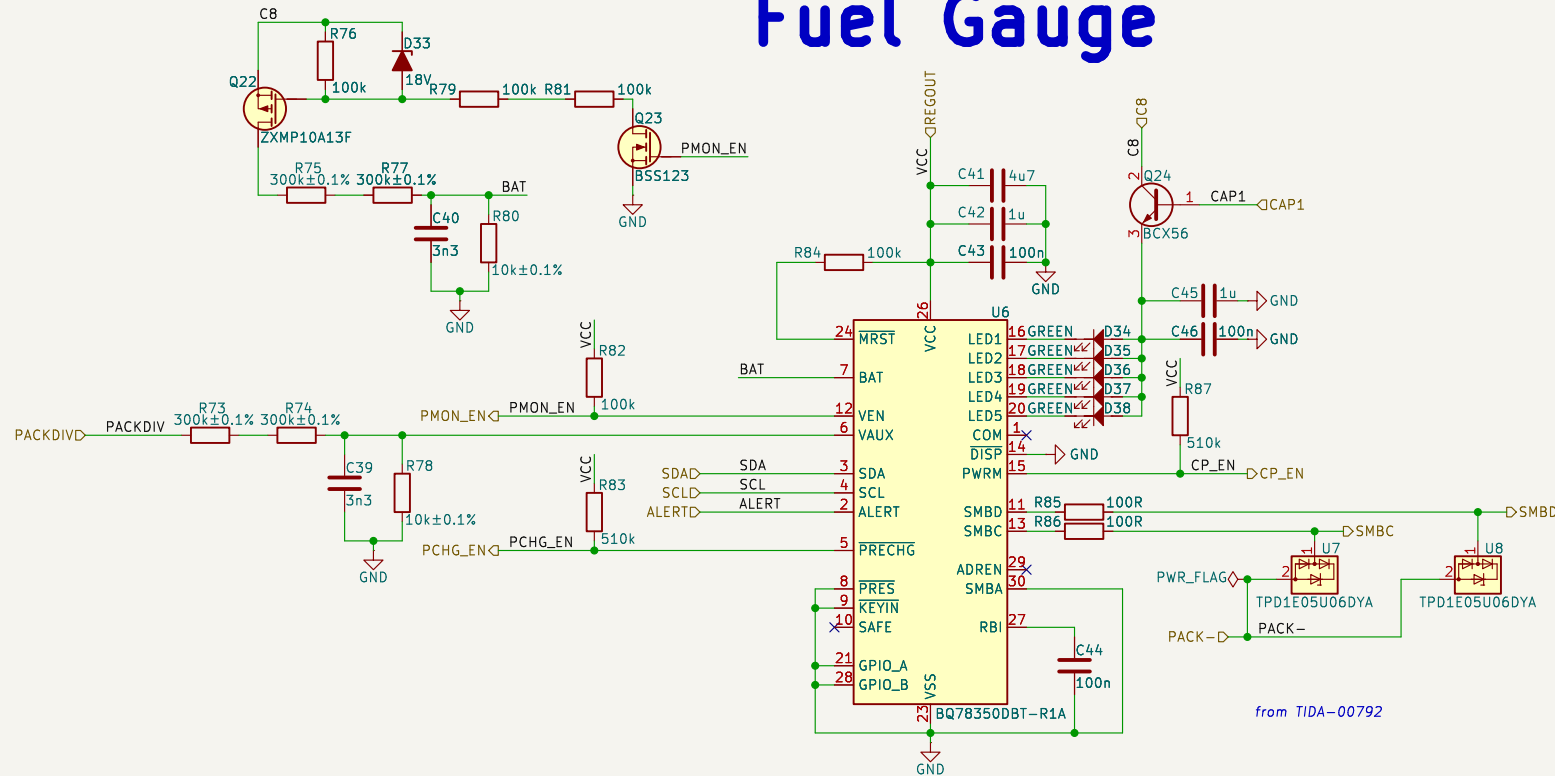
Date:

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Id: 10/33

Fuel Gauge



Flash this with TI's software, will need to connec to computer with
SMB to USB adapter, could use the expensive one from TI, but \$\$\$

Sheet: /BMS Protection Board/Fuel Gauge/
File: Fuel Gauge.kicad_sch

Title:

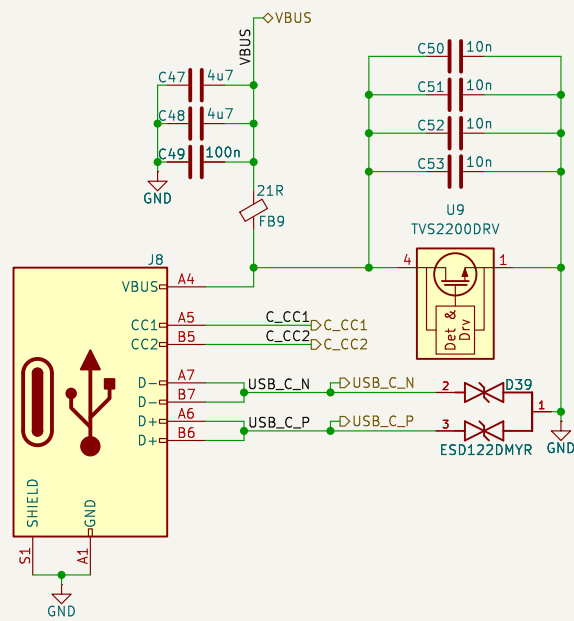
Size: A4

Date:

Rev:

KiCad E.D.A. 9.0.4

Id: 11/33



Sheet: /USB-C Power Delivery/USB-C Port/
File: USB-C Port.kicad_sch

Title:

Size: A4

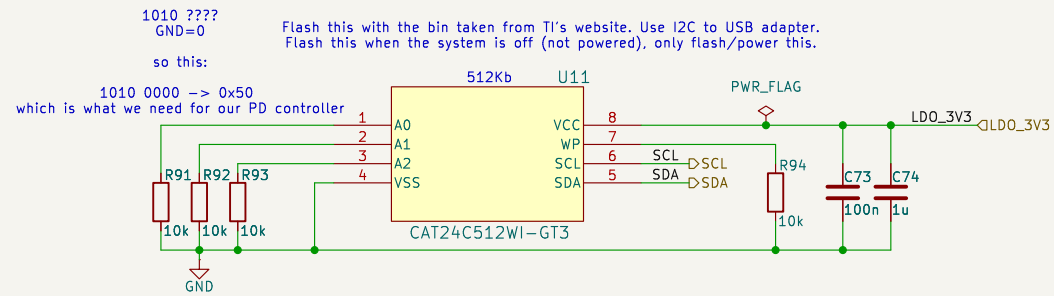
Date:

Rev:

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Sheet: /USB-C Power Delivery/USB-C EEPROM/
File: USB-C EEPROM.kicad_sch

Title:

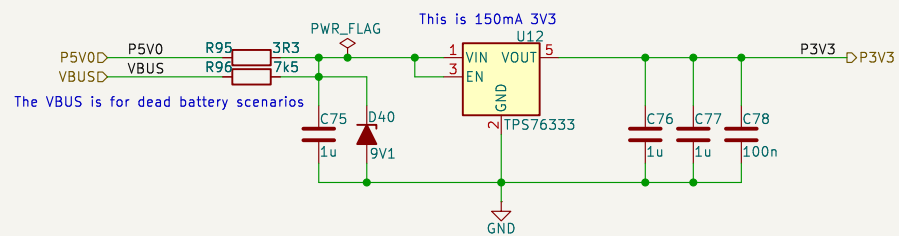
Size: A4

Date:

KiCad E.D.A. 9.0.4

Rev:

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Sheet: /USB-C Power Delivery/USB-C 3V3 LDO/
File: USB-C 3V3 LDO.kicad_sch

Title:

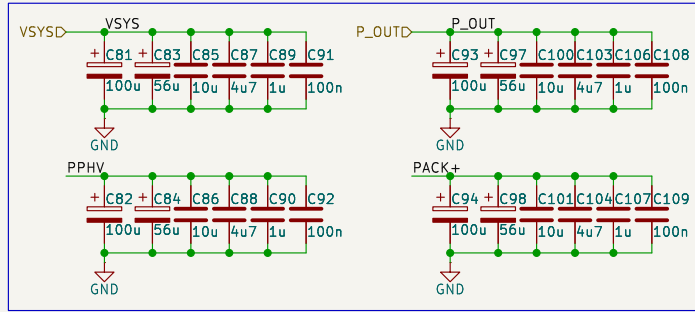
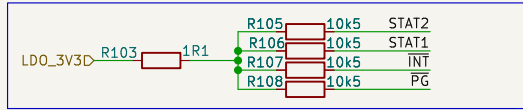
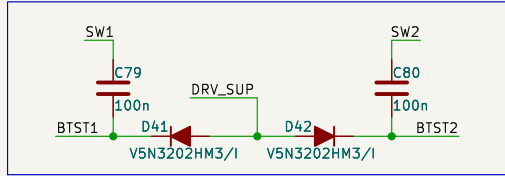
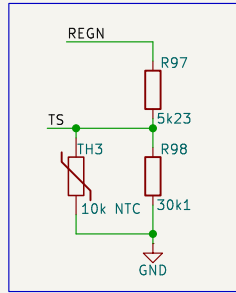
Size: A4

Date:

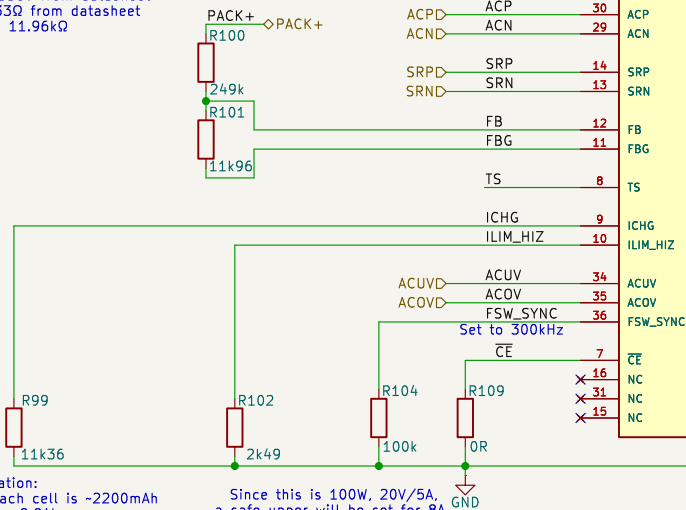
Rev:

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Target Voltage = 8 cells * 4.2 V/cell = 33.6 V
 $R_{top} = 249k\Omega$ (from datasheet's recommendation)
 $R_{bot} = B_{top} * (V_{fb} / (V_{bat_reg} - V_{fb})) + R_{fbg}$
 $V_{fb}: 1.536V$ from datasheet
 $R_{fbg}: 33\Omega$ from datasheet
 $R_{bot} = 11.96k\Omega$



For my application:
 854P setup, each cell is ~2200mAh
 $4 * 2200mAh = 8.8Ah$

Setting charging at 0.5°C
 $I_{chg} = 8.8Ah * 0.5 = 4.4A$
 $R = 50k\Omega / 4.4A = 11.36k\Omega$

Since this is 100W, 20V/5A,
 a safe upper will be set for 8A
 using 5mΩ sense resistor:
 $R = 20k\Omega / 8A = 2.5k\Omega$

Sheet: /USB-C Power Delivery/Buck-Boost Charger/
 File: Buck-Boost Charger.kicad_sch

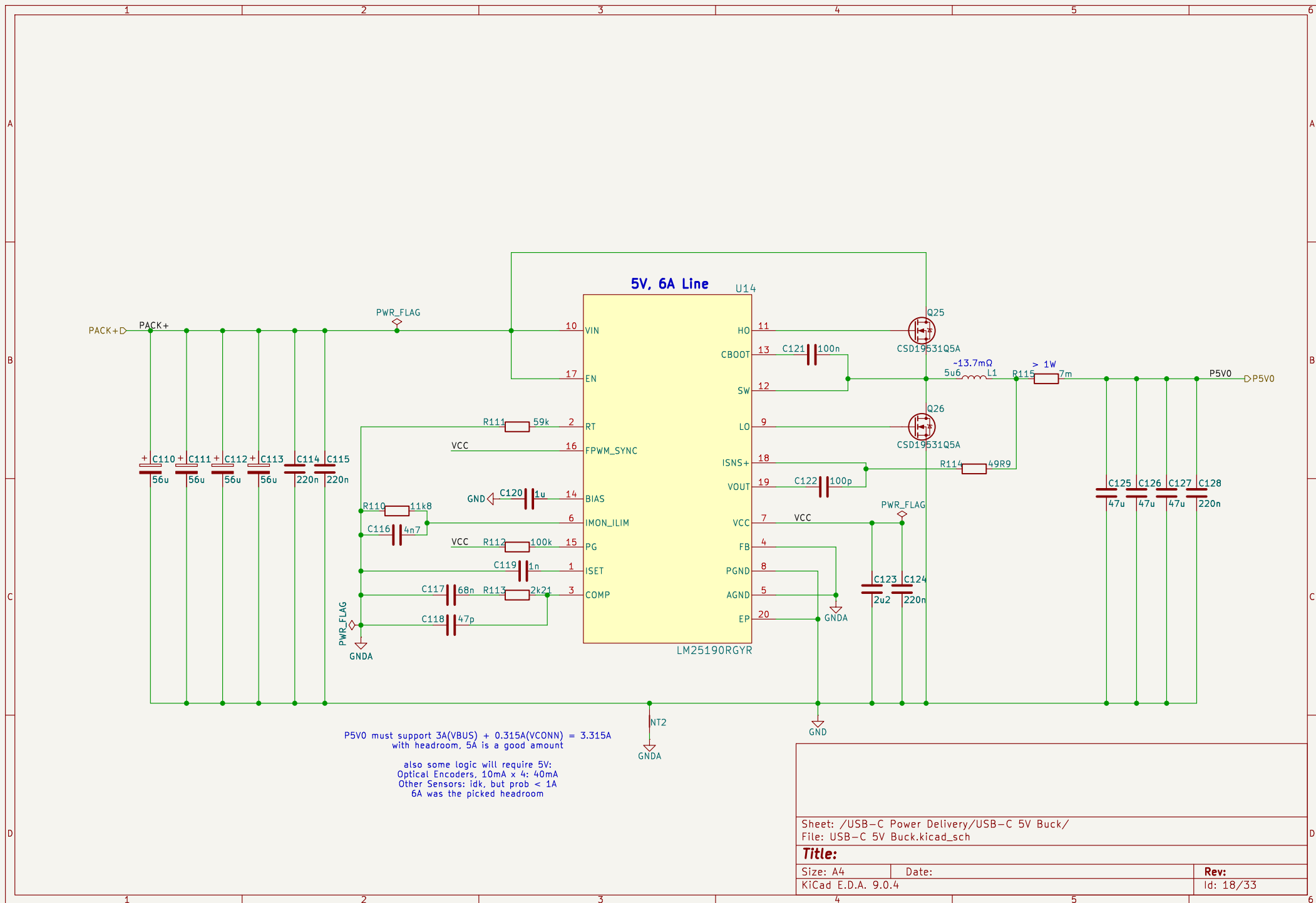
Title:

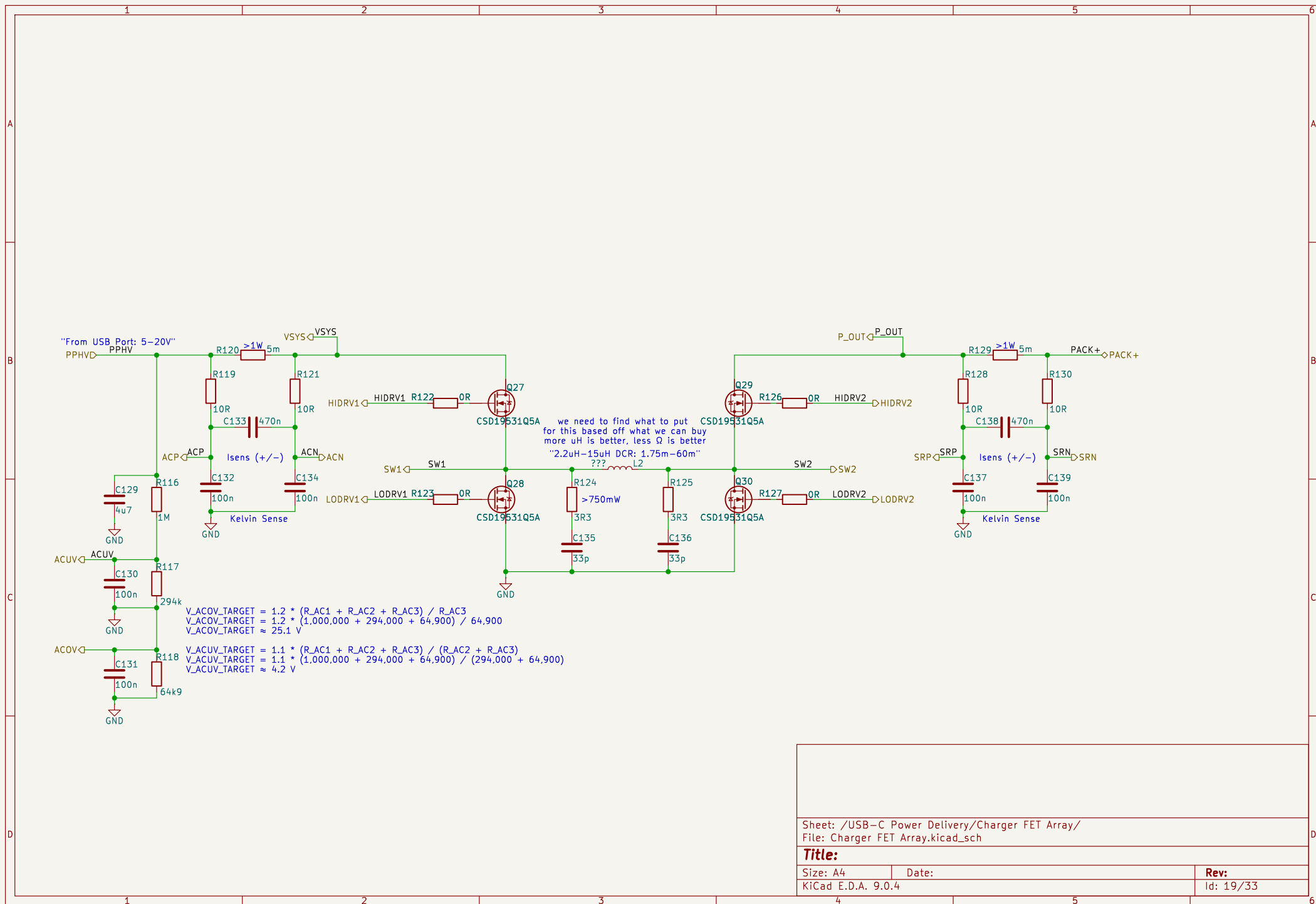
Size: A4 Date:

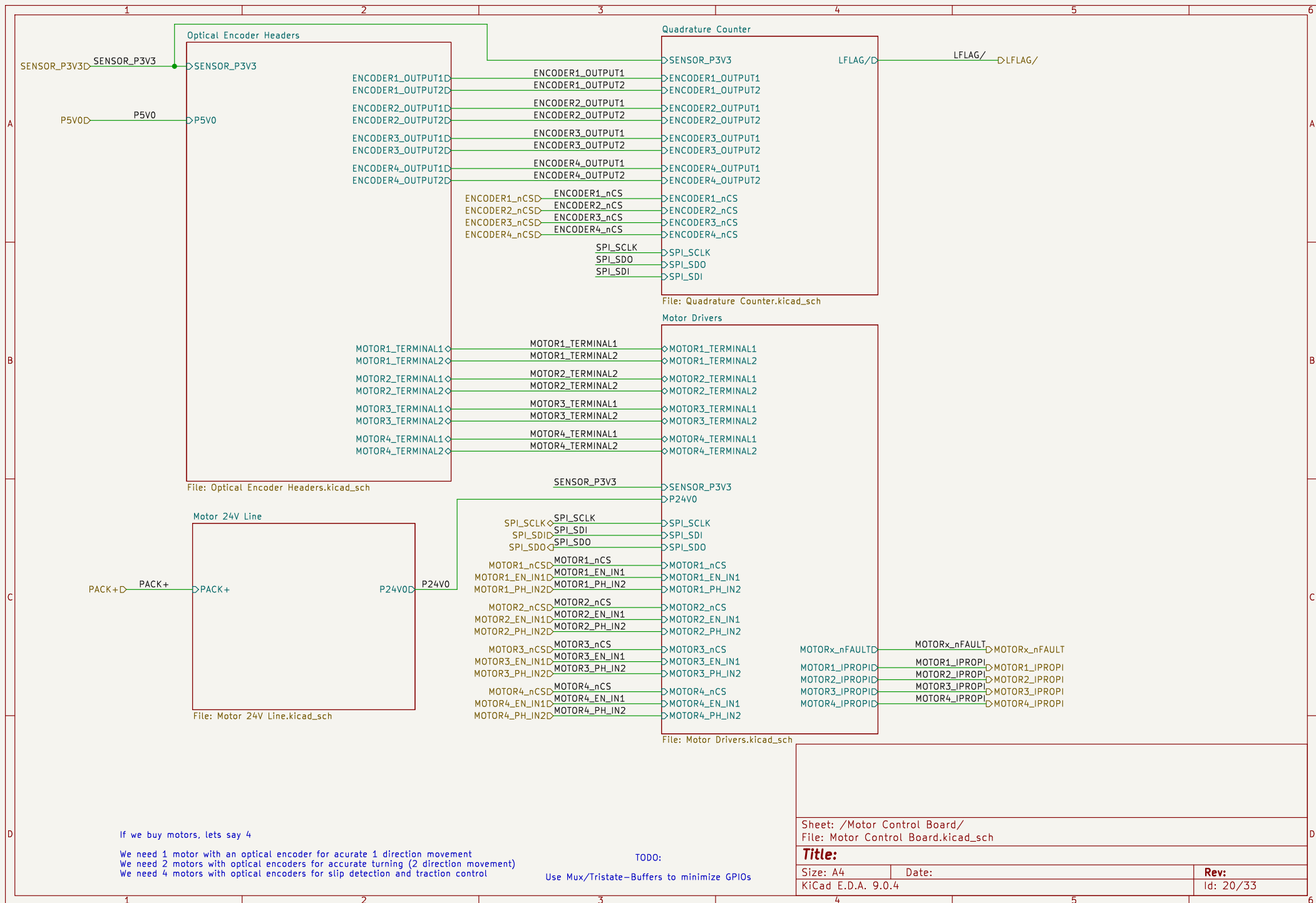
KiCad E.D.A. 9.0.4

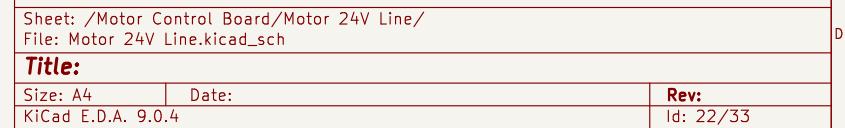
Rev:

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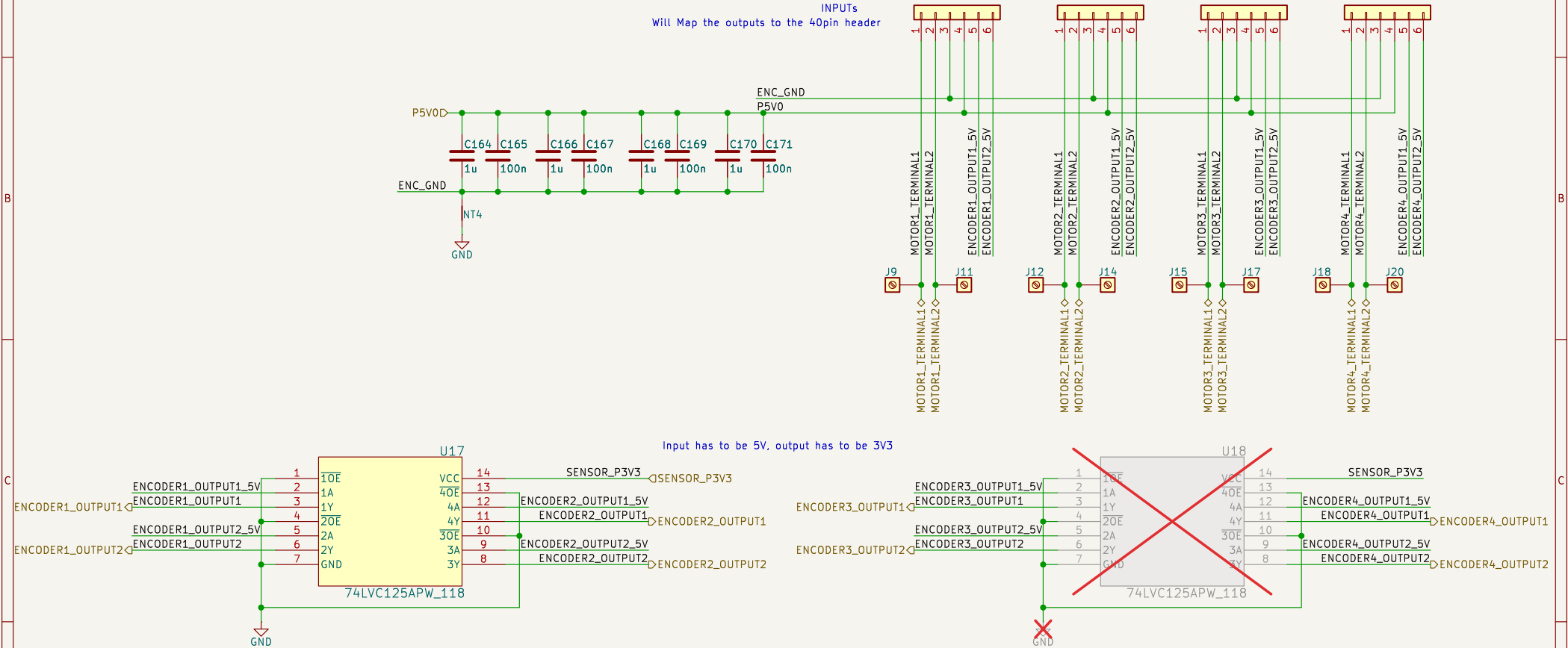








(1) Red	motor power (connects to one motor terminal)
(2) Black	motor power (connects to the other motor terminal)
(3) Green	encoder GND
(4) Blue	encoder Vcc (3.5 – 20 V)
(5) Yellow	encoder A output
(6) White	encoder B output



Sheet: /Motor Control Board/Optical Encoder Headers/
File: Optical Encoder Headers.kicad_sch

Title:

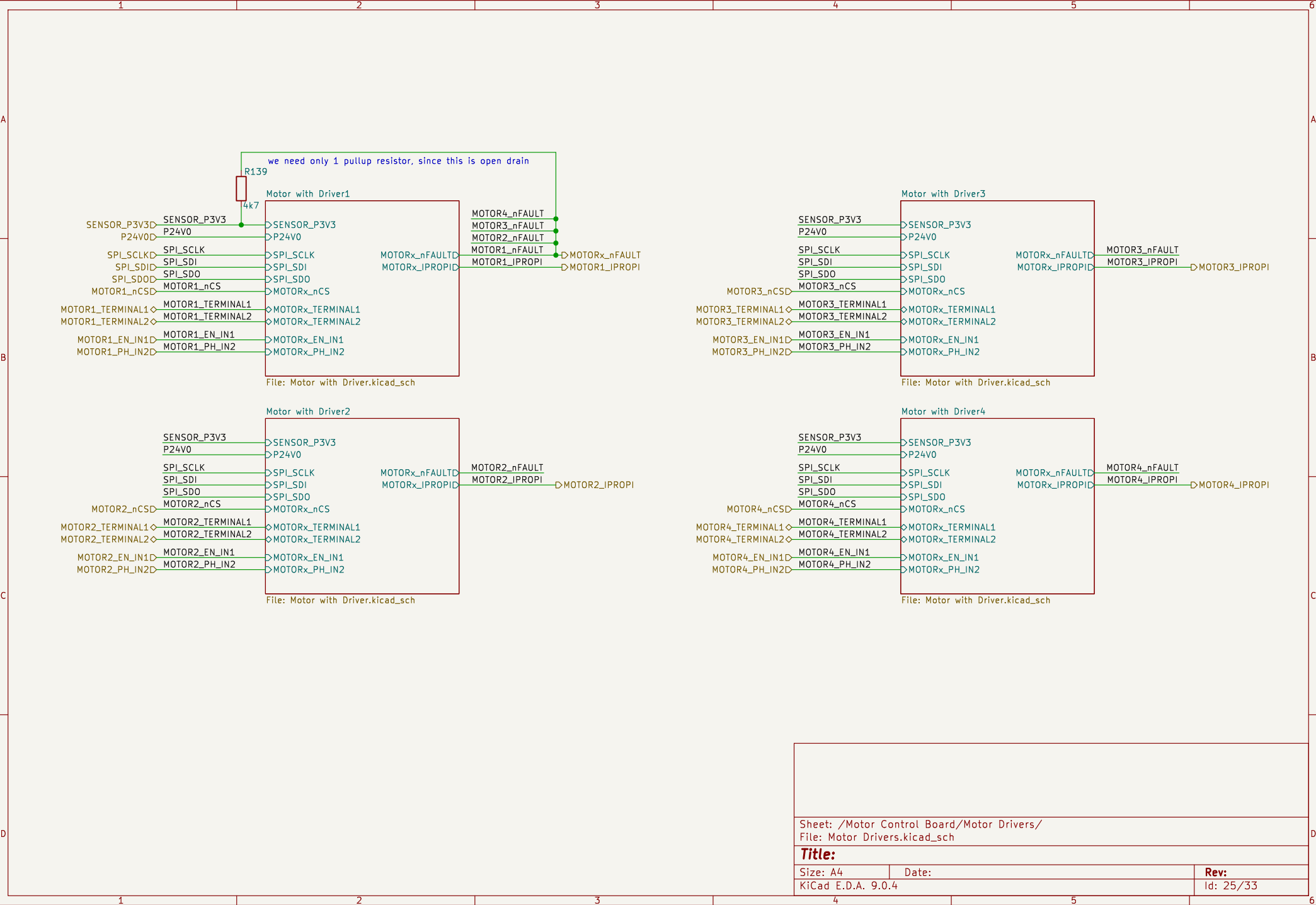
Size: A4

Date:

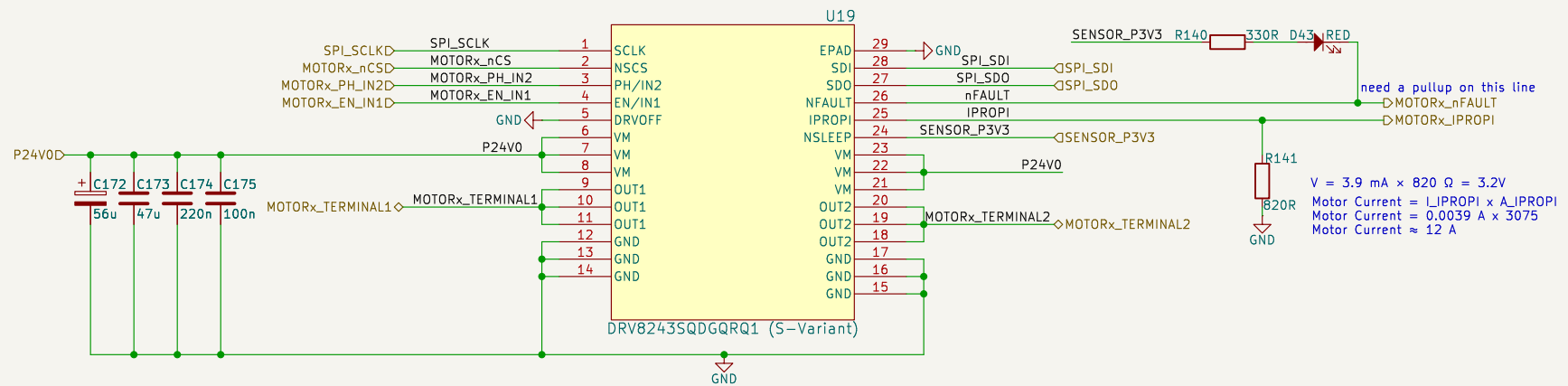
Rev:

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Id: 24/33



For the SPI (S) variant, the maximum is 10 MHz, but you should reduce it to 8 MHz if you have a 20 pF load on the SDO line.



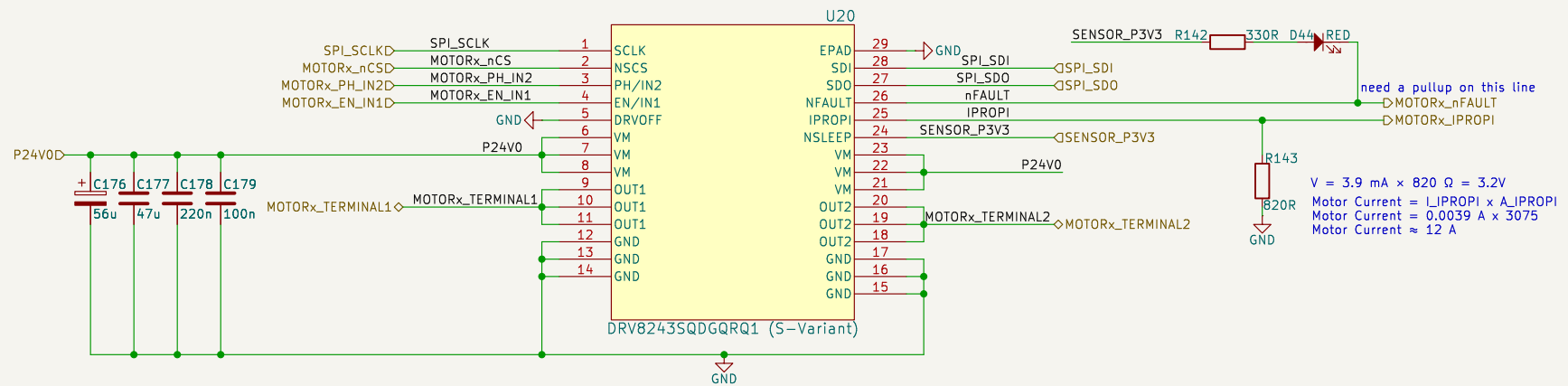
Title:

Date:

Rev:

Id: 26/33

For the SPI (S) variant, the maximum is 10 MHz, but you should reduce it to 8 MHz if you have a 20 pF load on the SDO line.



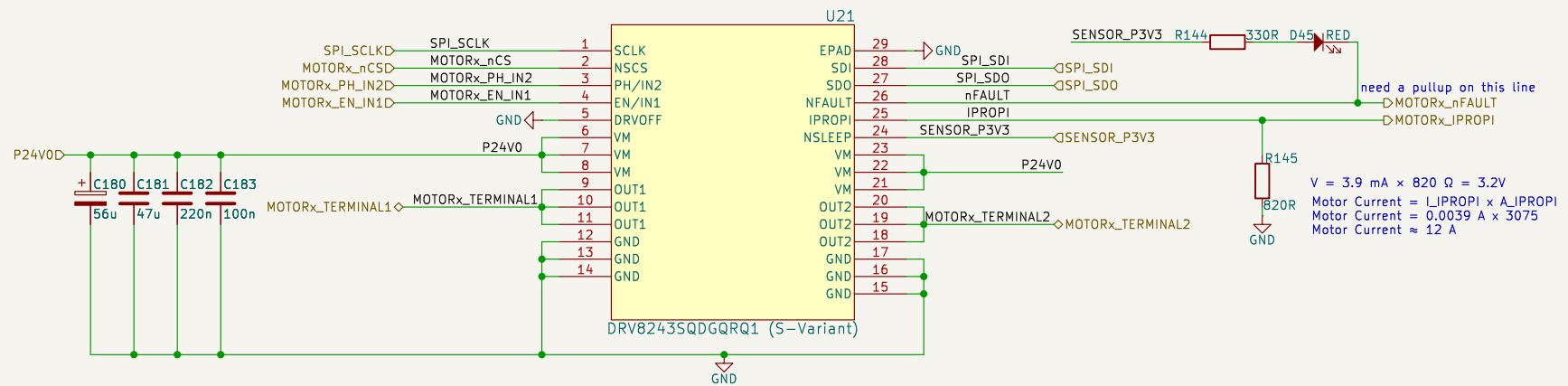
Title:

Date:

Rev:

Id: 27/33

For the SPI (S) variant, the maximum is 10 MHz, but you should reduce it to 8 MHz if you have a 20 pF load on the SDO line.



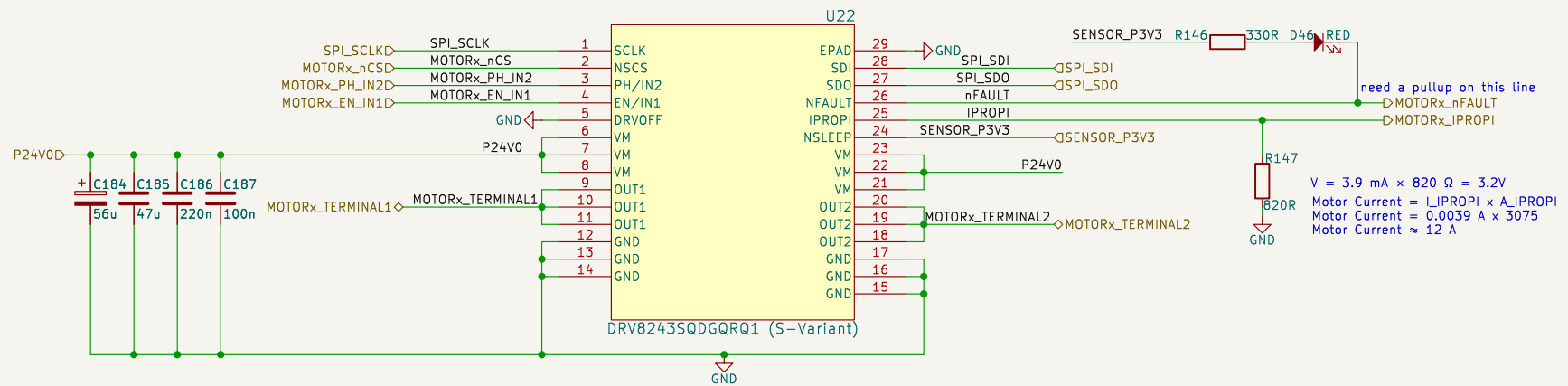
Title:

Date:

Rev:

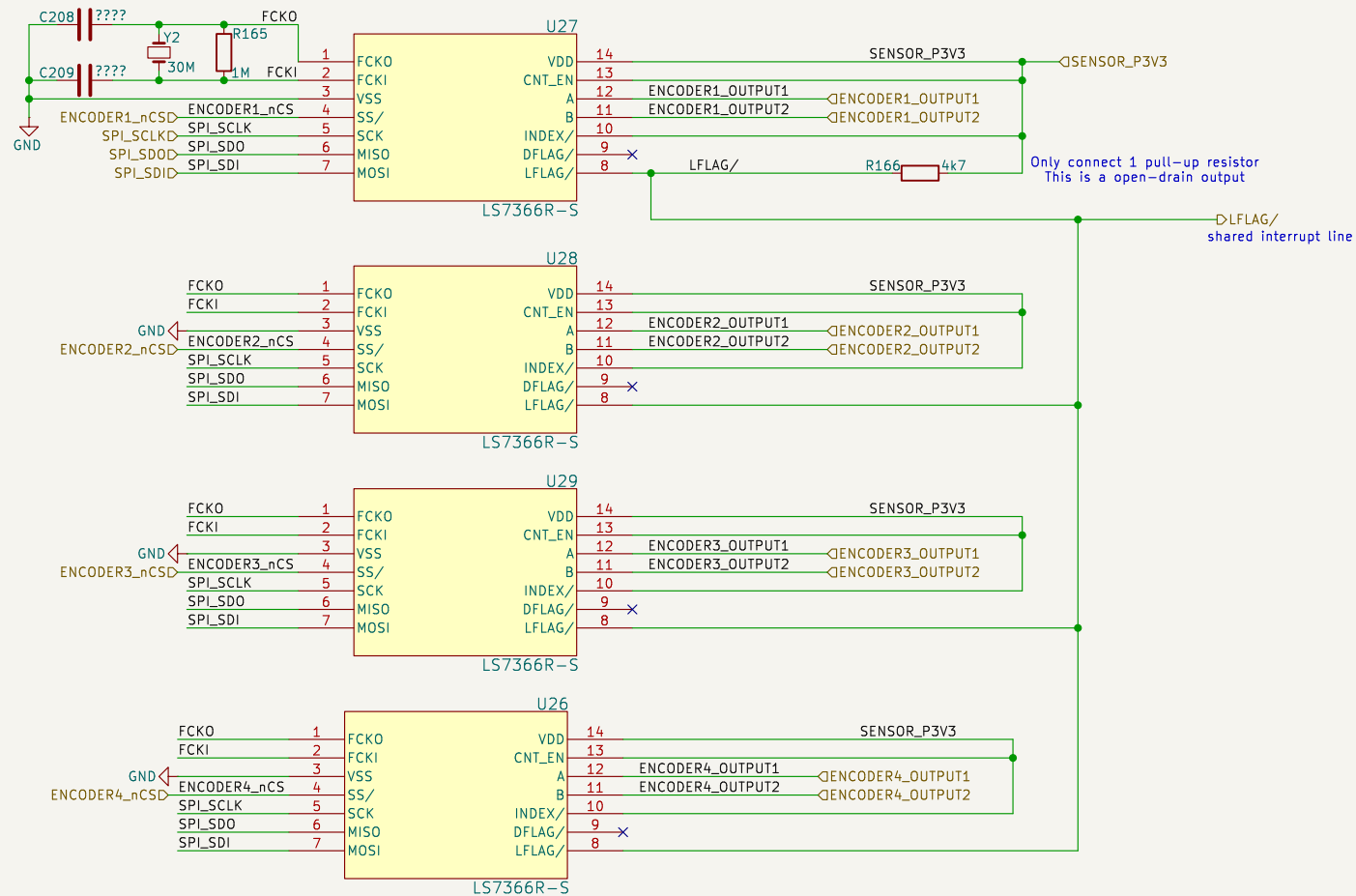
Id: 28/33

For the SPI (S) variant, the maximum is 10 MHz, but you should reduce it to 8 MHz if you have a 20 pF load on the SDO line.



Title:		
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KiCad E.D.A. 9.0.4		Id: 29/33

$C = 2 * (\text{manufacturer's specified crystal load capacitance} - \text{PCB parasitic capacitance seen by the crystal}) - 10\text{pF}$



Sheet: /Motor Control Board/Quadrature Counter/
File: Quadrature Counter.kicad_sch

Title:

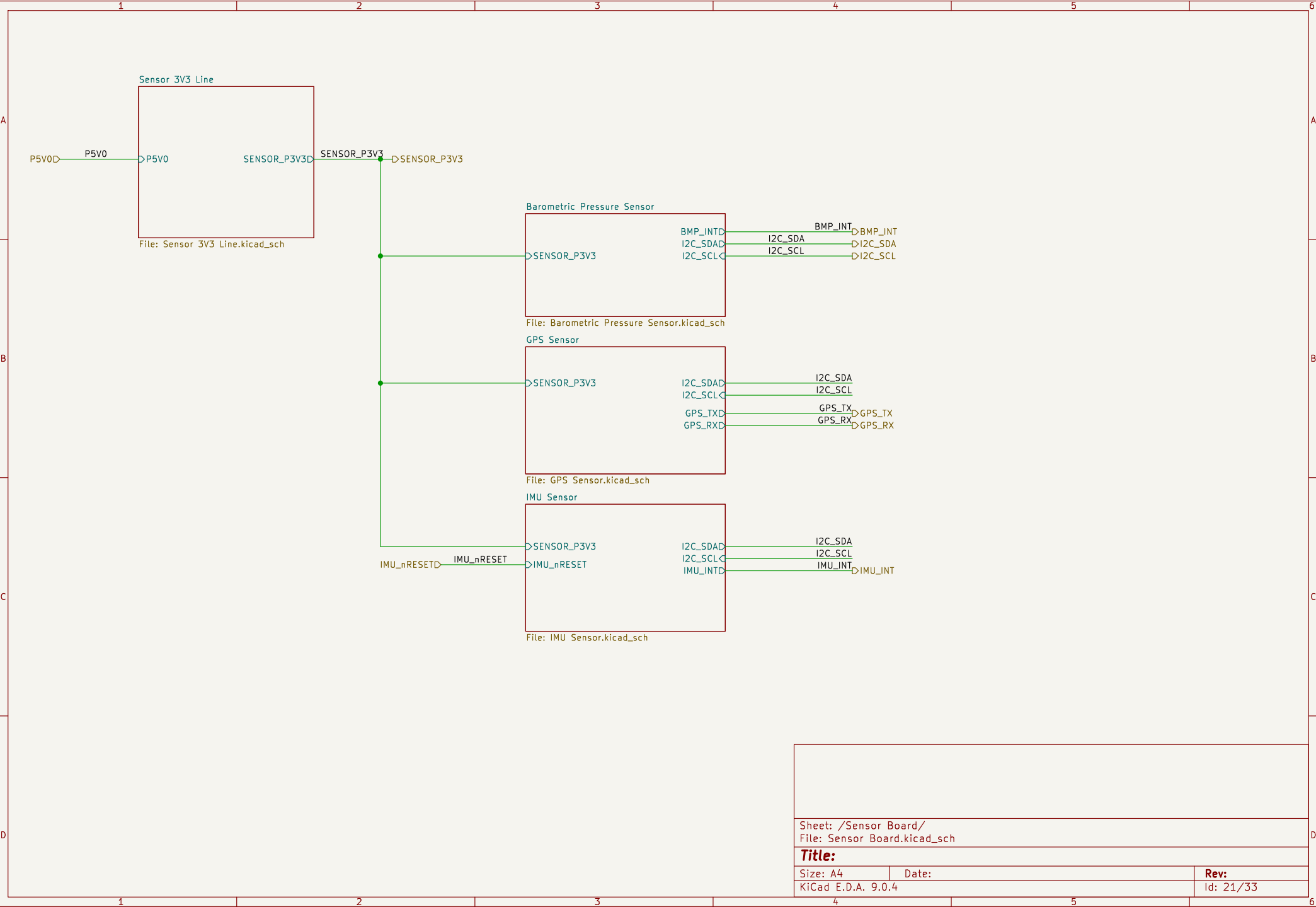
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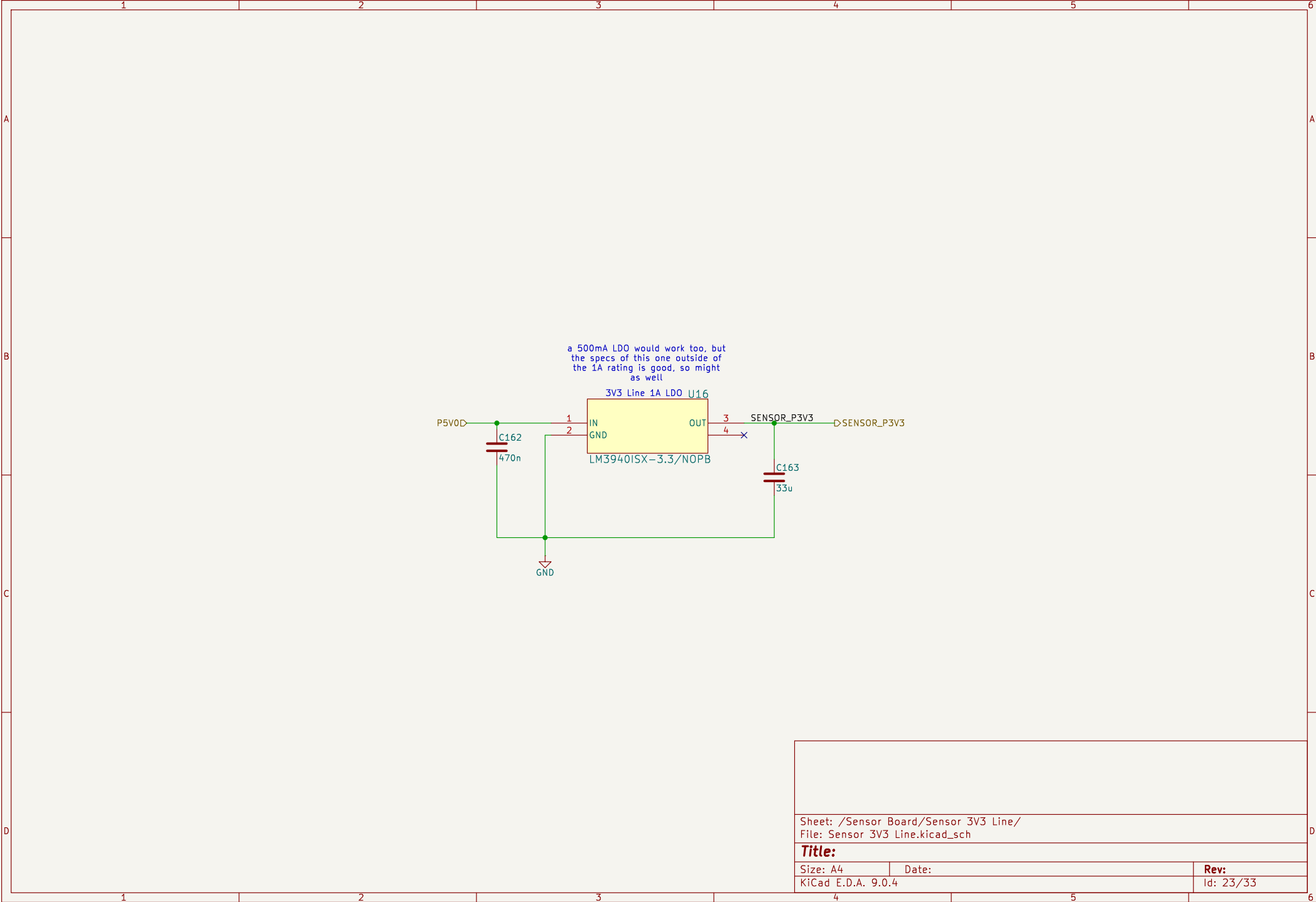
Date:

Rev:

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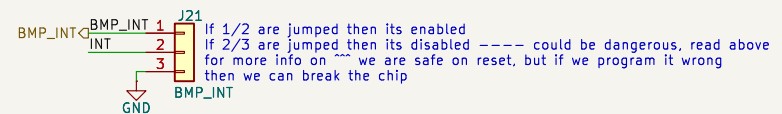
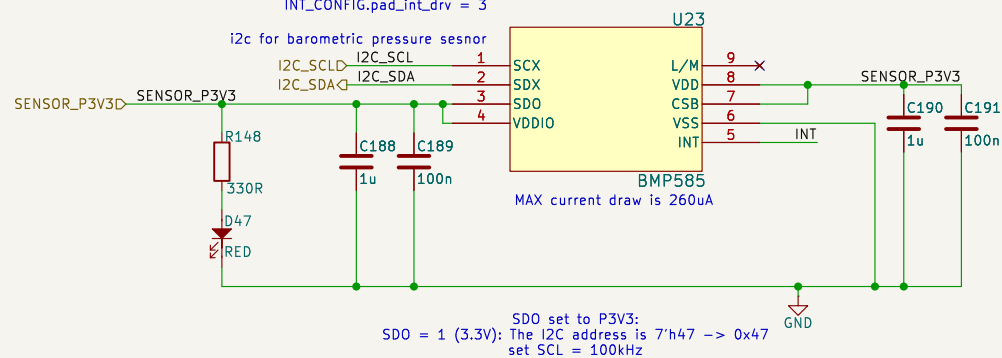




```

Datasheet sets the IRQ to 0 on start up, so never turn it on
PAGE49: 0x14 on reset is set to 0x35
PAGE49: <int_en 2bits> <int_od 2bits> <int_pol 2bits> <int_mode 2bits LSB>
PAGE51: 8.5, setup for 0x14, <0> <1> <0> <1>
this means: int_mode = latched, int_pol = active low, int_od = open_drain, int_en = disabled
INT_CONFIG.int_en = 0
INT_CONFIG.od = 1
INT_CONFIG.pol = 0
INT_CONFIG.mode = 1
INT_CONFIG.pad_int_drv = 3

```



Title:

Date:

Rev:

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