Which statements are correct ?

a.rs ,rt ,rd and shamt only have 5bits because registers are 32 bits

b. In logical operations, we would perform NOT a by a NOR b(b is $zero)

c. A good complier would use multiplication than shifting since shifting is slower.

d. Design principle 4 is simplicity favors regularity

Ans:b

a.

c. Shifting is faster.

d. Desgn principle 4 is “Good design demands good compromises.”.

Determine the following question is T/F, if F please explain it.

a. If the bits length of rs only have 4 bits, then we only have 12 registers can be used.

b. Given an assembly code [add $t0, $s1, $s2], where $t0 is the target register.

Ans:

a.F; Because we have 24 registers can be encoded.

b.F; $t0 is the rd.