


# Logan Sweet

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 logansweet

## Experience

Electrical Systems Integration Engineer, EMG wearables – **Meta**

NYC · July 2021 - May 2024

**Led MLB schematic development for Orion EMG wrist wearable through first formfactor build:**

- Collaborated cross-functionally to define system requirements, identify risks & unknowns, and evaluate tradeoffs for a new consumer product category
- Drove definition of system requirements to balance UX and power efficiency tradeoffs, then designed power architecture and chose components for a novel analog sensor system
- Designed a modular development board for digital & analog architecture studies, including component selection, schematic entry, vendor engagement, and board bringup
- Owned the MLB schematic, including component selection, IO assignment, OrCAD design entry, and organization of design reviews & OK2FAB for cross-functional approval
- Collaborated with engineers across 3 orgs as the wrist hardware EE team representative for consumer accessories

**Project owner for a PCBA manufacturing NTI:**

- Conducted build feasibility studies and evaluated an NTI's impact on cost, timeline, and technical risk versus overall product benefits
- Managed relationships and drove concurrent builds with four custom component and PCB manufacturers
- Designed test boards to evaluate component and vendor quality
- Created detailed documentation to enable reuse of NTI design by other internal teams

Electrical Design Engineer – **Skydio**

Redwood City, CA · June 2020 - June 2021

- Lead electrical engineer on the X2 enterprise drone controller, including 8 PCBAs & 1 FPCA
- Responsible for schematics, including component selection, alternate qualification, and supervising layout
- Collaborated with firmware, mechanical, and reliability teams to identify and resolve cross-functional design issues
- Designed and validated a custom battery solution with a pack vendor and defined gas gauge settings
- Served as primary liaison for a local SMT and test fixture manufacturer
- Partnered with the in-house manufacturing team to streamline early production FATP

Electrical System Integration Engineer, Pixel Phones – **Google**

Mountain View, CA · Oct 2018 - June 2020

- Worked with cross-functional teams on the development of PCBAs and FPCAs
- Owned design of FPCs, including directing simulation and layout engineers
- Responsible engineer for the schematic of a main logic board variant

Signal Integrity Intern – **TE Connectivity**

Harrisburg, PA · May - Aug. 2017

Principles of Engineering Lead Course Assistant – **Olin College**

Needham, MA · Fall 2016 & 2017

Engineering Fellow – **MIT Open Style Lab**

Cambridge, MA · June - Aug 2016

Product Management Intern – **Phone2Action**

Washington DC · Feb - May 2016

## Education

**Olin College of Engineering**

BS in Electrical & Computer Engineering  
Graduated May 2018      GPA: 3.6

## Skills

**Software:** OrCAD & Concept schematic capture, Allegro, PowerSI

**Lab:** Manual PCBA rework 0201 & larger, EE bringup & debug equipment (oscilloscope, DMM, logic analyzer, etc.)