

# Logan Sweet

## Experience

Electrical Systems Integration Engineer – **Meta** NYC · July 2021 - May 2024

**Schematic lead for Orion EMG wrist wearable MLB:**

- MLB schematic design lead and power architecture owner, including component selection & leading design reviews
- Collaborated cross-functionally to define project goals, identify risks, & evaluate tradeoffs to enable a new product category
- Drove definition of system requirements to balance user-facing latency and power efficiency tradeoffs
- Designed a modular dev board for digital & analog architecture studies, including schematic, layout, vendor engagement, component sourcing, and board bringup

*I was MLB schematic lead until Meta reorged during P1 build, when I joined the NTI team*

**Project owner for a PCBA manufacturing NTI:**

- Defined KPIs, schedule, and budget for a new technology investigation project
- Conducted build feasibility studies and evaluated the NTI's impact on cost, timeline, and technical risk
- Designed hardware to meet RF, mechanical, & reliability requirements
- Performed layout studies for HDI & mSAP PCB technologies
- Created PCBA schematics & layouts to evaluate component & vendor quality
- Managed vendor relationships and drove concurrent builds with 4 manufacturers

Electrical Design Engineer – **Skydio** Redwood City, CA · June 2020 - June 2021

- Lead electrical engineer on the X2 enterprise drone controller
- Responsible for PCBA & FPCA schematics, including component selection, alternate qualification, and supervising layout engineers
- Identified and resolved cross-functional design issues alongside firmware, mechanical, & reliability teams
- Designed and validated a custom battery solution with a pack vendor and programmed custom gas gauge settings
- Acted as primary liaison for the local SMT and test fixture manufacturer
- Partnered with in-house manufacturing team to streamline early production FATP

Electrical System Integration Engineer – **Google** Bay Area · Oct 2018 - June 2020

- Owned design of PCBAs & FPCAs, including schematic design and supervising simulation and layout
- Responsible for NFC, audio, display, & wireless charging subsystem integration

Signal Integrity Intern – **TE Connectivity** Harrisburg, PA · May - Aug. 2017

Lead Mechatronics Course TA – **Olin College** Neehdam, MA · Fall 2016 & 2017

Engineering Fellow – **MIT Open Style Lab** Cambridge, MA · June - Aug 2016


Product Management Intern – **Phone2Action** Washington DC · Feb - May 2016

## Contact

loganrsweet@gmail.com

615.584.8676

loganrsweet.com

 logansweet

## Education

**Olin College of Engineering**

BS Electrical & Computer Engineering

May 2018 GPA: 3.6

## Skills

### Technologies

SPI, UART, I2C, I3C

USB3, USB PD

NFC, Displayport

Gas gauge programming

### Lab

Rework/ hand soldering

Rapid prototyping

Board bringup & debug  
(oscilloscope, DMM, power supply, logic analyzer, VNA, etc.)

### Systems

Power architecture

Component selection

Hardware validation

Test procedure development

Factory test fixture design

Process documentation

### PCBA

HDI & mSAP PCBAs

SiP technology

FPCAs

BOM alternate management

### Software

OrCAD

Concept/ Design Entry HDL

Allegro

TI BQStudio

PowerSI

SPICE simulation