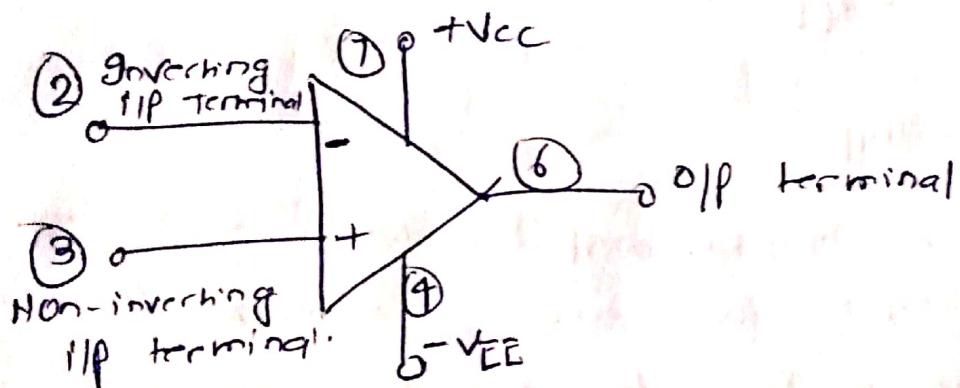


Operational Amplifier

Operational amplifiers are commonly known as "Op-amps". It was introduced in 1948 and was used to perform mathematical operations in Analog computers. Hence, the name is operation amplifier.

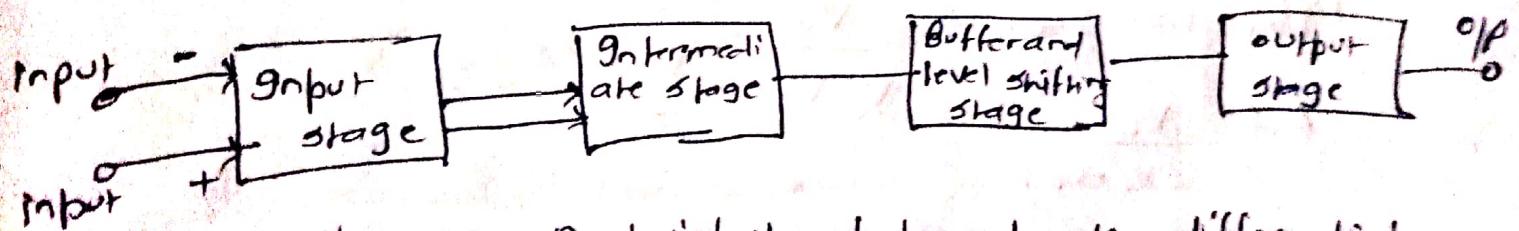
Op-amp symbol

It is a two input one output device.



If an input is given to inverting terminal, the op-amp will be of opposite polarity.

Block diagram representation

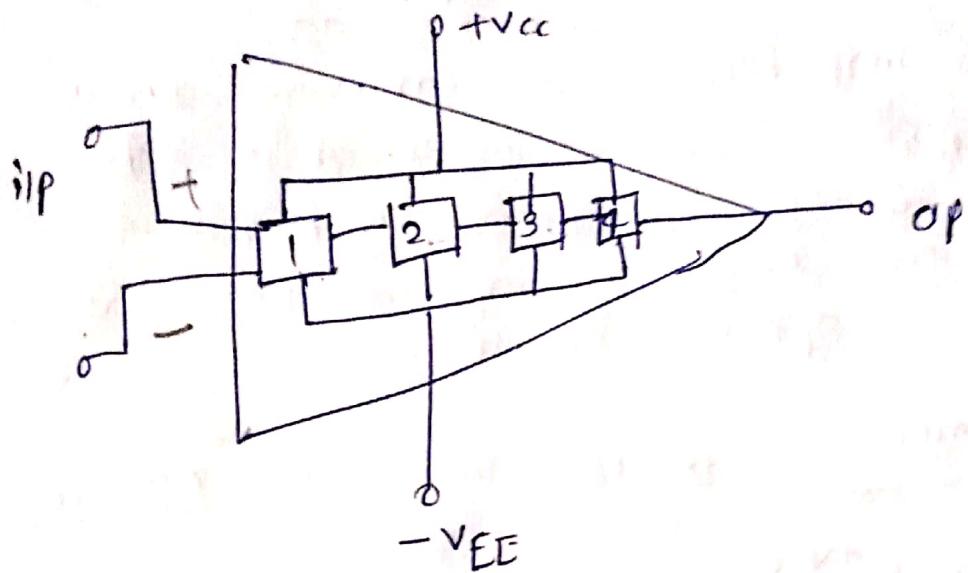


Input stage → Dual input, balanced op-amp differential amplifier

Intermediate stage — To have high gain, it has multi-stage cascaded amplifiers.

Output stage: — It has push-pull complementary amplifier.

So the overall block diagram will be represented like this.

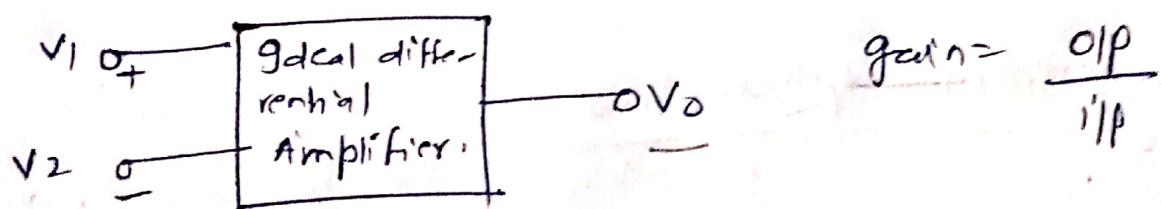


- 1 - iip stage
- 2 - Intermediate stage
- 3 - level shifting
- 4 - opp

Ideal op-amp

If it is basically an amplifier which will amplify the difference b/w two i/p signals.

Let us understand the basics of an differential amplifier which is a basic building block of an op-amp.



$$\text{Differential gain} = \frac{V_0}{V_1 - V_2} \quad \text{if } V_d = V_1 - V_2$$

$$A_d = \frac{V_0}{(V_1 - V_2)}$$

Common mode gain

$$\text{If } V_1 = V_2, \text{ then } V_0 = A_d(V_1 - V_2) = A_d(0) \\ V_0 = 0$$

But the off voltage of the amplifier, not only depends on the difference voltages but also on the average common level of the two inputs.

$$V_d = V_1 - V_2$$

This average common level of two o/p signals are called common mode signal.

$$V_c = \frac{V_1 + V_2}{2}$$

The gain with which it amplifies the common signal is known as common mode gain.

$$V_o = A_c \cdot V_c$$

$$A_c = \frac{V_o}{V_c}, V_c = \frac{V_1 + V_2}{2}$$

$$\boxed{V_o = A_d \cdot V_{di} \neq A_c \cdot V_c}$$

Common mode rejection ratio (CMRR)

This is the ability of a differential amplifier to reject a common mode signal.

It is given by ratio of differential mode voltage gain to common mode voltage gain.

$$CMRR = \left| \frac{A_d}{A_c} \right| ; \infty, A_c = 0$$

Ideal op-amp characteristics

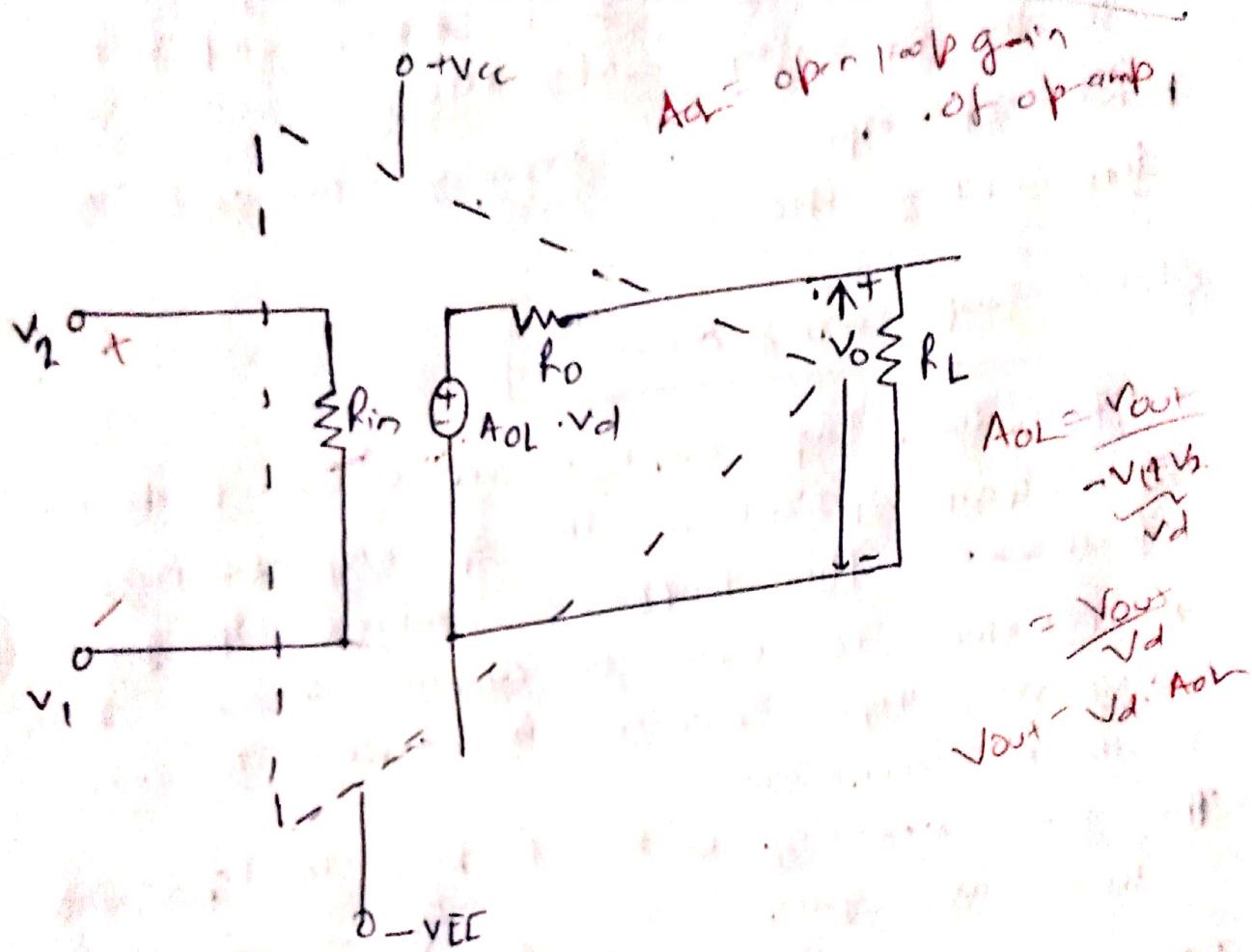
- a) Infinite voltage gain \rightarrow The open loop gain of an op-amp should be infinite.

$$\frac{V_o}{V_{in}} = \infty$$

- b). It should have infinite $1/\text{P}$ impedance.
- c). It should have zero o/p impedance.
- d). Zero offset voltage:- The presence of a small o/p voltage though $V_1 = V_2 = 0$ is called offset voltage. It should be 2mV for an ideal op-amp.
- e). Infinite bandwidth:- The range of frequency over which the amplifier performs is called bandwidth and it should be infinite for an ideal op-amp.
- f). Infinite CMRR:- The ideal op-amp have $A_c = 0$, hence $\text{CMRR} = \left| \frac{A_d}{A_c} \right| = \left| \frac{A_d}{0} \right| = \infty$.
- g). Infinite slew rate:-
 The slew rate ensures that change in o/p voltage occurs simultaneously with change in i/p voltage. It is given by

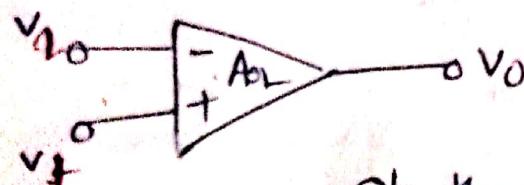
$$S = \frac{dV_o}{dt} \text{ max. (V/Sec)}$$
- It is defined as maximum rate of change of o/p voltage with time.
- h). The parameters of op-amp should not be dependent on temperature.

Equivalent circuit of a practical op-amp



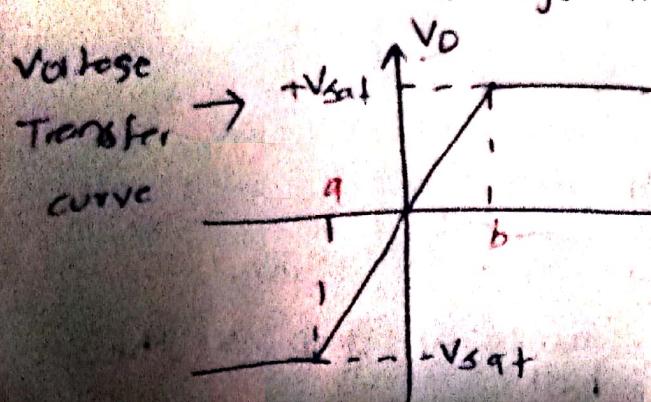
Problems with open loop configuration of an opamp

$A_{OL} = \text{open loop gain.}$



A_{OL} Due to very large open loop gain, a very small noise present

at the input gets amplified and it brings the op-amp in saturation.



The curve shows that only for a small input ($a-b$) it shows linear behaviour. Otherwise it is saturated.

This indicates the inability of op-amp to work as a linear small signal amplifier in the open-loop configuration.

Hence, op-amps are generally not used in this configuration.

Closed loop configuration

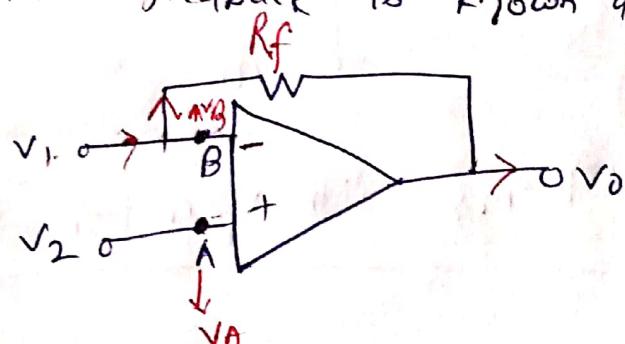
The closed loop configuration uses a feedback.

In linear applications, the op-amp uses negative feedback. This feedback controls the gain.

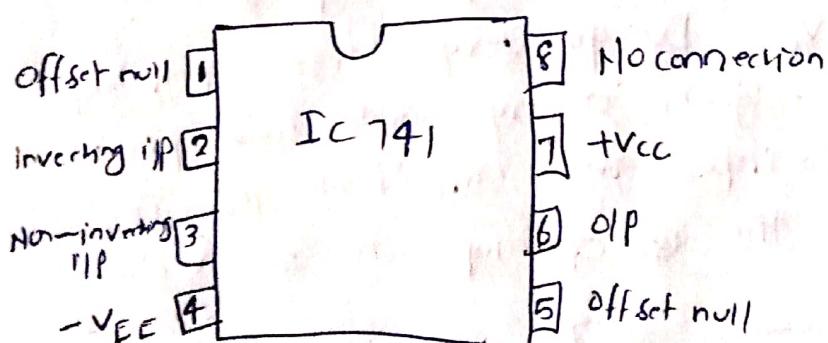
A resistor is used to connect the op-amp with negative input terminal to ensure negative feedback connection.

The gain resulting with feedback is known as closed loop gain.

Properties of



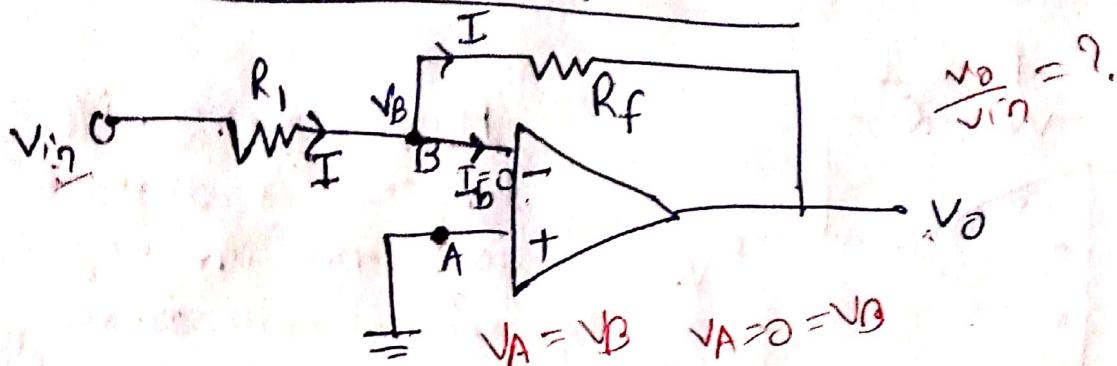
IC 741



Assumptions in ~~closed~~ op-amp

- (1) $V_A = V_B \rightarrow$ This is known as virtual ground.
- (2) No current will flow inside op-amp as the i/p resistance is very high.

Inverting amplifier



For inverting op-amp as the name suggests, the o/p has 180° phase shift w/r i/p. The non-inverting i/p terminal will be grounded. we have to find closed loop gain $\frac{V_o}{V_{in}}$.

$V_A = 0$ so by the concept of virtual ground, $V_B = 0$

at point B, no current will flow inside the op-amp hence the current I generated by V_{in} will flow towards feed back resistor R_f .

By applying KCL at node B

$$\frac{V_{in} - V_B}{R_1} = I = \frac{V_B - V_o}{R_f}$$

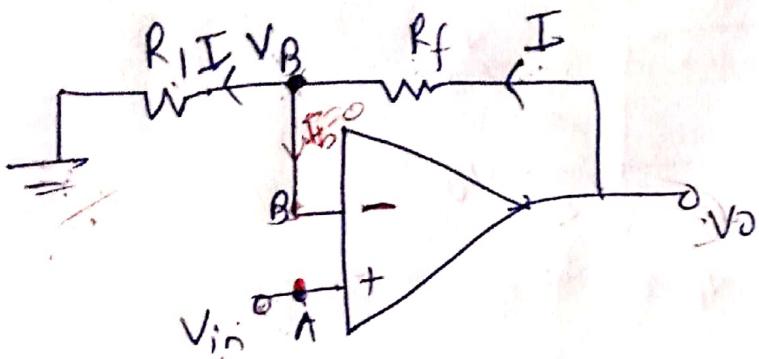
$$\frac{V_{in} - 0}{R_1} = \frac{0 - V_o}{R_f}$$

$$\frac{V_{in}}{R_1} = \frac{-V_o}{R_f}$$

$$\boxed{\frac{V_o}{V_{in}} = -\frac{R_f}{R_1}}$$

The negative sign indicates 180° phase shift b/w i/p and o/p signal. Hence, this is an inverting amp.

Non-inverting Amplifier



Here, inverting input terminal is grounded and non-inverting terminal will have output voltage.

$$V_A = V_{in} = V_B \quad - \text{By the concept of virtual ground.}$$

$\therefore V_o$ is +ve, the current will flow from V_o (higher potential) towards R_1 (ground).

No ~~out~~ current will flow inside the op-amp.

By KCL

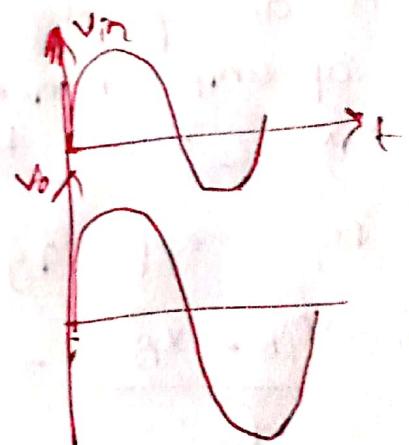
$$\frac{V_o - V_B}{R_f} = \frac{V_B - 0}{R_1} = I$$

$$\frac{V_o - V_{in}}{R_f} = \frac{V_{in}}{R_f} \quad | \because V_{in} = V_B$$

$$\frac{V_o}{R_f} = \frac{V_{in}}{R_f} + \frac{V_{in}}{R_1}$$

$$\frac{V_o}{R_f} = V_{in} \left(\frac{1}{R_f} + \frac{1}{R_1} \right)$$

$$\frac{V_o}{V_{in}} = R_f \left(\frac{1}{R_f} + \frac{1}{R_1} \right) = \left(1 + \frac{R_f}{R_1} \right)$$

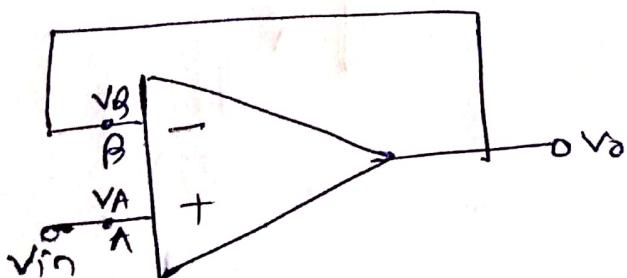


$$\frac{V_o}{V_{in}} = 1 + \frac{R_f}{R_1}$$

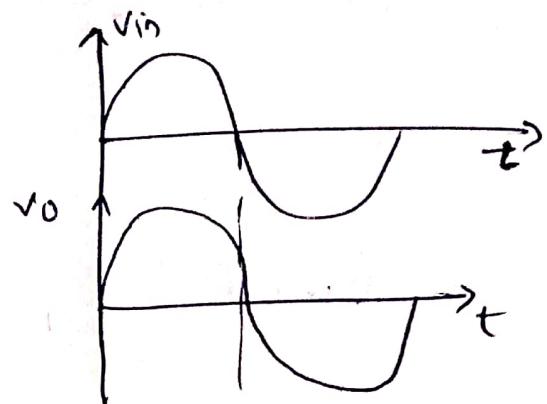
This is the gain of non-inverting amplifier.

Voltage follower

The circuit in which o/p voltage follows the i/p voltage is known as voltage follower



$$V_A = V_B = V_{in}$$



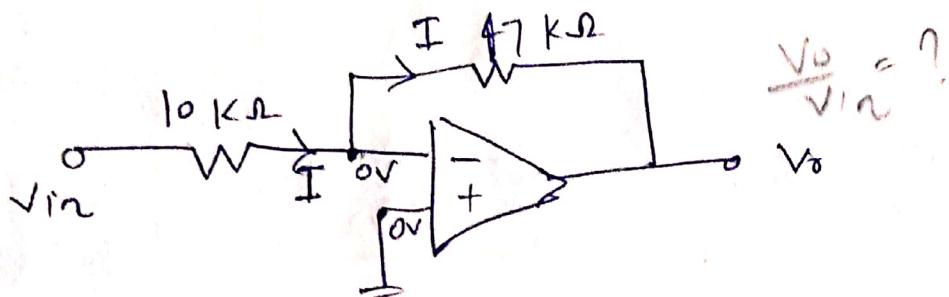
$$V_o = V_B$$

$$\boxed{V_o = V_{in}}$$

$\frac{V_o}{V_{in}} = 1$, this is also called source follower, unity gain amplifier, buffer amplifier.

Questions on inverting op-amp

Determine the voltage gain of the op-amp circuit shown below



$$I = \frac{V_{in} - 0}{10 \times 10^3} = \frac{0 - V_o}{47 \times 10^3}$$

If $V_{in} = 1V$

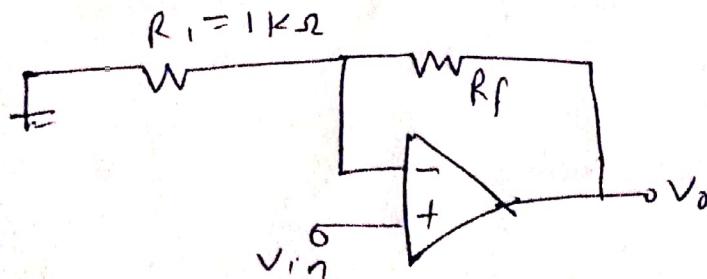
$$V_o = -4.7 \times 1$$

$$\boxed{V_o = -4.7V}$$

$$\frac{V_o}{V_{in}} = -\frac{47 \times 10^3}{10 \times 10^3}$$

$$\boxed{\frac{V_o}{V_{in}} = -4.7}$$

- 2) For a non-inverting amp, determine R_f if gain is 61.



$$\frac{V_o}{V_{in}} = \text{gain} = 1 + \frac{R_f}{R_1}$$

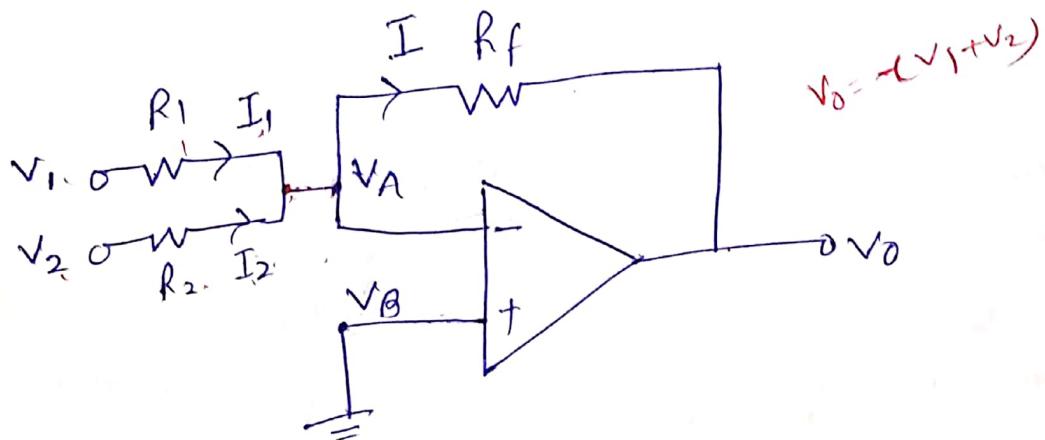
$$61 = 1 + \frac{R_f}{1}$$

$$\boxed{R_f = 60 \text{ k}\Omega}$$

Adder or Summer

Inverting Summer

As the input impedance of the op-amp is extremely large, more than one i/p signals can be applied to the inverting ~~op~~ terminal. Such circuit gives addition of the applied signal at the o/p. Hence it is called summer circuit.



\therefore point B is grounded, $V_B = 0$

By the concept of virtual ground, $V_A = V_B = 0$

from i/p side! —

$$I_1 = \frac{V_1 - V_A}{R_1} = \frac{V_1 - 0}{R_1} = \frac{V_1}{R_1}$$

$$I_2 = \frac{V_2 - V_A}{R_2} = \frac{V_2 - 0}{R_2} = \frac{V_2}{R_2}$$

$\therefore I = \frac{V_A - V_0}{R_f} = \frac{0 - V_0}{R_f} = \frac{-V_0}{R_f}$

$I = I_1 + I_2$

3.15
 4.30 W

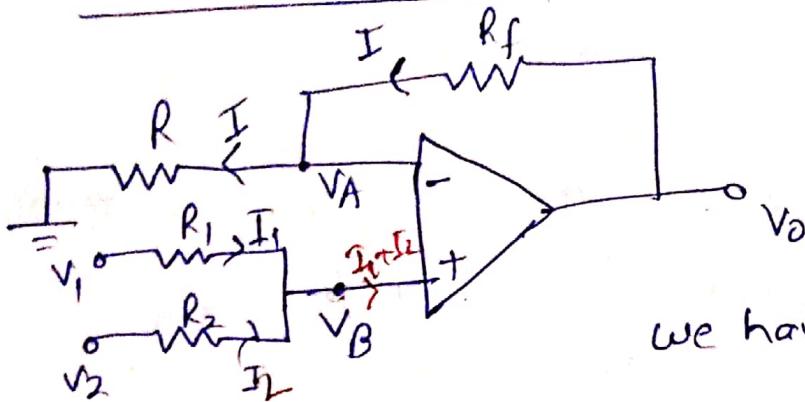
$$\frac{V_0}{R_f} = \frac{V_1}{R_1} + \frac{V_2}{R_2}$$

$$V_0 = - \left[\frac{R_f \cdot V_1}{R_1} + \frac{R_f \cdot V_2}{R_2} \right]$$

If $R_f = R_1 = R_2$

$$\boxed{V_0 = -[V_1 + V_2]}$$

Non-inverting summing amplifier



Here, if voltages are applied at the non-inverting terminal.

we have to find V_B as

voltage at point B is not zero.

At non-inverting i/p side

$$I_1 = \frac{V_1 - V_B}{R_1}$$

$$I_2 = \frac{V_2 - V_B}{R_2}$$

$I_1 + I_2 = 0$ as no current will flow inside the op-amp.

$$\frac{V_1 - V_B}{R_1} + \frac{V_2 - V_B}{R_2} = 0$$

$$\frac{V_1}{R_1} + \frac{V_2}{R_2} - V_B \left(\frac{1}{R_1} + \frac{1}{R_2} \right) = 0$$

$$V_B \left(\frac{1}{R_1} + \frac{1}{R_2} \right) = \frac{V_1}{R_1} + \frac{V_2}{R_2}$$

$$V_B = \left(\frac{V_1 R_2 + V_2 R_1}{R_1 + R_2} \right)$$

By the concept of virtual ground

$$V_A = V_B = \frac{V_1 R_2 + V_2 R_1}{R_1 + R_2} \quad \text{--- (1)}$$

at o/p side

$$I = \frac{V_O - V_A}{R_f}$$

at inverting i/p side

$$I = \frac{V_A - 0}{R}$$

$$\frac{V_O - V_A}{R_f} = \frac{V_A}{R}$$

$$\frac{V_O}{R_f} = V_A \left(\frac{1}{R_f} + \frac{1}{R} \right)$$

$$V_O = V_A \left(1 + \frac{R_f}{R} \right)$$

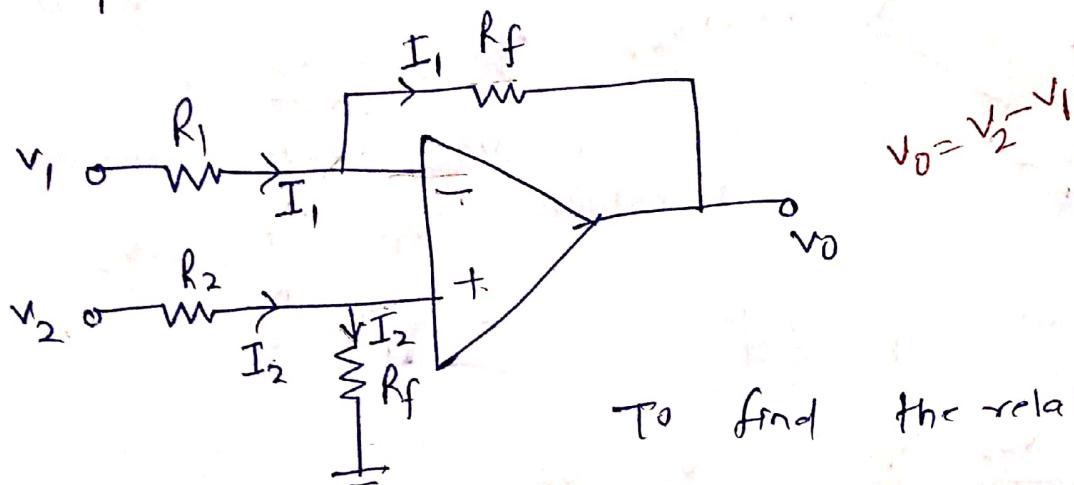
$$V_O = \left(\frac{V_1 R_2 + V_2 R_1}{R_1 + R_2} \right) \left(1 + \frac{R_f}{R} \right)$$

if $R_f = R_1 = R_2 = R$

$$V_O = V_1 + V_2$$

Subtractor or difference amplifier

In this type of circuit, two I/P voltages are applied at two I/P terminals, and the O/P will be difference of two I/Ps.



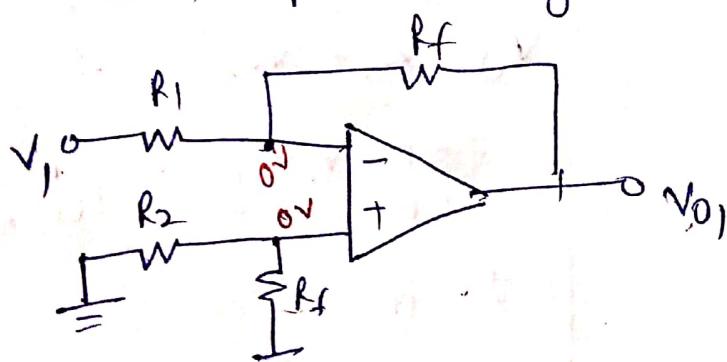
To find the relation b/w

I/P and O/P, we have to use superposition theorem.

Let us assume that V_{o1} is O/P when V_1 is acting and V_{o2} is the O/P when V_2 is acting.

Case-I

when V_1 is acting and V_2 is grounded,



Here, this amplifier is just like a inverting amplifier and we know the gain for inverting O/P amp is equal to

$$\text{gain} = -\frac{R_f}{R_1}$$

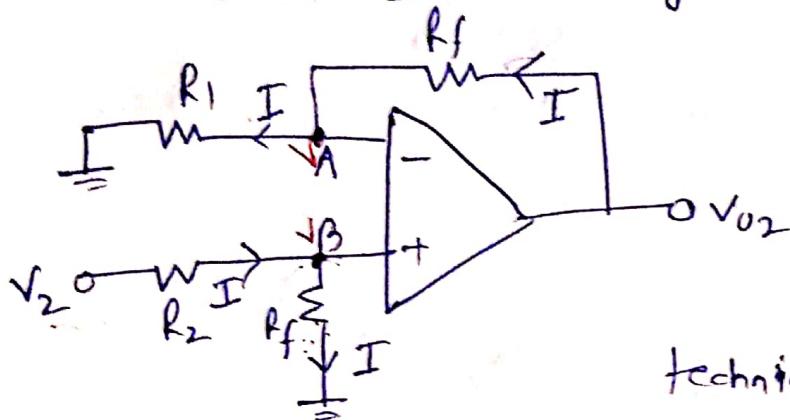
$$\frac{V_{o1}}{V_1} = -\frac{R_f}{R_1}$$

$$\boxed{\frac{V_{o1}}{V_1} = -\frac{R_f \cdot V_1}{R_1}}$$

→ (1)

Case-II

when V_2 is acting and V_1 is grounded.



By the concept of virtual ground $V_A = V_B$

To find V_B

By using voltage divider technique

$$V_B = \frac{R_f}{R_2 + R_f} \cdot V_2 \quad \text{--- (1)}$$

Now at o/p side

$$I = \frac{V_{02} - V_A}{R_f} = \frac{V_{02} - V_B}{R_f} \quad | \because V_A = V_B$$

at inverting i/p side

$$\frac{V_A - 0}{R_1} = I_o = \frac{V_A}{R_1} = \frac{V_B}{R_1}$$

$$\frac{V_{02} - V_B}{R_f} = \frac{V_B}{R_1}$$

$$\frac{V_{02}}{R_f} = V_B \left[\frac{1}{R_1} + \frac{1}{R_f} \right]$$

$$V_{02} = V_B \left[1 + \frac{R_f}{R_1} \right] \quad \text{--- (1)}$$

from eq. (1)

$$V_{02} = \left[1 + \frac{R_f}{R_1} \right] \left[\frac{R_f}{R_2 + R_f} \cdot V_2 \right]$$

$\boxed{V = V_D}$

$$V_0 = V_{01} + V_{02}$$

$$= -\frac{R_f}{R_1} V_1 + \left[1 + \frac{R_f}{R_1} \right] \left[\frac{R_f}{R_f + R_2} \cdot V_2 \right]$$

$$g_f \quad R_f = R_1 = R_2$$

$$V_0 = -V_1 + \left[1 + \left(\frac{1}{2} \right) \cdot V_2 \right]$$

$$= -V_1 + V_2$$

$$\boxed{V_0 = V_2 - V_1}$$