VIETNAM NATIONAL UNIVERSITY, HO CHI MINH CITY UNIVERSITY OF TECHNOLOGY FACULTY OF COMPUTER SCIENCE AND ENGINEERING



MICROPROCESSORS MICRO-CONTROLLERS CO3009 - CC01

Report

Lab 2 - Timer

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This is the drive link contains all file in Lab 1, including STM32 file and Proteus simulation file: Github Link and Back up GG Drive Link



1 Software Timer

Except exercise 1, all other exercises use the software timer so this HAL_TIM_PeriodElapsedCallback() function is used instead

```
void HAL_TIM_PeriodElapsedCallback(TIM_HandleTypeDef *htim)
{
Run_timer();
}
```

Program 1.1: HAL TIM PeriodElapsedCallback() function

```
#ifndef INC_SOFTWARE_TIMER_H_
#define INC_SOFTWARE_TIMER_H_

extern int timer_flag[];

void Set_timer(int duration, int arr_pos);
void Run_timer();

#endif /* INC_SOFTWARE_TIMER_H_ */
```

Program 1.2: software timer.h



```
#include "software_timer.h"
3 const int timer_arr_size = 18;
int timer_counter[18] = {
      0, 0, 0, 0, 0, 0,
      0, 0, 0, 0, 0, 0,
      0, 0, 0, 0, 0, 0};
  int timer_flag[18] = {
      0, 0, 0, 0, 0, 0,
      0, 0, 0, 0, 0, 0,
10
      0, 0, 0, 0, 0, 0};
12
void Set_timer(int duration, int arr_pos)
15 {
    timer_counter[arr_pos] = duration;
    timer_flag[arr_pos] = 0;
18 }
19
void Run_timer()
21 {
    int i = 0;
22
    while(i < timer_arr_size)</pre>
23
24
      if(timer_counter[i] > 0)
      {
26
        timer_counter[i]--;
27
        if(timer_counter[i] <= 0)</pre>
29
           timer_flag[i] = 1;
30
31
      }
32
      i++;
33
    }
34
35 }
```

Program 1.3: software timer.c

2 Exercise 1



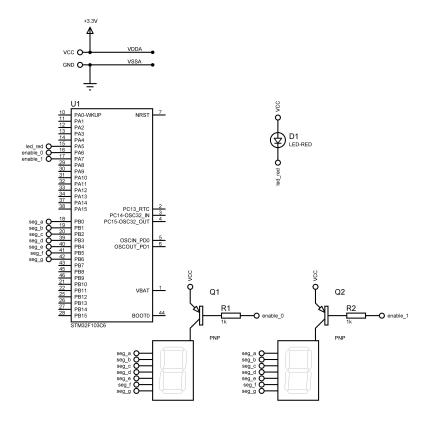


Figure 2.1: Github Link and Back up GG Drive Link

```
int enable_trigger = 0;
int counter = 50;
void HAL_TIM_PeriodElapsedCallback(TIM_HandleTypeDef *htim)
4 {
    counter --;
    int trigger_num = 0;
    if(counter <= 0)</pre>
      counter = 50;
9
      HAL_GPIO_TogglePin(led_red_GPIO_Port, led_red_Pin);
10
      switch (enable_trigger) {
13
          HAL_GPIO_WritePin(enable_0_GPIO_Port, enable_0_Pin, RESET)
14
          HAL_GPIO_WritePin(enable_1_GPIO_Port, enable_1_Pin, SET);
          enable_trigger = 1;
16
          trigger_num = 1;
17
          break;
        case 1:
19
```



```
HAL_GPIO_WritePin(enable_1_GPIO_Port, enable_1_Pin, RESET)
          HAL_GPIO_WritePin(enable_0_GPIO_Port, enable_0_Pin, SET);
21
          enable_trigger = 0;
22
          trigger_num = 2;
23
          break;
        default:
25
          break;
26
      }
28
      display7SEG(trigger_num,
29
          seg_a_GPIO_Port, seg_a_Pin,
30
          seg_b_GPIO_Port, seg_b_Pin,
          seg_c_GPIO_Port, seg_c_Pin,
32
          seg_d_GPIO_Port, seg_d_Pin,
33
          seg_e_GPIO_Port, seg_e_Pin,
34
          seg_f_GPIO_Port, seg_f_Pin,
          seg_g_GPIO_Port, seg_g_Pin);
36
37
38 }
```

Program 2.1: Ex1 source code



3 Exercise 2

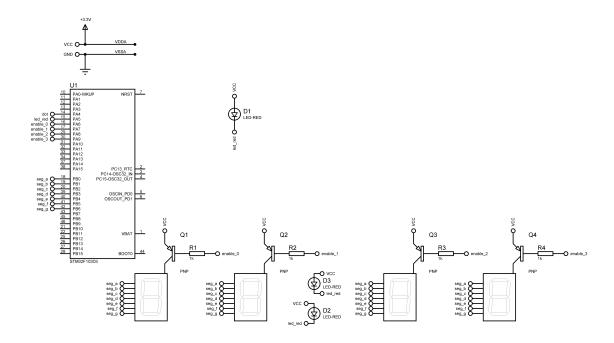


Figure 3.1: Github Link and Back up GG Drive Link



```
Set_timer(100, 0);
    Set_timer(50, 1);
    int enable_trigger = 0;
    int trigger_num = 0;
    while (1)
6
      if(timer_flag[0] == 1)
7
        Set_timer(100,0);
        HAL_GPIO_TogglePin(led_red_GPIO_Port, led_red_Pin);
      if(timer_flag[1] == 1)
14
        Set_timer(50,1);
        switch (enable_trigger)
        case 0:
18
          HAL_GPIO_WritePin(enable_0_GPIO_Port, enable_0_Pin, RESET)
19
          HAL_GPIO_WritePin(enable_1_GPIO_Port, enable_1_Pin, SET);
          HAL_GPIO_WritePin(enable_2_GPIO_Port, enable_2_Pin, SET);
21
          HAL_GPIO_WritePin(enable_3_GPIO_Port, enable_3_Pin, SET);
22
          enable_trigger = 1;
          trigger_num = 1;
          break;
        case 1:
26
          HAL_GPIO_WritePin(enable_0_GPIO_Port, enable_0_Pin, SET);
          HAL_GPIO_WritePin(enable_1_GPIO_Port, enable_1_Pin, RESET)
28
          HAL_GPIO_WritePin(enable_2_GPIO_Port, enable_2_Pin, SET);
29
          HAL_GPIO_WritePin(enable_3_GPIO_Port, enable_3_Pin, SET);
30
          enable_trigger = 2;
31
          trigger_num = 2;
32
          break;
33
        case 2:
          HAL_GPIO_WritePin(enable_0_GPIO_Port, enable_0_Pin, SET);
35
          HAL_GPIO_WritePin(enable_1_GPIO_Port, enable_1_Pin, SET);
36
          HAL_GPIO_WritePin(enable_2_GPIO_Port, enable_2_Pin, RESET)
37
          HAL_GPIO_WritePin(enable_3_GPIO_Port, enable_3_Pin, SET);
          enable_trigger = 3;
39
          trigger_num = 3;
          break;
        case 3:
42
          HAL_GPIO_WritePin(enable_0_GPIO_Port, enable_0_Pin, SET);
43
          HAL_GPIO_WritePin(enable_1_GPIO_Port, enable_1_Pin, SET);
44
          HAL_GPIO_WritePin(enable_2_GPIO_Port, enable_2_Pin, SET);
45
          HAL_GPIO_WritePin(enable_3_GPIO_Port, enable_3_Pin, RESET)
46
```



```
enable_trigger = 0;
          trigger_num = 0;
48
          break;
49
        default:
50
           break;
53
        display7SEG(trigger_num,
             seg_a_GPIO_Port, seg_a_Pin,
55
             seg_b_GPIO_Port, seg_b_Pin,
56
             seg_c_GPIO_Port, seg_c_Pin,
             seg_d_GPIO_Port, seg_d_Pin,
             seg_e_GPIO_Port, seg_e_Pin,
59
             seg_f_GPIO_Port, seg_f_Pin,
60
             seg_g_GPIO_Port, seg_g_Pin);
61
62
63
      /* USER CODE END WHILE */
64
      /* USER CODE BEGIN 3 */
67
```

Program 3.1: main loop



4 Exercise 3+4

```
void update7SEG(int num)
  {
      switch (num)
      {
      case 0:
        HAL_GPIO_WritePin(enable_0_GPIO_Port, enable_0_Pin, RESET);
6
        HAL_GPIO_WritePin(enable_1_GPIO_Port, enable_1_Pin, SET);
        HAL_GPIO_WritePin(enable_2_GPIO_Port, enable_2_Pin, SET);
        HAL_GPIO_WritePin(enable_3_GPIO_Port, enable_3_Pin, SET);
        break;
      case 1:
11
        HAL_GPIO_WritePin(enable_0_GPIO_Port, enable_0_Pin, SET);
        HAL_GPIO_WritePin(enable_1_GPIO_Port, enable_1_Pin, RESET);
        HAL_GPIO_WritePin(enable_2_GPIO_Port, enable_2_Pin, SET);
14
        HAL_GPIO_WritePin(enable_3_GPIO_Port, enable_3_Pin, SET);
        break;
      case 2:
17
        HAL_GPIO_WritePin(enable_0_GPIO_Port, enable_0_Pin, SET);
18
        HAL_GPIO_WritePin(enable_1_GPIO_Port, enable_1_Pin, SET);
19
        HAL_GPIO_WritePin(enable_2_GPIO_Port, enable_2_Pin, RESET);
20
        HAL_GPIO_WritePin(enable_3_GPIO_Port, enable_3_Pin, SET);
        break;
22
      case 3:
23
        HAL_GPIO_WritePin(enable_O_GPIO_Port, enable_O_Pin, SET);
24
        HAL_GPIO_WritePin(enable_1_GPIO_Port, enable_1_Pin, SET);
        HAL_GPIO_WritePin(enable_2_GPIO_Port, enable_2_Pin, SET);
26
        HAL_GPIO_WritePin(enable_3_GPIO_Port, enable_3_Pin, RESET);
27
        break;
      default:
        break;
30
31
32 }
```

Program 4.1: update7SEG() function



```
Set_timer(100, 0);
    Set_timer(25, 1);
    const int MAX_LED = 4;
    int index_led = 0;
    int led_buffer [4] = {1 , 2 , 3 , 4};
    while (1)
7
      if (index_led >= MAX_LED)
10
        index_led = 0;
      }
      if(timer_flag[0] == 1)
14
        Set_timer(100, 0);
        HAL_GPIO_TogglePin(led_red_GPIO_Port, led_red_Pin);
      }
17
      if(timer_flag[1] == 1)
18
19
        Set_timer(25, 1);
20
        display7SEG(led_buffer[index_led],
             seg_a_GPIO_Port, seg_a_Pin,
22
             seg_b_GPIO_Port, seg_b_Pin,
23
             seg_c_GPIO_Port, seg_c_Pin,
             seg_d_GPIO_Port, seg_d_Pin,
             seg_e_GPIO_Port, seg_e_Pin,
26
             seg_f_GPIO_Port, seg_f_Pin,
             seg_g_GPIO_Port, seg_g_Pin);
        update7SEG(index_led++);
29
30
      /* USER CODE END WHILE */
31
32
      /* USER CODE BEGIN 3 */
33
34
```

Program 4.2: Ex3 + Ex4 source



5 Exercise 5

```
void updateClockBuffer(int hour, int minute, int* led_buffer)
    if (hour >= 0 && hour <10)</pre>
      led_buffer[0] = 0;
      led_buffer[1] = hour;
    }
    else
9
      led_buffer[0] = hour/10;
11
      led_buffer[1] = hour%10;
12
13
    if (minute >= 0 && minute <10)</pre>
15
16
      led_buffer[2] = 0;
17
      led_buffer[3] = minute;
18
    }
19
    else
20
21
      led_buffer[2] = minute/10;
22
      led_buffer[3] = minute%10;
23
24
25 }
```

Program 5.1: updateClockBuffer() function



6 Exercise 7+8

```
Set_timer(100, 0);
    Set_timer(50, 1);
    int hour = 24 , minute = 59 , second = 50;
    const int MAX_LED = 4;
    int led_buffer [4] = {1 , 2 , 3 , 4};
    int index_led = 0;
6
    while (1)
8
      if(timer_flag[0] == 1)
9
      {
10
        Set_timer(100, 0);
11
        HAL_GPIO_TogglePin(led_red_GPIO_Port, led_red_Pin);
        HAL_GPIO_TogglePin(dot_GPIO_Port, dot_Pin);
13
        second++;
14
        if(second >= 60)
        {
16
           second = 0;
17
          minute++;
18
        }
19
        if(minute >= 60)
20
21
22
          minute = 0;
          hour++;
23
        }
24
        if(hour >= 24)
26
          hour = 0;
27
        }
28
      }
29
30
      updateClockBuffer(hour, minute, led_buffer);
31
32
      if(timer_flag[1] == 1)
33
34
        Set_timer(50, 1);
35
        if (index_led >= MAX_LED)
          index_led = 0;
38
        }
39
        switch (index_led)
41
        case 0:
42
          HAL_GPIO_WritePin(enable_O_GPIO_Port, enable_O_Pin, RESET)
43
           HAL_GPIO_WritePin(enable_1_GPIO_Port, enable_1_Pin, SET);
          HAL_GPIO_WritePin(enable_2_GPIO_Port, enable_2_Pin, SET);
45
           HAL_GPIO_WritePin(enable_3_GPIO_Port, enable_3_Pin, SET);
46
```



```
break;
        case 1:
          HAL_GPIO_WritePin(enable_0_GPIO_Port, enable_0_Pin, SET);
49
          HAL_GPIO_WritePin(enable_1_GPIO_Port, enable_1_Pin, RESET)
50
          HAL_GPIO_WritePin(enable_2_GPIO_Port, enable_2_Pin, SET);
          HAL_GPIO_WritePin(enable_3_GPIO_Port, enable_3_Pin, SET);
          break;
        case 2:
          HAL_GPIO_WritePin(enable_0_GPIO_Port, enable_0_Pin, SET);
          HAL_GPIO_WritePin(enable_1_GPIO_Port, enable_1_Pin, SET);
56
          HAL_GPIO_WritePin(enable_2_GPIO_Port, enable_2_Pin, RESET)
          HAL_GPIO_WritePin(enable_3_GPIO_Port, enable_3_Pin, SET);
58
          break:
59
        case 3:
60
          HAL_GPIO_WritePin(enable_0_GPIO_Port, enable_0_Pin, SET);
61
          HAL_GPIO_WritePin(enable_1_GPIO_Port, enable_1_Pin, SET);
          HAL_GPIO_WritePin(enable_2_GPIO_Port, enable_2_Pin, SET);
63
          HAL_GPIO_WritePin(enable_3_GPIO_Port, enable_3_Pin, RESET)
          break;
65
        default:
66
          break;
67
        }
        display7SEG(led_buffer[index_led],
                   seg_a_GPIO_Port, seg_a_Pin,
                   seg_b_GPIO_Port, seg_b_Pin,
                   seg_c_GPIO_Port, seg_c_Pin,
                   seg_d_GPIO_Port, seg_d_Pin,
74
                   seg_e_GPIO_Port, seg_e_Pin,
75
                   seg_f_GPIO_Port, seg_f_Pin,
76
                   seg_g_GPIO_Port, seg_g_Pin);
        index_led++;
      }
80
      /* USER CODE END WHILE */
81
82
      /* USER CODE BEGIN 3 */
83
84
```

Program 6.1: Ex7 + Ex8 source