DIGITAL ELECTRONICS LAB WORKSHEET

Experiment 1.3

Visit https://alasso.tech/

Student Name: Alasso

Branch:

UID:

Section/Group:
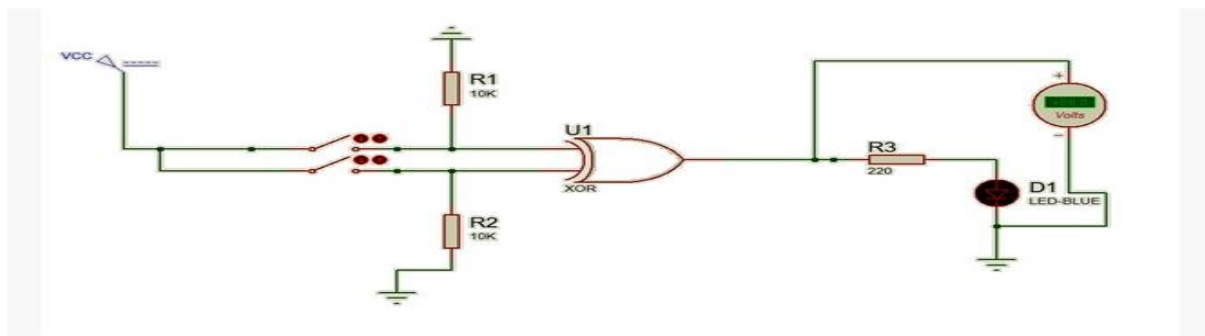
Date of performance:

Subject name: Digital Electronics

Aim:

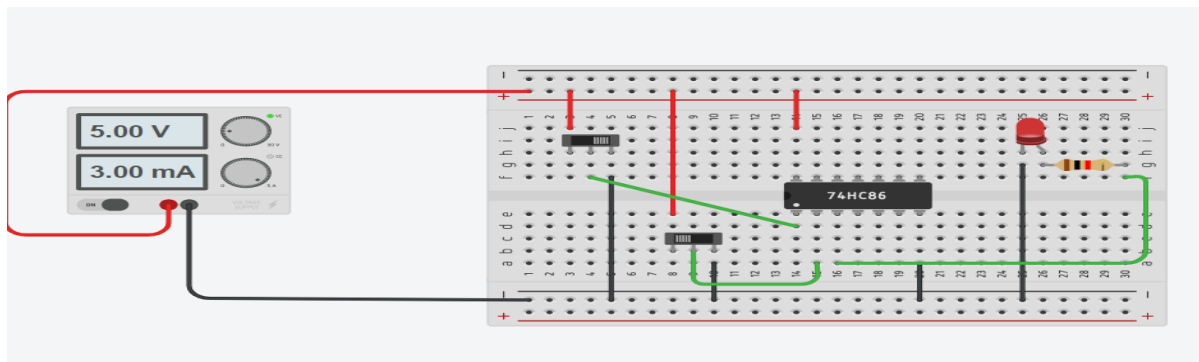Design a two-way switch for room light (XOR).

Apparatus:

7400 (NAND) IC, 7402 (NOR) IC, 7486 (XOR) IC, 5V Power Supply, Breadboard, Connecting wires, Simulation software, Windows 10 PC

Circuit diagram/ Block diagram:

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Simulation Results:

Result

The practical applications of logic gates (NAND, NOR & XOR) were studied and implemented.

Evaluation Grid:

Sr. No.	Parameters	Marks Obtained	Max Marks
1.	Worksheet completion including writing learning objectives/Outcomes. (To be submitted at the end of the day).		10
2.	Post Lab Quiz Result.		5
3.	Student Engagement in Simulation/Demonstration/Performance and Controls/Pre-Lab Questions.		5
	Signature of Faculty (with Date):	Total marks obtain	