

Binary	Total CPU	Net FPGA	Arguments Set	Results Awaited	Total FPGA	Comms Share	Efficiency	Improvement	Parallelism Offset	Frequency
trenz_1024_bit_FSharpParallelAlgorithm	41786	209,7942	0	204	215	0,00%	98%	19335%		
trenz_1024_bit_Fix64Calculator	878	769,2309	1	770	778	0,13%	99%	13%		
trenz_1024_bit_GenomeMatcher	0	0,1561	0	1	3	0,00%	5%	-100%		
trenz_1024_bit_ImageProcessingAlgorithms	2788	26,294	15	54	115	13,04%	23%	2324%	+10	111 111 110Hz
trenz_1024_bit_Loopback	0	0,0001	0	1	12	0,00%	0%	-100%		
trenz_1024_bit_MemoryTest	0	0,0001	0	0	3	0,00%	0%	-100%		
trenz_1024_bit_MonteCarloPiEstimator	4035	31,145	1	25	37	2,70%	84%	10805%	+10	124 999 999Hz
trenz_1024_bit_ObjectOrientedShowcase	0	0,0009	0	1	3	0,00%	0%	-100%		
trenz_1024_bit_ParallelAlgorithm	29395	240,0046	1	240	248	0,40%	97%	11753%	+20	124 999 999Hz
trenz_1024_bit_Posit32AdvancedCalculator	0	0,0255	1	1	3	33,33%	1%	-100%		
trenz_1024_bit_Posit32Calculator	113	521,6813	1	521	532	0,19%	98%	-79%		
trenz_1024_bit_Posit32FusedCalculator	82	44,4477	1	42	50	2,00%	89%	64%		
trenz_1024_bit_PositCalculator	46594	6875,0092	1	6881	6904	0,01%	100%	575%		
trenz_1024_bit_PrimeCalculator	0	0,0003	0	1	6	0,00%	0%	-100%		
trenz_1024_bit_RecursiveAlgorithms	0	0,0006	0	1	3	0,00%	0%	-100%		
trenz_1024_bit_SimdCalculator	0	0,0135	1	1	6	16,67%	0%	-100%		
trenz_1024_bit_UnumCalculator	20	4,7977	1	10	51	1,96%	9%	-61%		

<u>Legend</u>	
Time in ms	
Net FPGA	reported by hardware
Arguments Set	data upload into device
Results Awaited	from kernel scheduled to data returned
Total FPGA	duration in the communication service
Percentage	
Comms Share	timeshare of data transfer (upload)
Efficiency	timshare of processing (net over total)
Improvement	total FPGA speedup over total CPU
Improvement Colors	
Green	speed improvement, examples of beleficial use-case
Red	speed loss, further investigation is advised
Grey	the algorithm is so simple that CPU time is less than 1ms, invalid use-cases where it will never be worth it due to the communication/infrastructure overhead