

Experiment 01: Study of Zener Diode and IC 7805

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1 Aim

- To study Zener diode as a voltage regulator.
- To study IC 7805 as a voltage stabiliser.

2 Theory

2.1 Zener Diode

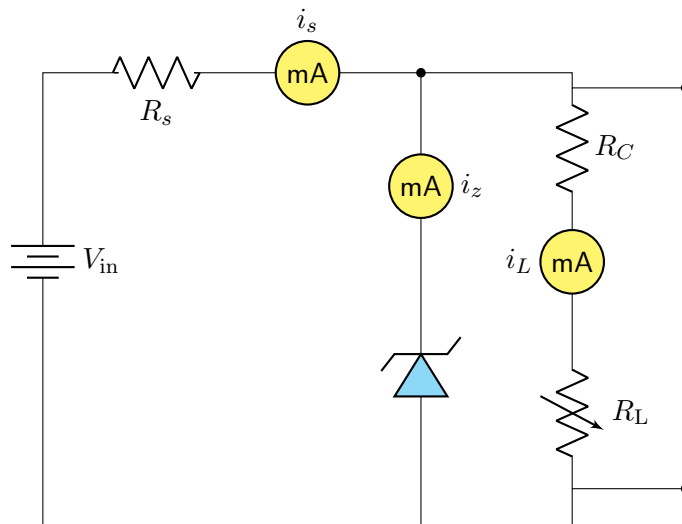


Figure 1: Circuit Diagram for Load and Line Regulation Using a Zener Diode

Zener diode is a specialised diode which works as a regular diode when forward biased but on reverse biasing, the voltage remain constant for a wide range of current. Thus, Zener diode is used as a shunt voltage regulator for regulating voltage across small loads. The breakdown voltage of Zener diodes will be constant for a wide range of current. Zener diode is connected parallel to the load to make it reverse bias and once the Zener diode exceeds knee voltage, the voltage across the load will become constant.

2.2 Integrated Circuit

In Zener diode, the regulation is not perfect and output voltage increases very slowly with increasing reverse input voltage. To resolve this, we use Integrated Circuit (IC) based voltage regulators. We will use IC 7805 for the experiment. The 7805 IC voltage regulator has 3 pins. Pin 1 takes the input voltage and Pin 3 produces the output voltage. The ground of both input and output are given to Pin 2.

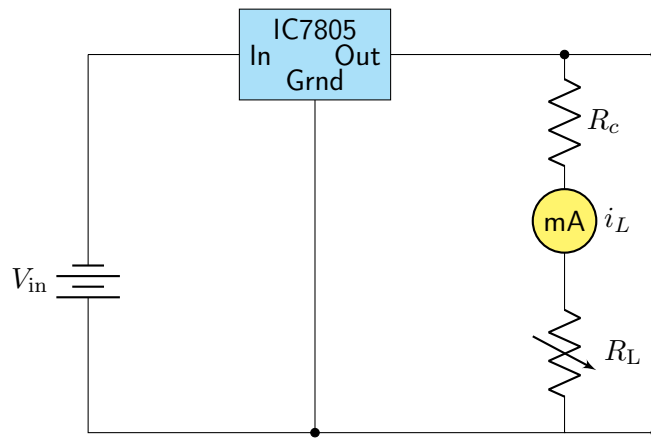


Figure 2: Circuit Diagram for load and line regulation using IC 7805

3 Data and Calculation

3.1 Zener Diode:

3.1.1 Line Regulation

For the line regulation, we first fixed the load resistance to $R_L = 1.1 \text{ k}\Omega$ and varied the input voltage. The output voltage, current across zener diode and the current in the circuit was measured for each input voltage. The data is tabulated below:

V_i (V)	I_s (mA)	I_z (mA)	V_o (V)
0	0	0.00000	0.01
0.6	0	0.00000	0.4
1	0.12	0.00000	0.69
1.6	0.23	0.00000	1.02
2.1	0.35	0.00000	1.31
2.4	0.46	0.00000	1.52
3	0.12	0.00000	1.85
3.5	0.23	0.00000	2.16
3.9	0.58	0.00000	2.43
4.5	0.69	0.00000	2.77
5	0.81	0.00002	3.09
5.4	0.93	0.00008	3.31
6	1.05	0.00023	3.68
6.5	1.18	0.00040	3.92
7	1.27	0.00083	4.23
7.5	1.39	0.00161	4.57
8	1.51	0.00337	4.87
8.5	1.63	0.00637	5.16
8.9	1.74	0.01132	5.40
9.5	1.86	0.01666	5.71
10	1.98	0.03700	6.05
10.5	1.58	0.04000	6.35
11	2.11	0.10000	6.50
11.5	2.30	0.30000	6.53
11.9	2.50	0.50000	6.57
12.5	2.74	0.70000	6.55
13	3.01	1.00000	6.56
13.6	3.24	1.30000	6.57
13.9	3.41	1.40000	6.57

V_i (V)	I_s (mA)	I_z (mA)	V_o (V)
14.5	3.67	1.70000	6.57
15	3.89	1.90000	6.58

Proportionality of Zener and circuit current:

From the data, we plotted I_z vs I_s and found that the graph is linear. Initially the current was very low (in range of microamperes). At around $V_i = 10.5$ V, the current started to increase rapidly (in range of milliamperes). The graph for data points after $V_i = 10.5$ V is shown below along with linear fit curve:

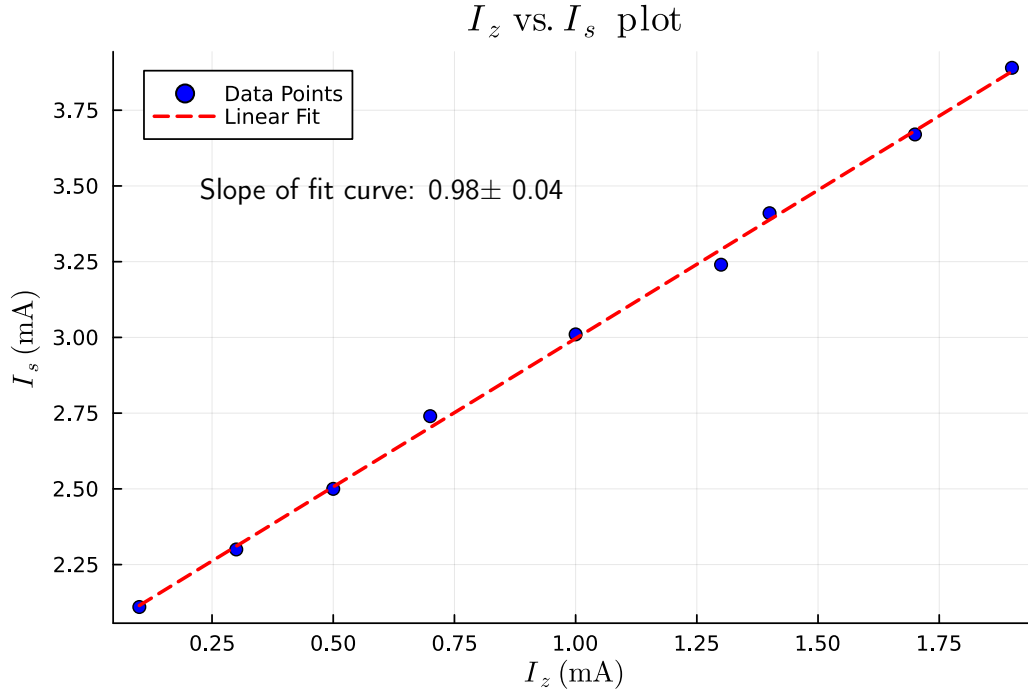


Figure 3: Plot of I_z vs I_s with constant load

From the linear fit, we obtained the slope to be $m = 0.98 \pm 0.04$ which verifies $\delta I_z = \delta I_s$.

Estimating Breakdown Voltage From the above table, we plot the input vs the output voltage.

Input vs. Output voltage plot

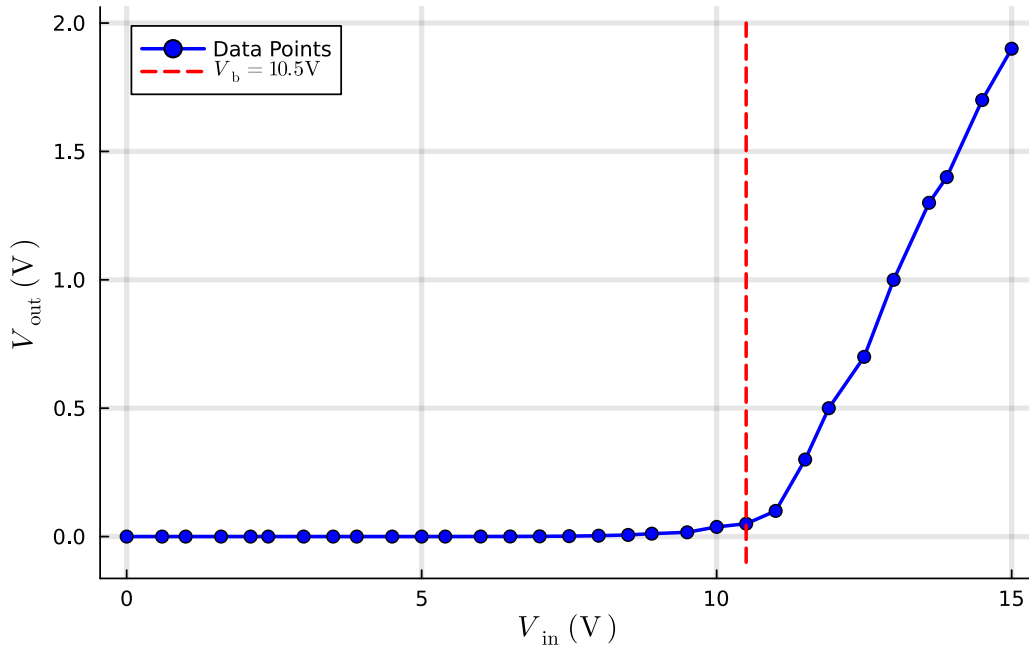


Figure 4: Plot between input and output voltage with constant load

From the plot, we can see that initially there was no change in the output voltage but at $V_{in} = 10.5$, the output voltage increased rapidly. Thus, from the experimental plot, we can estimate the breakdown voltage of the given Zener diode to be:

$$V_b = 10.5V$$

3.1.2 Load Regulation

Without R_c :

For the load regulation, we fixed the input voltage to $V_i = 15$ V and varied the load resistance using a potentiometer. The output voltage, current across zener diode and the load current was measured for each load resistance. The data is tabulated below:

R_L (k Ω)	I_L (mA)	I_z (mA)	V_o (V)
0.007	6.92	0	0.2
0.057	6.76	0	0.5
0.106	6.61	0	0.9
0.153	6.5	0	1.2
0.203	6.36	0	1.4
0.249	6.25	0	1.7
0.304	6.11	0	2.1
0.354	6.02	0	2.3
0.396	5.92	0	2.6
0.452	5.78	0	2.8
0.513	5.64	0	3.1
0.551	5.53	0	3.3
0.609	5.44	0	3.5
0.655	5.34	0	3.7
0.713	5.24	0	3.9
0.751	5.16	0	4.1
0.802	5.06	0	4.3
0.856	5	0	4.5
0.906	4.92	0	4.7

R_L (k Ω)	I_L (mA)	I_z (mA)	V_o (V)
0.955	4.84	0	4.9
1.017	4.73	0	5

With R_c :

3.2 IC 7805

3.2.1 Line Regulation

3.2.2 Load Regulation

4 Conclusion