

Experiment 02: Study of Transistor Characteristics

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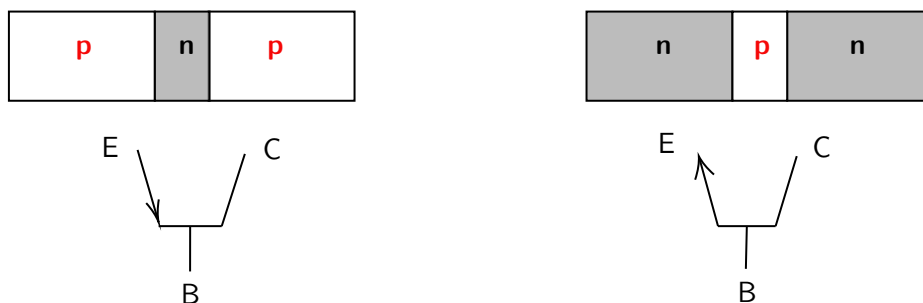
1 Aim

To obtain the input and output characteristics of an n-p-n transistor in CE configuration.

2 Theory

2.1 Bipolar Junction Transistor (BJT)

The Bipolar Junction Transistor (BJT) is essentially a combination of two PN junction diodes. According to the order in which the diodes are connected, we have npn and pnp transistors. The emitter region is much smaller than the collector and much more strongly doped while the base is very thin and very lightly doped. A large collector is needed since it should have more surface area for dissipating the power generated there.



It is a three terminal device with the terminals named as emitter, base and collector but to act as an amplifier, we need four terminals. Hence, we make one terminal common. The most commonly studied configuration is the common emitter configuration where the emitter is common to both input and output.

A BJT works in three modes: Active, Saturation and Cutoff Modes, according to which junction is forward or reverse biased.

- **Active Mode:** BE junction is forward biased and BC junction is reverse biased. In this region, transistor acts as an amplifier.
- **Saturation Mode:** BE junction is forward biased and BC junction is forward biased.
- **Cutoff Mode:** BE junction is reverse biased and BC junction is reverse biased.

2.2 Common Emitter Configuration of BJT

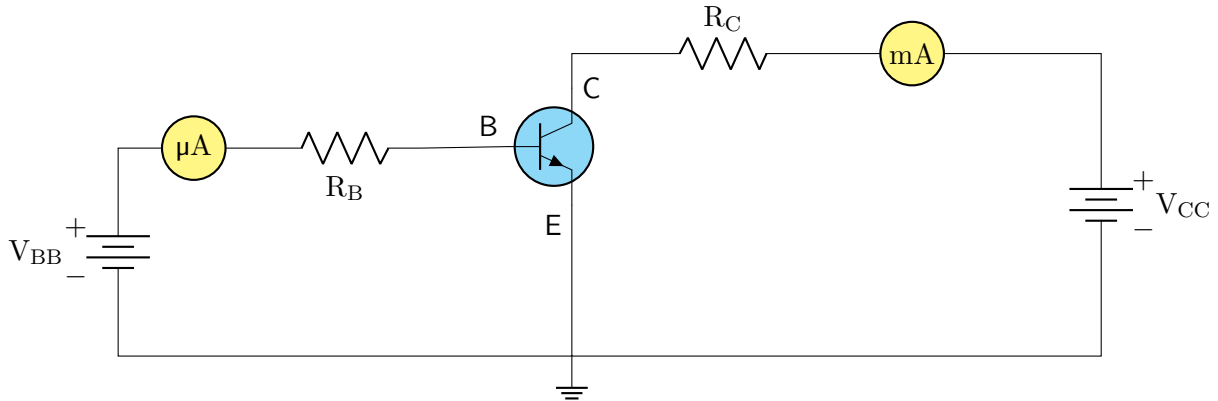


Figure 1: Circuit diagram of a common emitter configuration of a NPN transistor

The above figure shows the common emitter configuration of the transistor. From the figure, we can see:

$$I_E = I_B + I_C$$

Normally when used as an amplifier, the base emitter junction is kept in forward bias and the collector base junction is kept in reverse bias. So, due to the forward bias of the base emitter junction electrons go from emitter to the base, this electrons get affected by the reverse bias of the collector base junction and as the base is very thin and lightly doped most of the electrons go to the collector. So the emitter current is almost equal to the collector current. We define two current gain parameters β and α for the transistor as follows:

$$\beta = \frac{I_C}{I_B} \quad \alpha = \frac{I_C}{I_E}$$

From this, we get a relation between α and β as:

$$\alpha = \frac{\beta}{1 + \beta} \implies \beta = \frac{\alpha}{1 - \alpha}$$

Generally β is much greater than 1 while α is very close to but less than 1. In the CE configuration the input current and voltage are I_B and V_{BE} , while the output current and voltage are I_C and V_{CE} . Thus, for the input characteristics, we plot I_B vs V_{BE} and for the output characteristics, we plot I_C vs V_{CE} .

3 Data and Calculations

3.1 Input Characteristics

The following tables show the data obtained for the input characteristics of the n-p-n transistor in CE configuration. We used $R_B = 1 \text{ k}\Omega$ and $R_C = 1 \text{ }\Omega$. The data was obtained by varying the base voltage V_{BB} from 0 to 1.2 V in steps of 0.1 V. The collector voltage V_{CC} was kept constant at almost 2V. The base current I_B and collector current I_C were measured for each value of V_{BB} . The base-emitter voltage V_{BE} and collector-emitter voltage V_{CE} were also measured for each value of V_{BB} . The data was obtained for two different values of V_{CC} as 2 V and 3 V.

V_{BB} (V)	V_{BE} (V)	I_B (μA)	I_C (mA)	V_{CC} (V)	V_{CE} (V)
0	0.036	0	0	2	2
0.1	0.178	0	0	2	2
0.2	0.251	0	0	2	2
0.3	0.378	0	0	2	2

V_{BB} (V)	V_{BE} (V)	I_B (μ A)	I_C (mA)	V_{CC} (V)	V_{CE} (V)
0.4	0.449	0	0	2	2
0.5	0.589	3	5	2	2
0.6	0.655	28	4.7	2	2
0.7	0.698	91	16.3	2	2
0.8	0.729	182	32.6	2	2
0.9	0.738	226	41.1	2	2
1	0.762	338	61.6	2	2
1.1	0.778	414	76.1	2	2
1.2	0.792	499	92.3	2	2

V_{BB} (V)	V_{BE} (V)	I_B (μ A)	I_C (mA)	V_{CC} (V)	V_{CE} (V)
0	0.038	0	0	3	3
0.1	0.19	0	0	3	3
0.2	0.259	0	0	3	3
0.3	0.355	1	0	3	3
0.4	0.466	1	0	3	3
0.5	0.607	9	1.6	3	3
0.6	0.642	27	5	3	3
0.7	0.684	88	16.2	3	3
0.8	0.71	158	29.4	3	3
0.9	0.729	242	45.9	3	3
1	0.749	334	64.8	3	3
1.1	0.767	449	87.2	3	3
1.2	0.765	468	93	3	3

We plotted the I_B vs. V_{BE} graph for the two values of V_{CC} and obtained the following graphs.

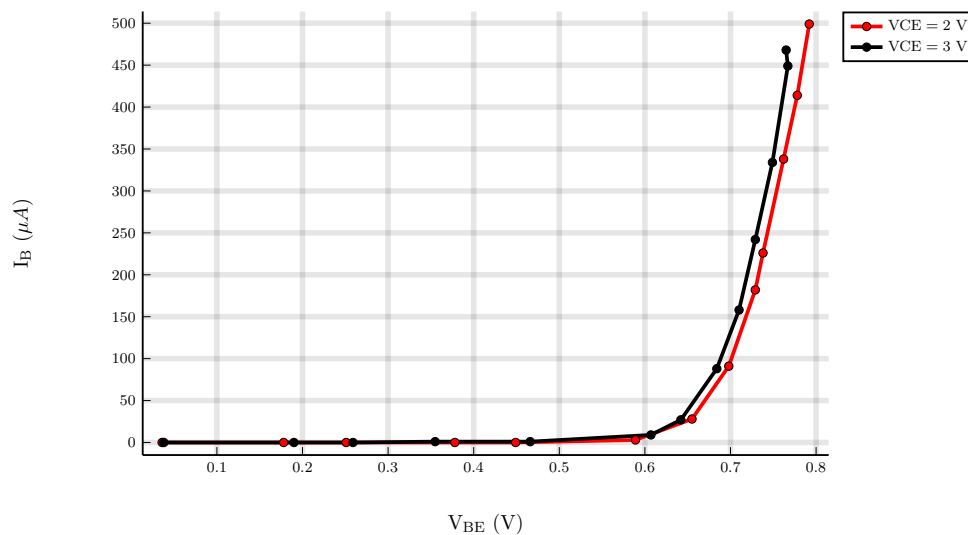


Figure 2: Input characteristics of a BJT transistor

We note that the input characteristics for the two values of V_{CC} are opposite of what is expected. The curve for $V_{CE} = 3$ V should be below that of $V_{CE} = 2$ V. This can be due to the values being very close to each other, so these could not be clearly distinguished and hence the error.

3.2 Output Characteristics

The following tables show the data obtained for the output characteristics of the transistor in CE configuration. The data was obtained by varying the base voltage V_{BB} . We used $R_B = 220\ \Omega$ and $R_C = 1\ \text{k}\Omega$. I_B was kept constant by varying V_{BB} . The collector current I_C and collector-emitter voltage V_{CE} were measured. The data was obtained for three different values of $I_B = 25\ \mu\text{A}$, $30\ \mu\text{A}$ and $40\ \mu\text{A}$.

V_{BB} (V)	I_B (μA)	V_{CC} (V)	V_{CE} (V)	I_C (μA)
0.5	25	0.0	0.004	10
0.5	25	0.5	0.04	506
0.5	25	1.0	0.057	922
0.5	25	1.5	0.069	1308
0.5	25	2.0	0.081	1784
0.5	25	2.5	0.099	2390
0.5	25	3.0	0.107	2880
0.5	25	3.5	0.127	3350
0.5	25	4.0	0.15	3780
0.6	25	4.2	0.172	3960
0.6	25	4.4	0.2	4150
0.6	25	4.6	0.228	4340
0.6	25	4.8	0.42	4350
0.6	25	4.7	0.349	4380
0.6	25	5.0	0.49	4340
0.6	25	5.2	0.696	4460
0.6	25	5.5	0.978	4460

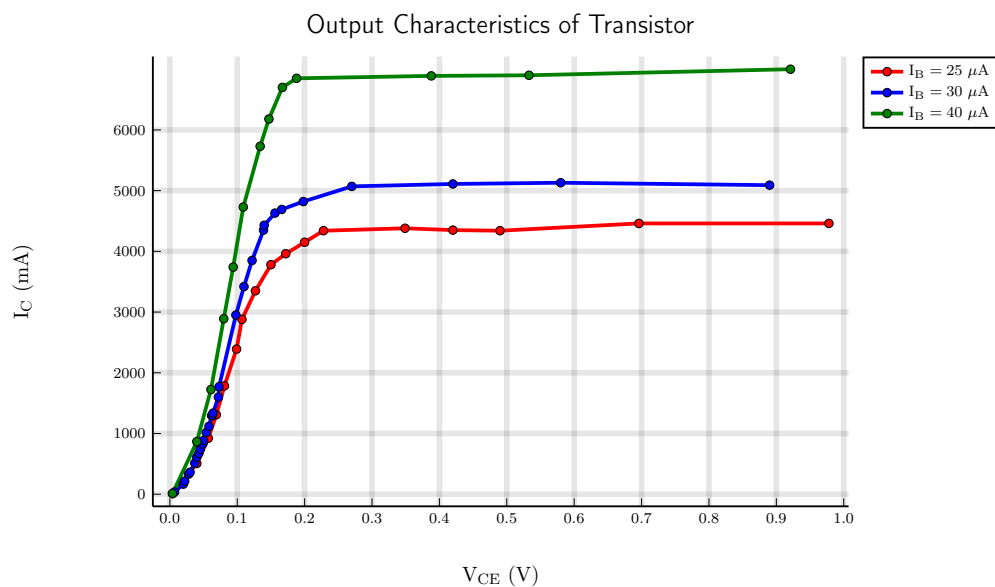
Table 2: Data obtained for output characteristics at $I_B = 25\ \mu\text{A}$

V_{CC} (V)	V_{BB} (V)	I_B (μA)	V_{CE} (V)	I_C (μA)
0.0	0.5	30	0.007	37
0.1	0.5	30	0.02	166
0.2	0.5	30	0.022	214
0.3	0.5	30	0.028	331
0.4	0.5	30	0.03	362
0.5	0.5	30	0.037	513
0.6	0.5	30	0.04	610
0.7	0.5	30	0.043	671
0.8	0.5	30	0.045	737
0.9	0.5	30	0.049	829
1.0	0.5	30	0.05	884
1.1	0.5	30	0.054	1016
1.2	0.5	30	0.058	1118
1.3	0.5	30	0.062	1294
1.4	0.5	30	0.063	1296
1.5	0.5	30	0.064	1335
1.8	0.5	30	0.072	1599
2.0	0.5	30	0.073	1770
3.0	0.5	30	0.098	2950
3.5	0.6	30	0.11	3420
4.0	0.6	30	0.122	3850
4.5	0.6	30	0.139	4350

V_{CC} (V)	V_{BB} (V)	I_B (μ A)	V_{CE} (V)	I_C (μ A)
4.6	0.6	30	0.14	4430
4.8	0.6	30	0.156	4630
4.9	0.6	30	0.166	4690
5.0	0.6	30	0.198	4820
5.5	0.6	30	0.27	5070
5.6	0.6	30	0.42	5110
5.8	0.6	30	0.58	5130
6.0	0.6	30	0.89	5090

Table 3: Data obtained for output characteristics at $I_B = 30 \mu\text{A}$

V_{BB} (V)	I_B (μ A)	V_{CC} (V)	V_{CE} (V)	I_C (μ A)
0.5	40	0.0	0.004	12
0.5	40	1.0	0.04	866
0.5	40	2.0	0.061	1724
0.5	40	3.0	0.08	2890
0.5	40	4.0	0.094	3740
0.6	40	5.0	0.109	4730
0.6	40	6.0	0.134	5730
0.6	40	6.5	0.147	6180
0.6	40	7.0	0.167	6700
0.6	40	7.2	0.188	6850
0.6	40	7.4	0.388	6890
0.6	40	7.6	0.533	6900
0.6	40	8.0	0.921	7000

Table 4: Data obtained for output characteristics at $I_B = 40 \mu\text{A}$ **Figure 3:** Output characteristics of BJT

We see that the collector current I_C initially increases steeply with the increase in V_{CE} but reaches saturation and remains almost constant after a certain value of the voltage. Ideally, the collector current should increase

linearly with the increase in V_{CE} after a certain value of the voltage but we did not observe this in our experiment very clearly. So may be the increasing rate is so small that it is getting suppressed by the fluctuation in V_{CE} values or maybe the increase would be more visible for higher values of V_{CE} which we did not measure.

3.2.1 Calculation of β

We will calculate the value of the current gain β for the transistor in the active region using the formula $\beta = \frac{I_C}{I_B}$. Once we reach the region where the transistor is in the active region, we take the values of the collector and the base current at different values of V_{CE} . The following table shows the values of β calculated for the transistor.

V_{CE} (V)	I_C (μ A)	I_B (μ A)	β
0.188	6850	40	171.25
0.388	6890	40	172.25
0.533	6900	40	172.5
0.921	7000	40	175.00
0.270	5070	30	169.00
0.420	5110	30	170.33
0.580	5130	30	171.00
0.890	5090	30	169.66
0.420	4350	25	174.00
0.349	4380	25	175.20
0.490	4340	25	173.60
0.696	4460	25	178.40
0.978	4460	25	178.40

From this, we get the average value of β for the transistor as 173.12.

4 Sources of Error

The following could be potential sources of error which could have affected our results:

- Different multimeters could have different internal resistances which could have affected the measurement of current and voltage.
- There was much fluctuation in I_B in the output characteristics and thus, I_B may not be exactly constant for the curves. I_B was very sensitive to the change in V_{BB} .

5 Discussion and Conclusion

In this experiment, we obtained the input and output characteristics of a transistor in CE configuration. For the input characteristics, we got an exponential like plot for T_B as expected. However, we observed that the input characteristics for two different values of V_{CE} are not as expected which could be due to the values of the two curves being very close to each other.

We could perform the experiment for two values of V_{CE} with larger difference for the input characteristics. However, we also have to maintain the maximum current through the transistor, otherwise due to heat generated by the current, the transistor may get destroyed.

The output characteristics show that the collector current I_C initially increases steeply with the increase in V_{CE} but reaches saturation and remains almost constant after a certain value of the voltage. However, we did not observe the linear increase in I_C with V_{CE} after a certain value of the voltage. This could be due to the error in the measurement of the current and voltage.