

Experiment 02: Study of Transistor Characteristics

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1 Aim

2 Theory

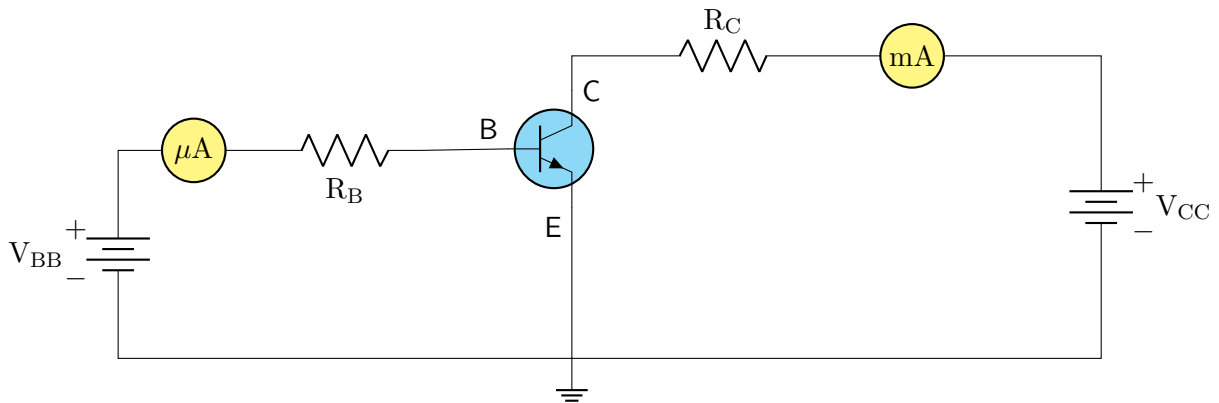
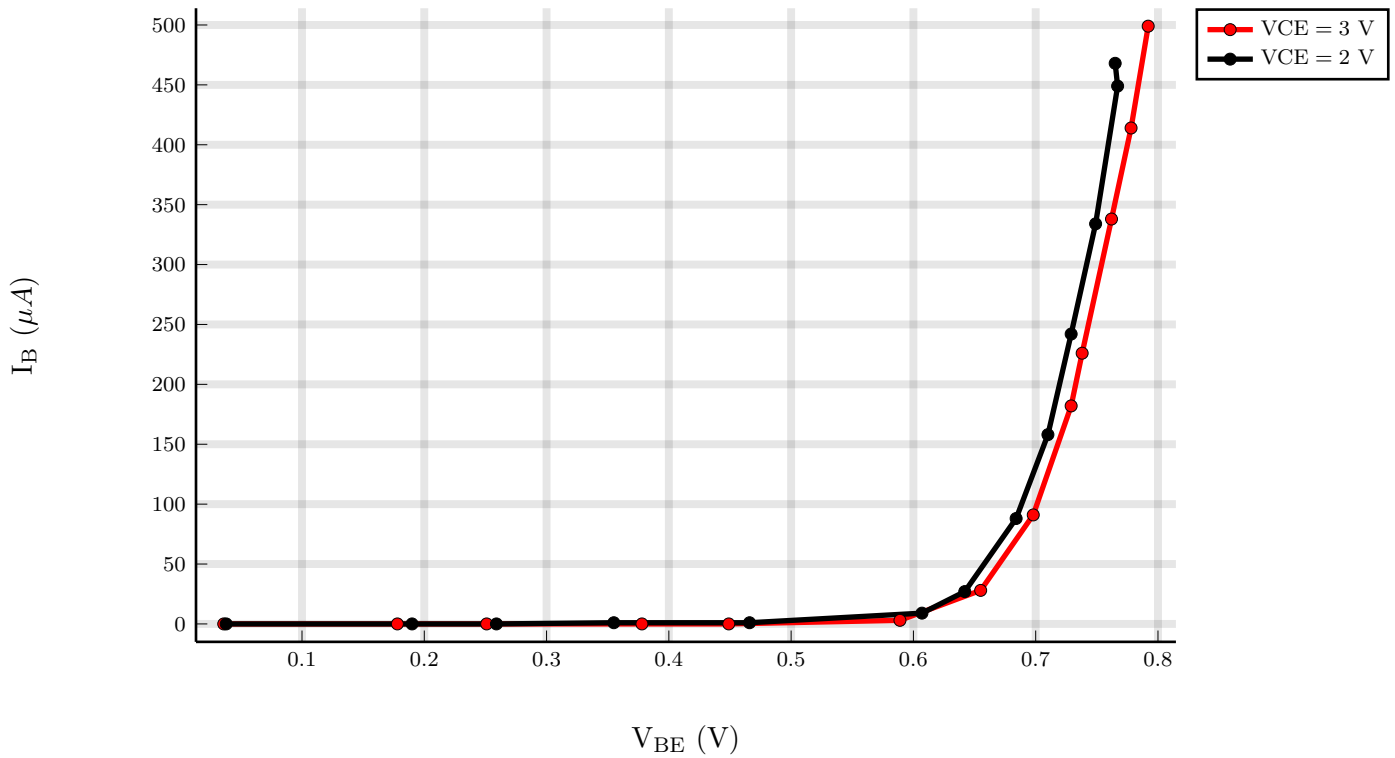


Figure 1: Circuit diagram of a common emitter configuration of a NPN transistor

3 Data and Calculations

3.1 Input Characteristics



3.2 Output Characteristics

V_{BB} (V)	I_B (μA)	V_{CC} (V)	V_{CE} (mV)	I_C (μA)
0.5	25	0.0	0.004	10
0.5	25	0.5	0.04	506
0.5	25	1.0	0.057	922
0.5	25	1.5	0.069	1308
0.5	25	2.0	0.081	1784
0.5	25	2.5	0.099	2390
0.5	25	3.0	0.107	2880
0.5	25	3.5	0.127	3350
0.5	25	4.0	0.15	3780
0.6	25	4.2	0.172	3960
0.6	25	4.4	0.2	4150
0.6	25	4.6	0.228	4340
0.6	25	4.8	0.42	4350
0.6	25	4.7	0.349	4380
0.6	25	5.0	0.49	4340
0.6	25	5.2	0.696	4460
0.6	25	5.5	0.978	4460

Table 1: Data obtained for output characteristics at $I_B = 25\text{ }\mu A$

V_{CC} (V)	V_{BB} (V)	I_B (μ A)	V_{CE} (mV)	I_C (μ A)
0	0.5	30	0.007	37
0.1	0.5	30	0.02	166
0.2	0.5	30	0.022	214
0.3	0.5	30	0.028	331
0.4	0.5	30	0.03	362
0.5	0.5	30	0.037	513
0.6	0.5	30	0.04	610
0.7	0.5	30	0.043	671
0.8	0.5	30	0.045	737
0.9	0.5	30	0.049	829
1	0.5	30	0.05	884
1.1	0.5	30	0.054	1016
1.2	0.5	30	0.058	1118
1.3	0.5	30	0.062	1294
1.4	0.5	30	0.063	1296
1.5	0.5	30	0.064	1335
1.8	0.5	30	0.072	1599
2	0.5	30	0.073	1770
3	0.5	30	0.098	2950
3.5	0.6	30	0.11	3420
4	0.6	30	0.122	3850
4.5	0.6	30	0.139	4350
4.6	0.6	30	0.14	4430
4.8	0.6	30	0.156	4630
4.9	0.6	30	0.166	4690
5	0.6	30	0.198	4820
5.5	0.6	30	0.27	5070
5.6	0.6	30	0.42	5110
5.8	0.6	30	0.58	5130
6	0.6	30	0.89	5090

Table 2: Data obtained for output characteristics at $I_B = 30 \mu$ A

V_{BB} (V)	I_B (μ A)	V_{CC} (V)	V_{CE} (mV)	I_C (μ A)
0.5	40	0.0	0.004	12
0.5	40	1.0	0.04	866
0.5	40	2.0	0.061	1724
0.5	40	3.0	0.08	2890
0.5	40	4.0	0.094	3740
0.6	40	5.0	0.109	4730
0.6	40	6.0	0.134	5730
0.6	40	6.5	0.147	6180
0.6	40	7.0	0.167	6700
0.6	40	7.2	0.188	6850
0.6	40	7.4	0.388	6890
0.6	40	7.6	0.533	6900
0.6	40	8.0	0.921	7000

Table 3: Data obtained for output characteristics at $I_B = 40 \mu$ A

