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Diode Circuit

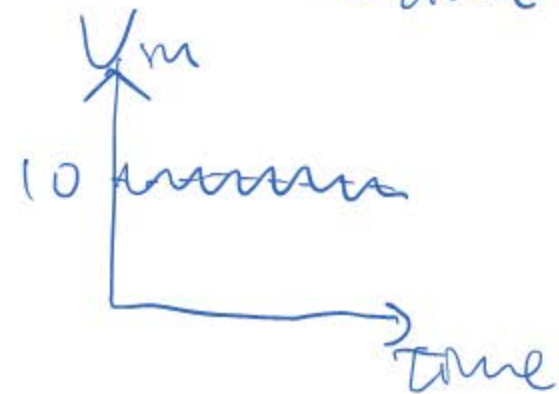
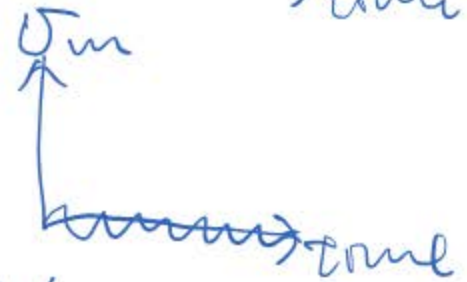
VE311 Electronic Circuits (Fall 2020)

Dr. Chang-Ching Tu

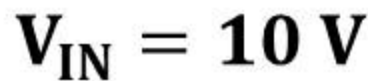
$$V_{IN} = 10V$$

$$V_{in} = 0.018mwt$$

$$V_m = 10 + 0.018mwt$$



$$I_D = I_S \left(e^{\frac{qV_D}{kT}} - 1 \right)$$



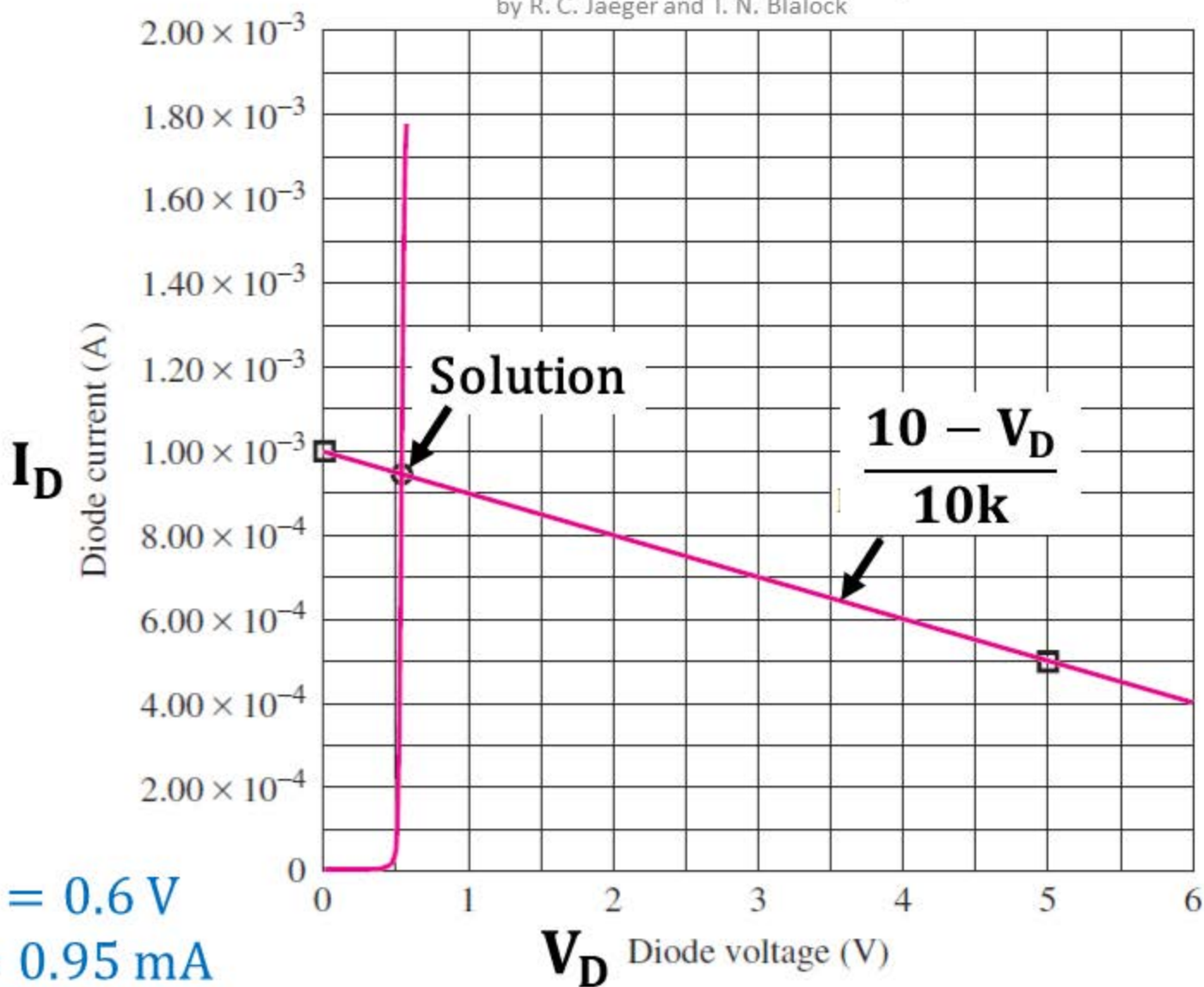
$$I_D = ?$$

$$V_D = ?$$

1. Graphical analysis
2. Mathematical analysis
3. Simplified analysis
(ideal diode)
4. Simplified analysis
(constant voltage drop)

Graphical Analysis

Source: Microelectronic Circuit Design, 4th Edition,
by R. C. Jaeger and T. N. Blalock



Mathematical Analysis

$$V_{IN} = I_D R + V_D$$

$$V_{IN} = \left[I_S \left(e^{\frac{qV_D}{kT}} - 1 \right) \right] R + V_D$$

$$10 = \left[10^{-13} \left(e^{\frac{V_D}{0.0258}} - 1 \right) \right] 10^4 + V_D$$

Above is a transcendental equation which does not have a close-form **analytical solution**. So, through trial and error, we seek a **numerical solution**.

function xd = diode(vd)

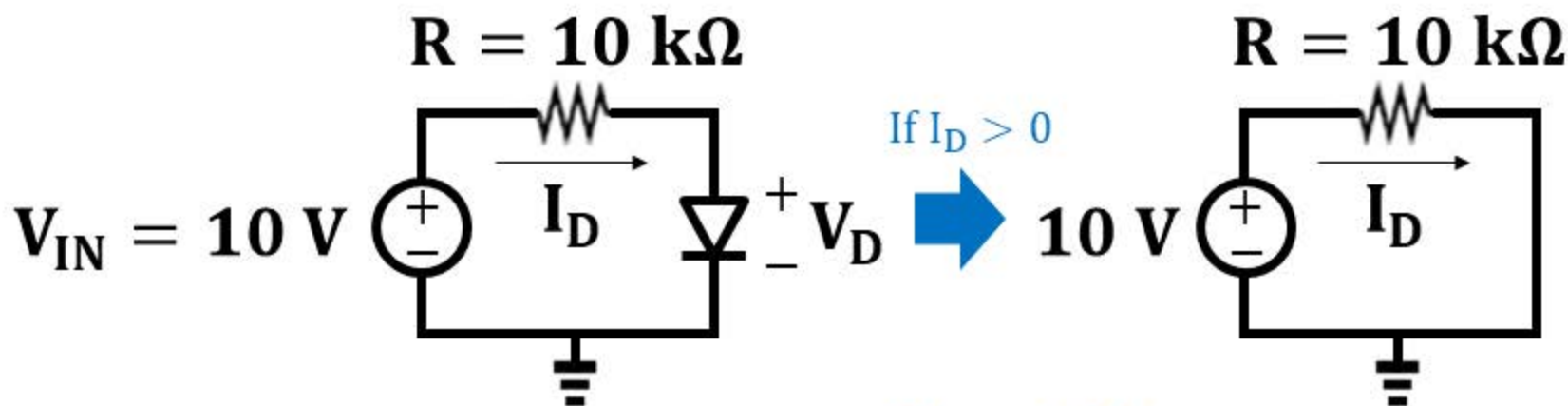
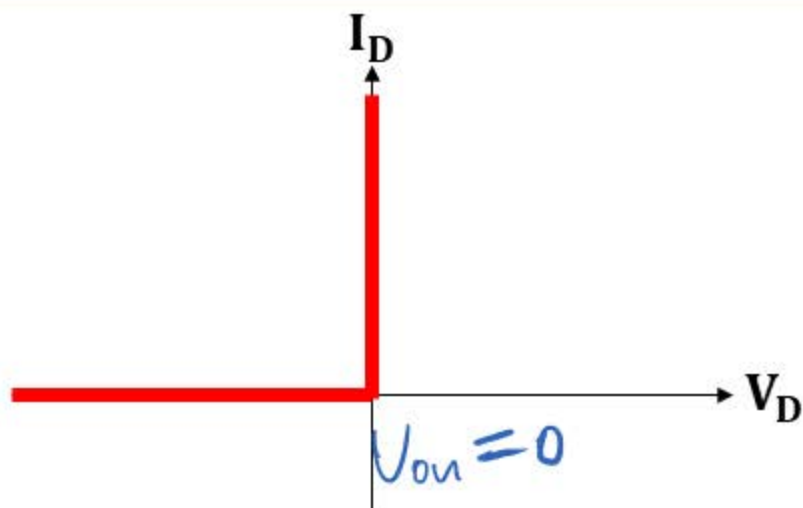
$$xd = 10 - (10^{-9}) * \left(\exp\left(\frac{vd}{0.0258}\right) - 1 \right) - vd$$

Use MATLAB to plot xd as a function of vd, and find out a vd that makes xd closest to zero.

$$V_D = 0.5742 \text{ V}$$

$$I_D = 0.944 \text{ mA}$$

Simplified Analysis (Ideal Diode)

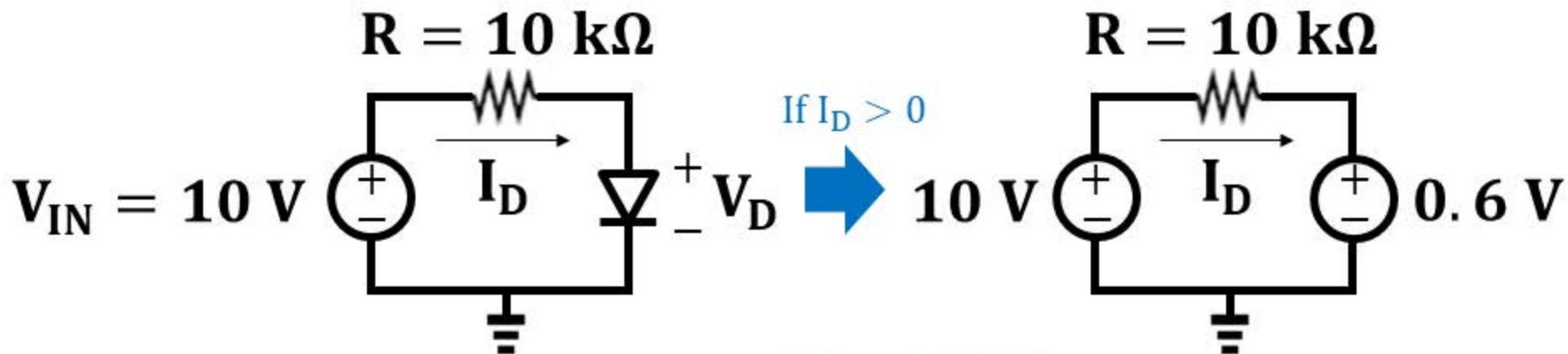
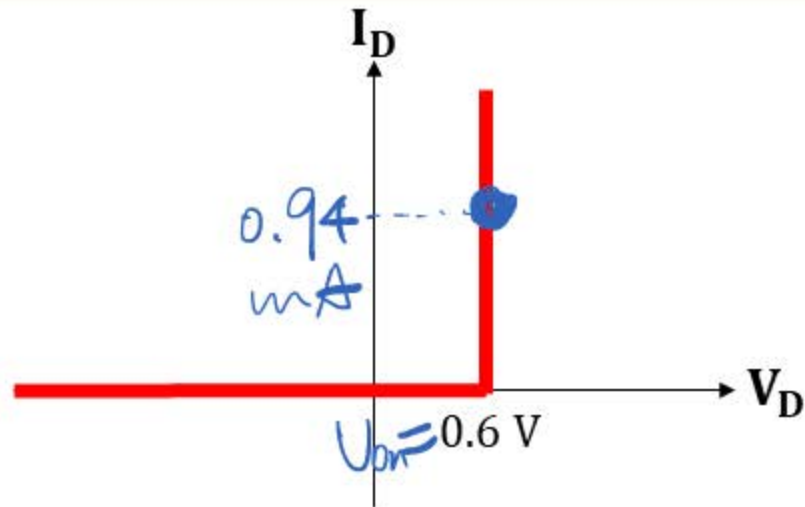


$$V_D = 0\text{ V}$$

$$I_D = \frac{(10 - 0)\text{ V}}{10\text{ k}\Omega} = 1\text{ mA}$$

Simplified Analysis (Constant Voltage Drop)

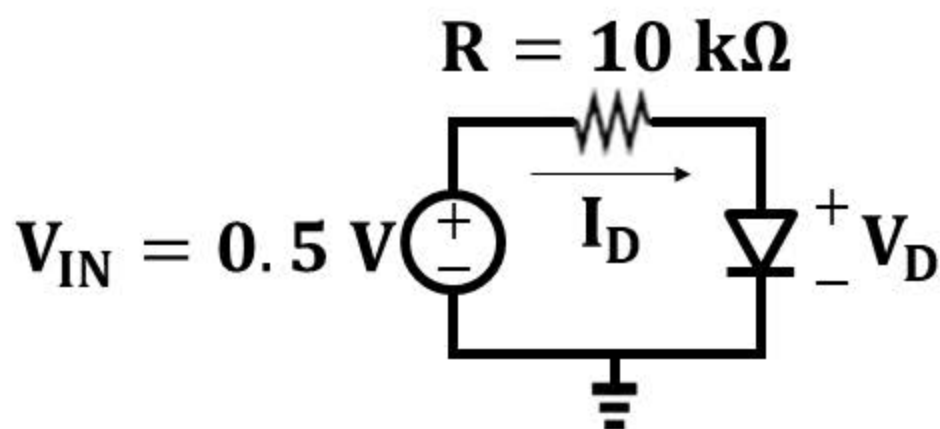
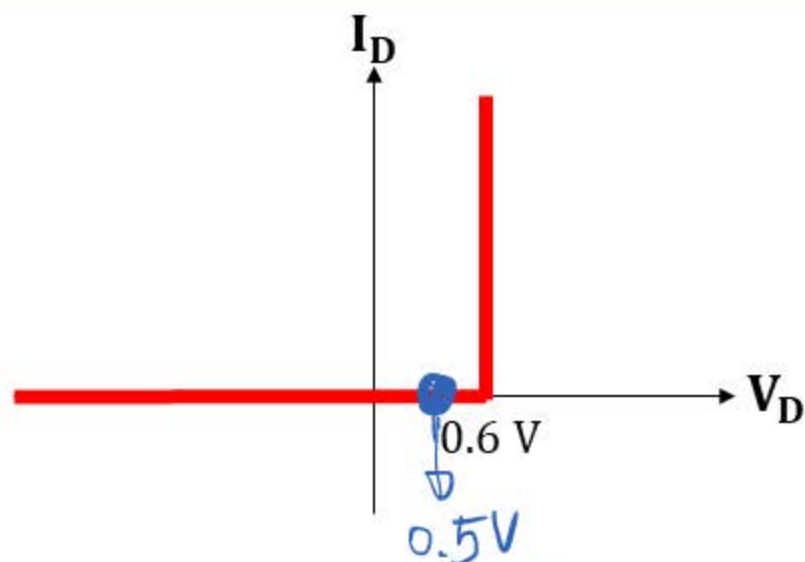
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V_D a power supply?
What happens if $V_{IN} = 0.5$ V?

$$V_D = 0.6 \text{ V}$$
$$I_D = \frac{(10 - 0.6) \text{ V}}{10 \text{ k}\Omega} = 0.94 \text{ mA}$$

Simplified Analysis (Constant Voltage Drop)

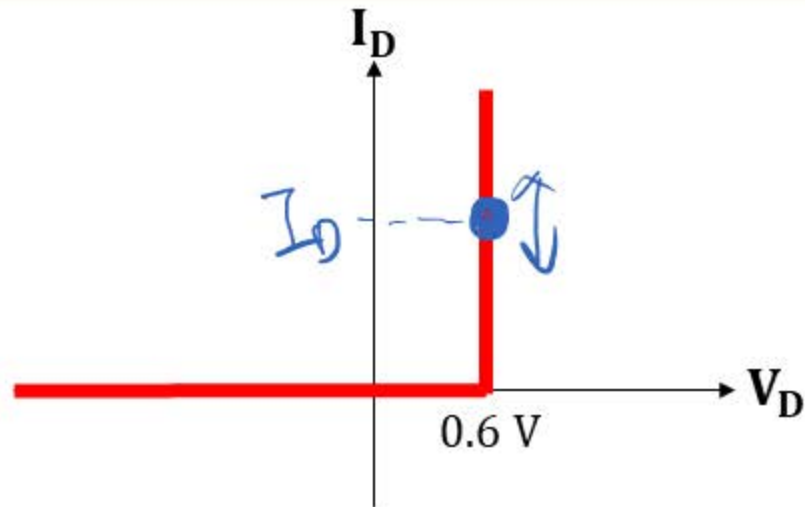


$$I_D = 0\text{ A}$$

$$V_D = 0.5\text{ V}$$

Simplified Analysis (Constant Voltage Drop)

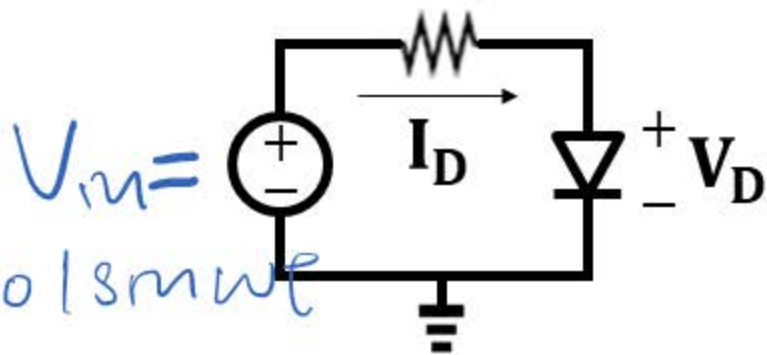
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$2 + 0.015 \text{ mW}$

$$I_d = I_0 + \hat{I}_d = \frac{V_m - 0.6}{10 \text{ k}\Omega}$$
$$V_0 = 0.6 \text{ V}$$

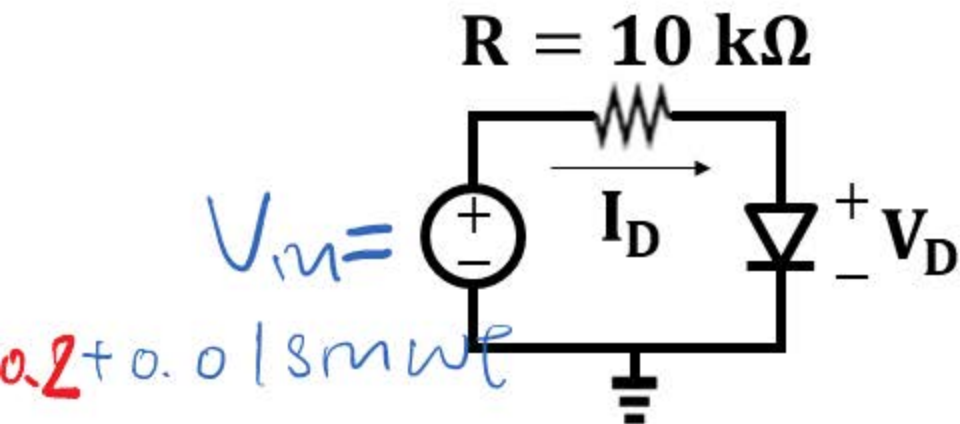
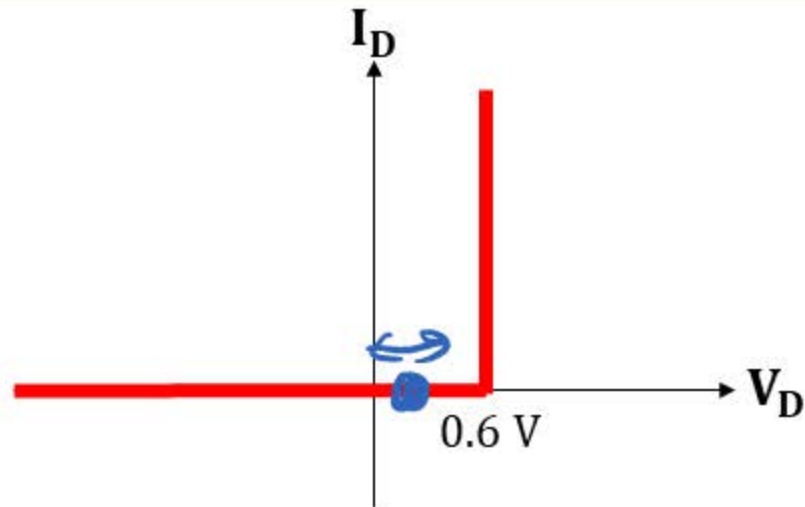
$R = 10 \text{ k}\Omega$



$V_m =$

$2 + 0.015 \text{ mW}$

Simplified Analysis (Constant Voltage Drop)

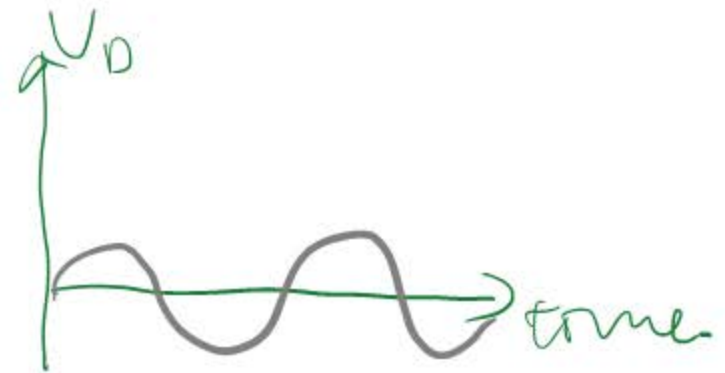
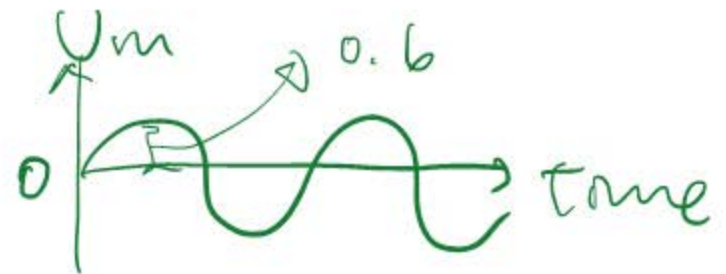
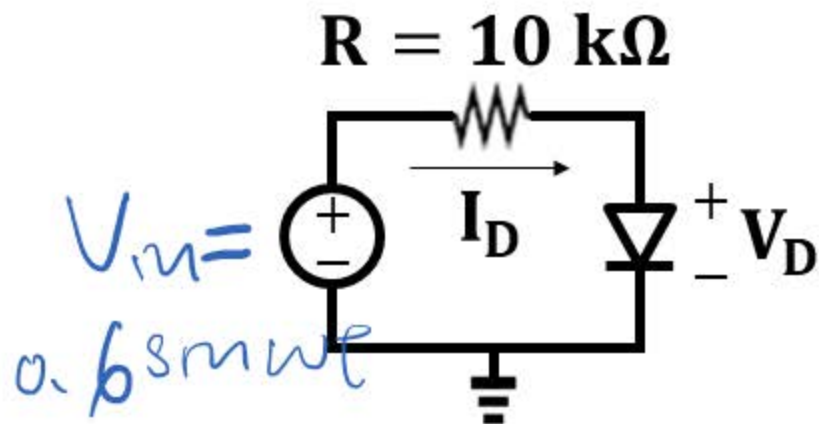
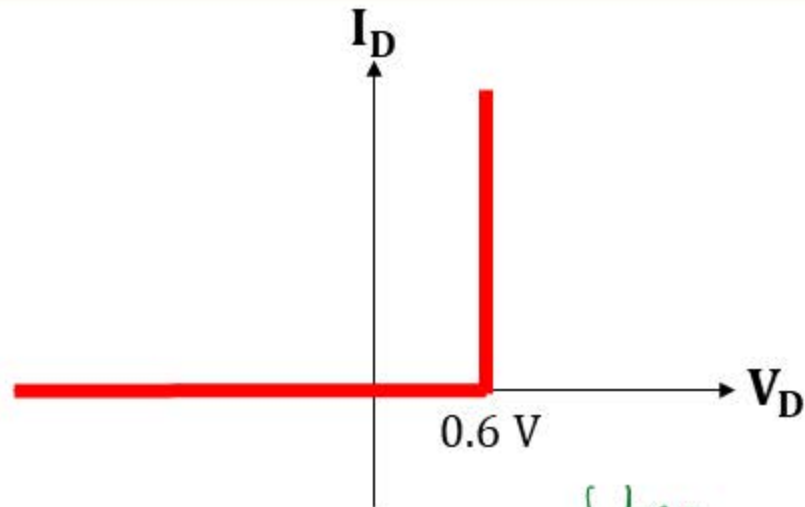


$$I_d = 0 \text{ A}$$

$$V_D = 0.2 + 0.018 \text{ mV}$$

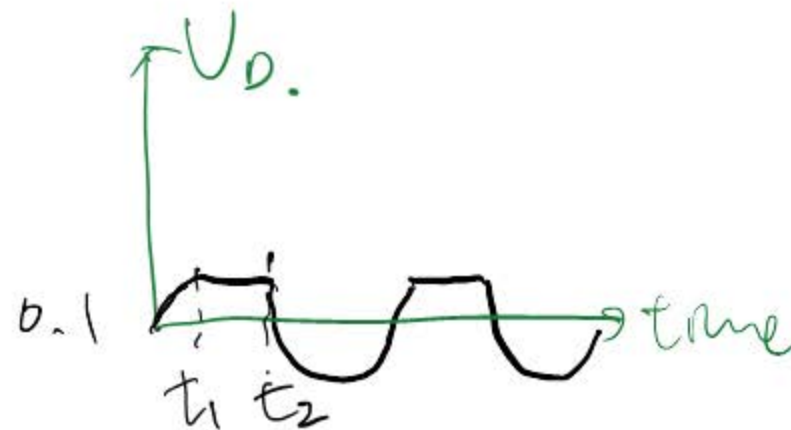
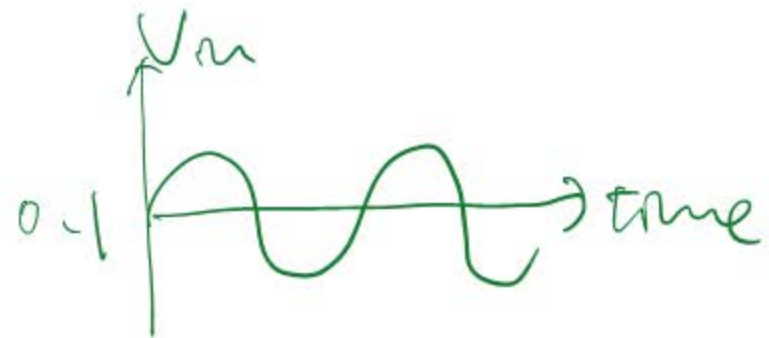
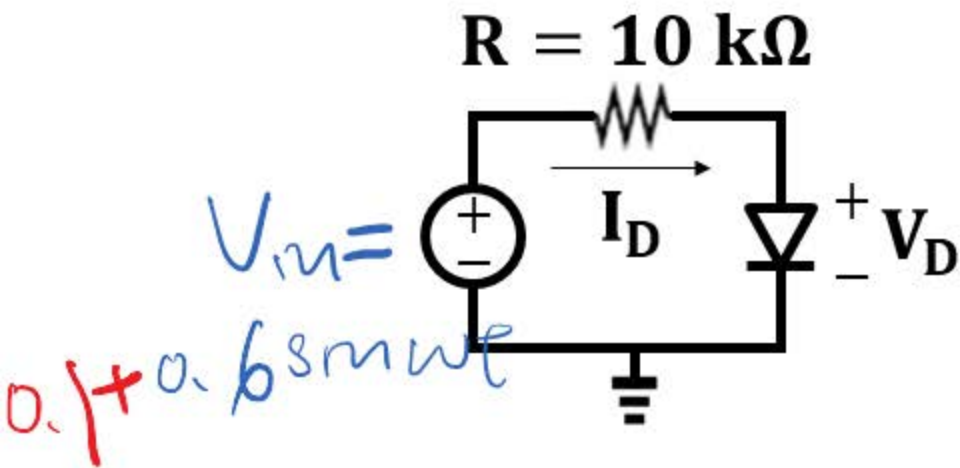
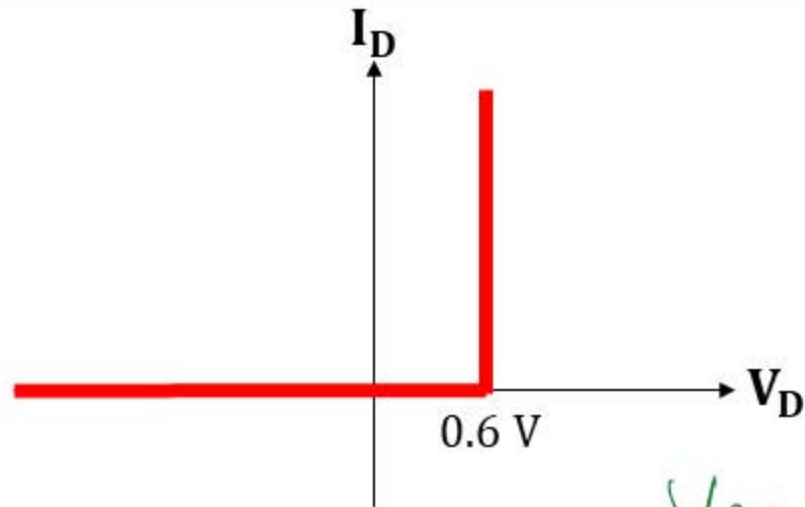
Simplified Analysis (Constant Voltage Drop)

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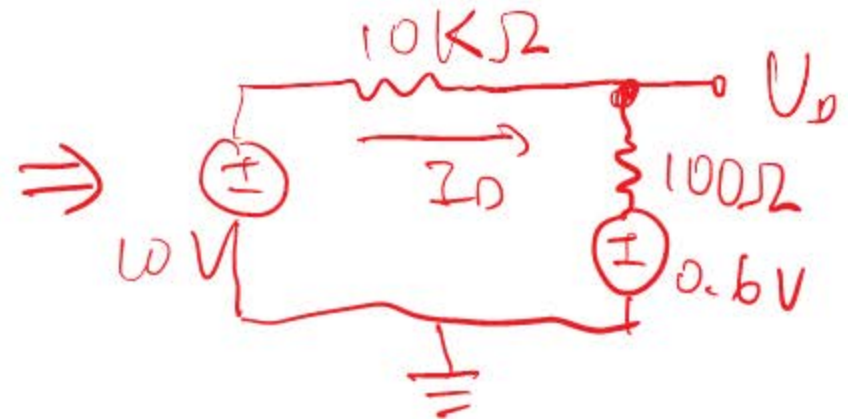
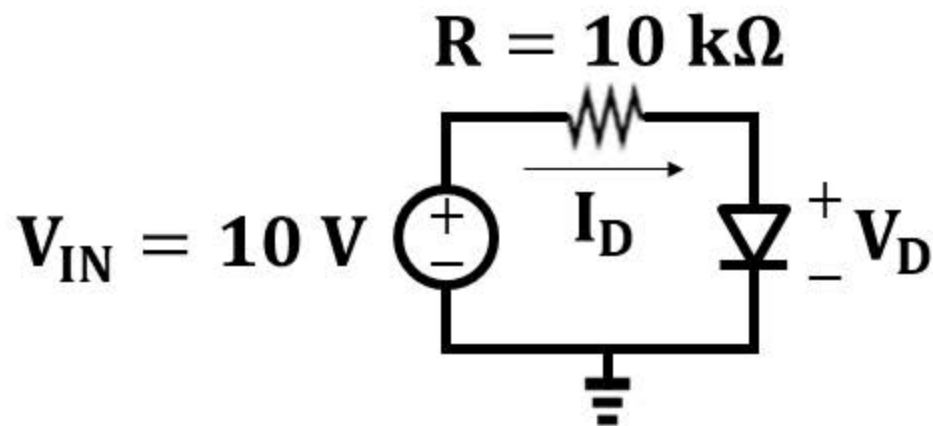
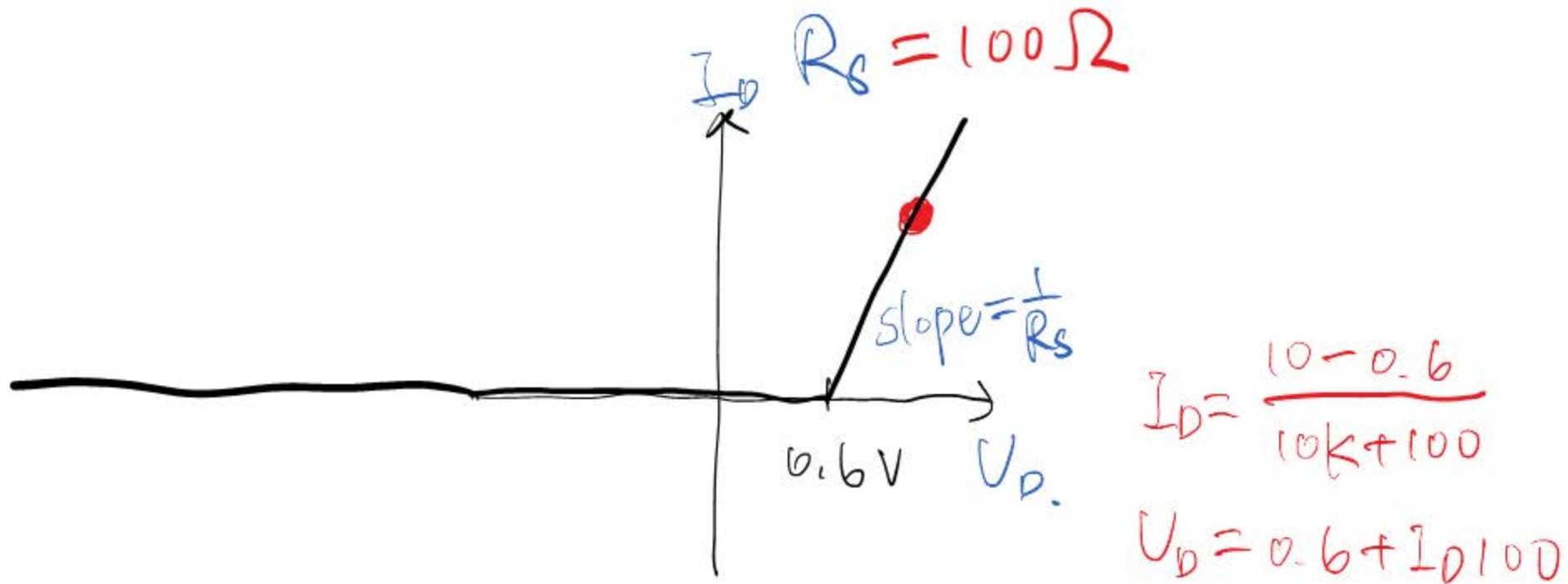
Simplified Analysis (Constant Voltage Drop)

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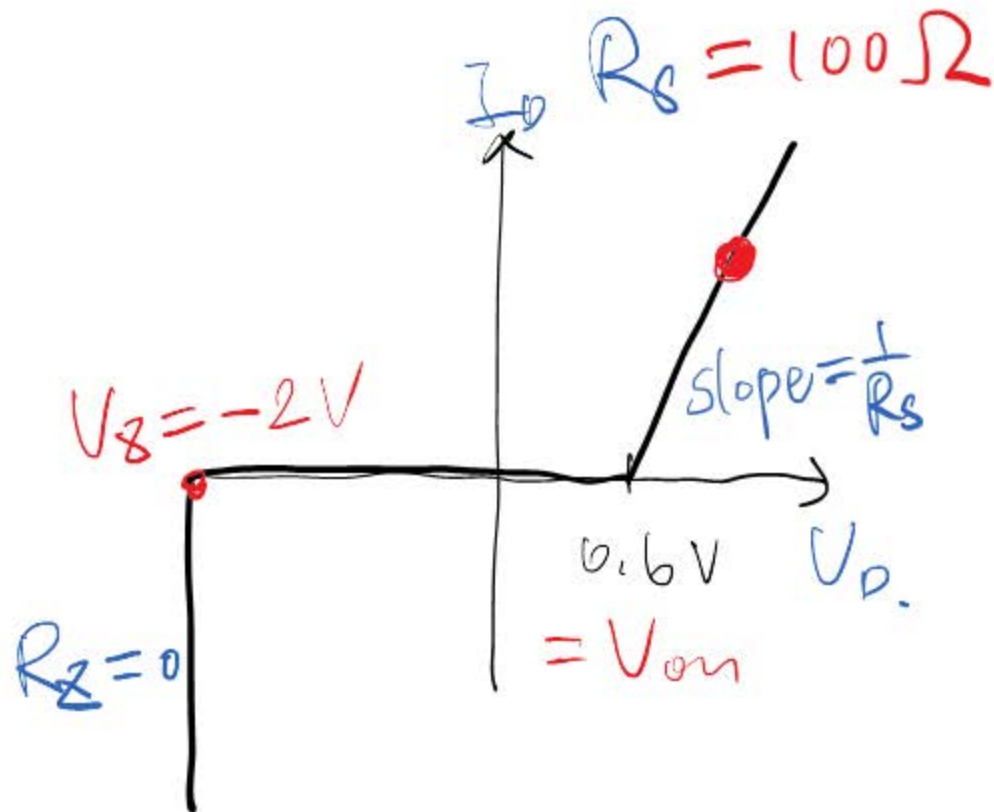
Simplified Analysis (Constant Voltage Drop)

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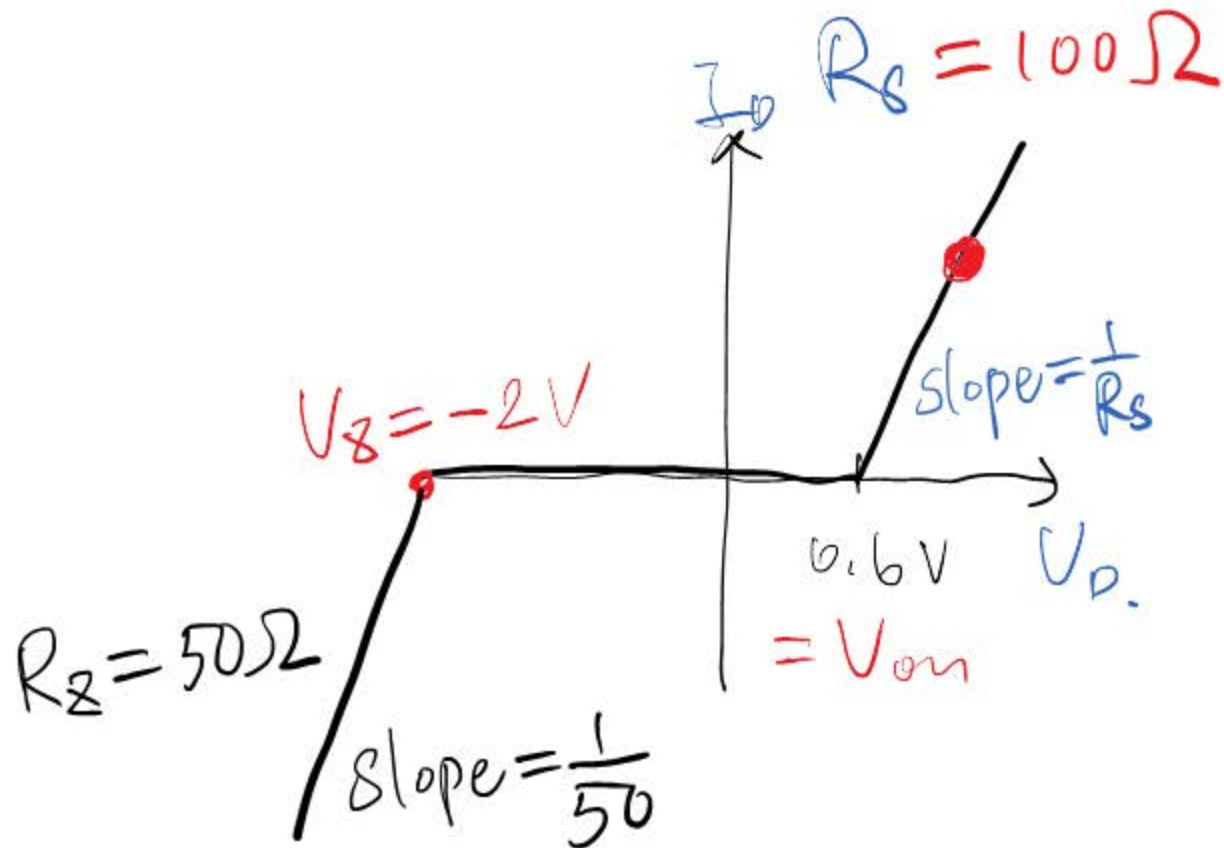
Simplified Analysis (Constant Voltage Drop)

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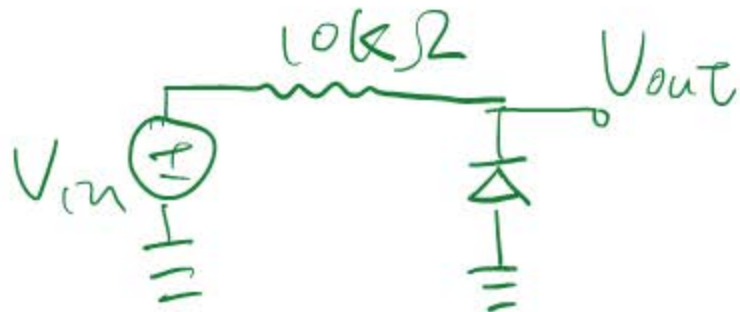
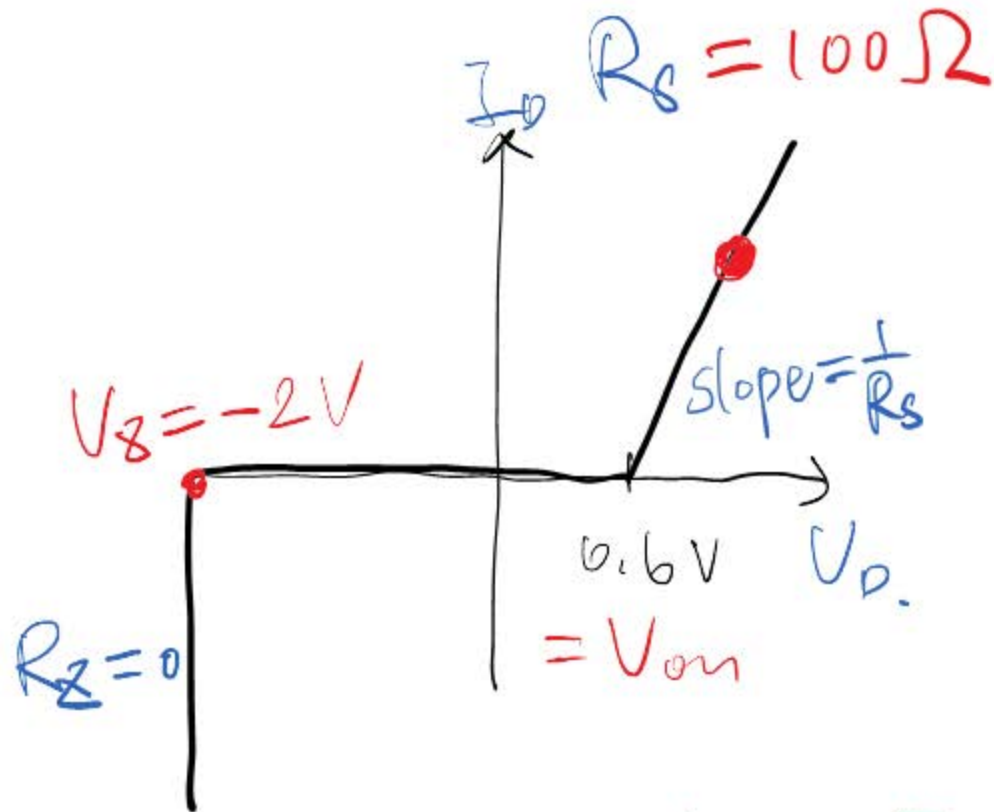


Simplified Analysis (Constant Voltage Drop)

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Simplified Analysis (Constant Voltage Drop)



$$V_m = 5 + 0.013 \sin(2\pi 60t)$$

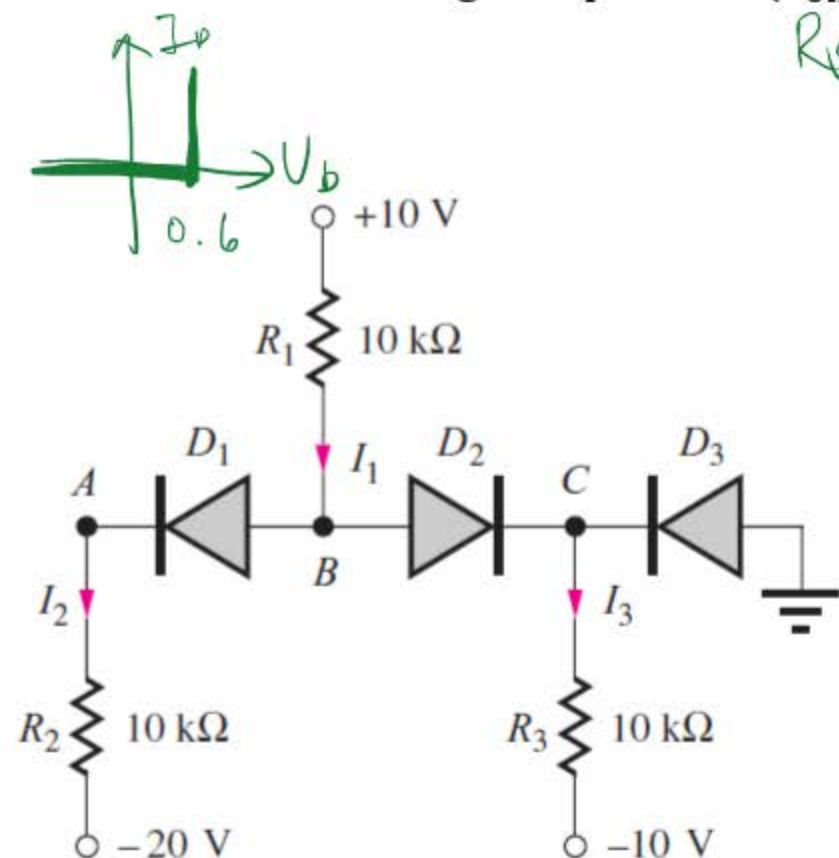
$$V_{out} = 2$$

Comparison

	V_D	I_D
Graphical Analysis	0.6 V	0.95 mA
Mathematical Analysis	0.5742 V	0.944 mA
Ideal Diode Model	0 V	1 mA
Constant Voltage Drop Model	0.6 V	0.94 mA

Example

Use constant voltage drop model ($V_{on} = 0.6 \text{ V}$) to calculate V_D and I_D of each diode.



- $R_S = 0$
- Assume no current flowing through D_3 *D1 on, D2 on, D3 off*

$$\frac{10 - V_B}{10k} = \frac{V_B - 0.6 + 20}{10k} + \frac{V_B - 0.6 + 10}{10k}$$

$$V_B = -6.27 \text{ V}$$

$$V_C = -6.87 \text{ V} \Rightarrow D_3 \text{ in forward bias}$$

Assumption NOT valid

- Assume no current flowing through D_2 *D1 on, D2 off, D3 on*

$$\frac{10 - V_B}{10k} = \frac{V_B - 0.6 + 20}{10k}$$

$$V_B = -4.7 \text{ V}$$

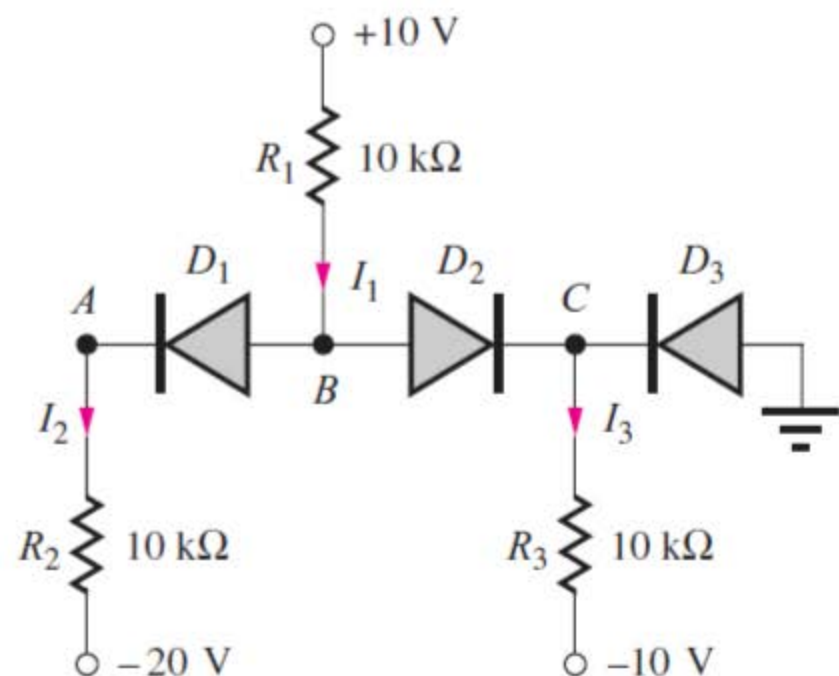
$$V_C = -0.6 \text{ V} \Rightarrow D_2 \text{ indeed in reverse bias}$$

Assumption valid

$V_{D1} = 0.6 \text{ V}$	$V_{D2} = -4.1 \text{ V}$	$V_{D3} = 0.6 \text{ V}$
$I_{D1} = 1.47 \text{ mA}$	$I_{D2} = 0 \text{ mA}$	$I_{D3} = 0.94 \text{ mA}$

Example

Use ideal diode model ($V_{on} = 0 \text{ V}$) to calculate V_D and I_D of each diode.



- Assume D_3 in reverse bias

$$\frac{10 - V_B}{10k} = \frac{V_B + 20}{10k} + \frac{V_B + 10}{10k}$$

$$V_B = -6.67 \text{ V}$$

$$V_C = -6.67 \text{ V} \Rightarrow D_3 \text{ in forward bias}$$

Assumption NOT valid

- Assume D_2 in reverse bias

$$\frac{10 - V_B}{10k} = \frac{V_B + 20}{10k}$$

$$V_B = -5 \text{ V}$$

$$V_C = 0 \text{ V} \Rightarrow D_2 \text{ indeed in reverse bias}$$

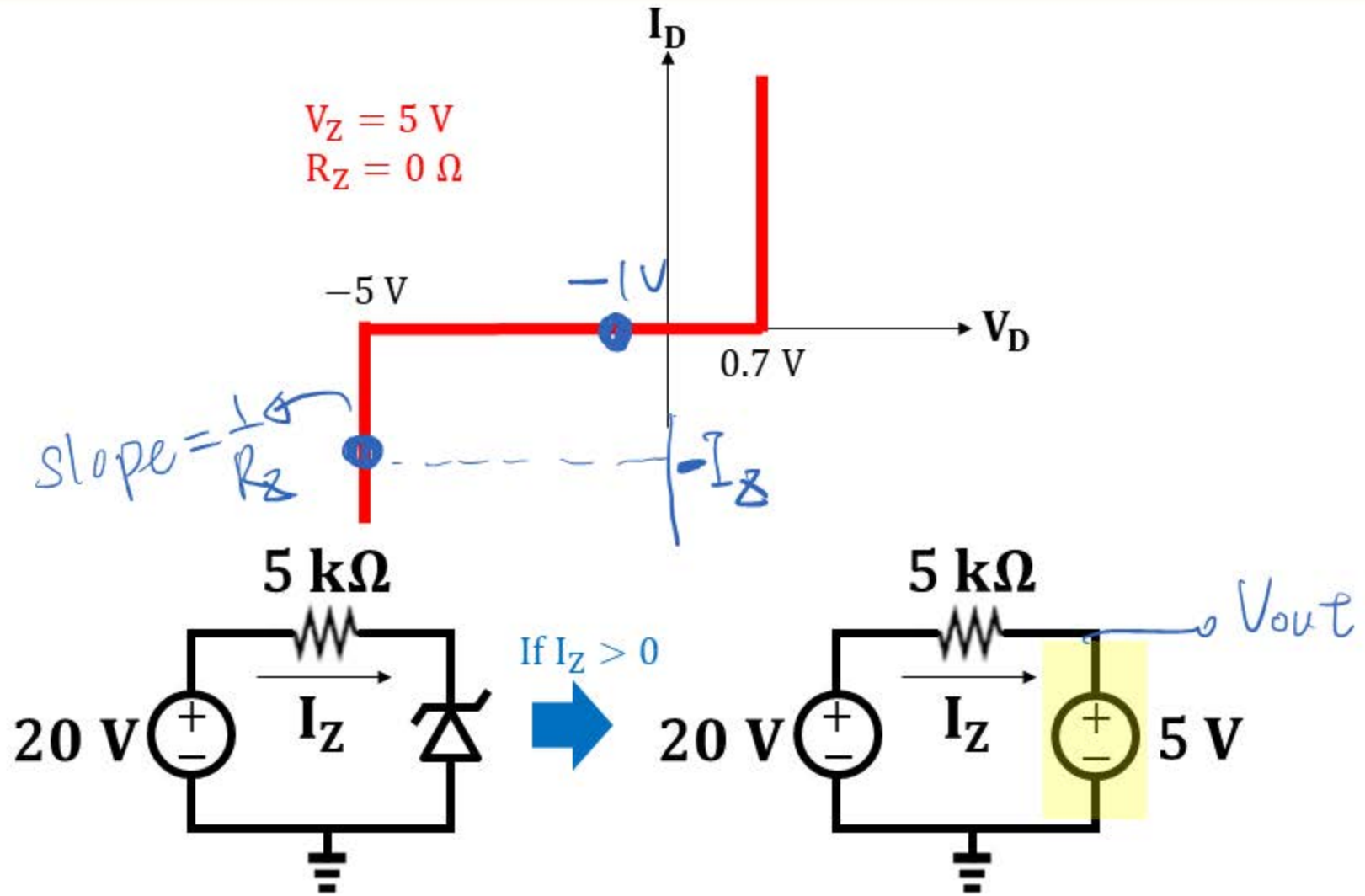
Assumption valid

$V_{D1} = 0 \text{ V}$	$V_{D2} = -5 \text{ V}$	$V_{D3} = 0 \text{ V}$
$I_{D1} = 1.5 \text{ mA}$	$I_{D2} = 0 \text{ mA}$	$I_{D3} = 1 \text{ mA}$

Zener Diode Circuit

Simplified Analysis for Zener Diode ($R_Z = 0$)

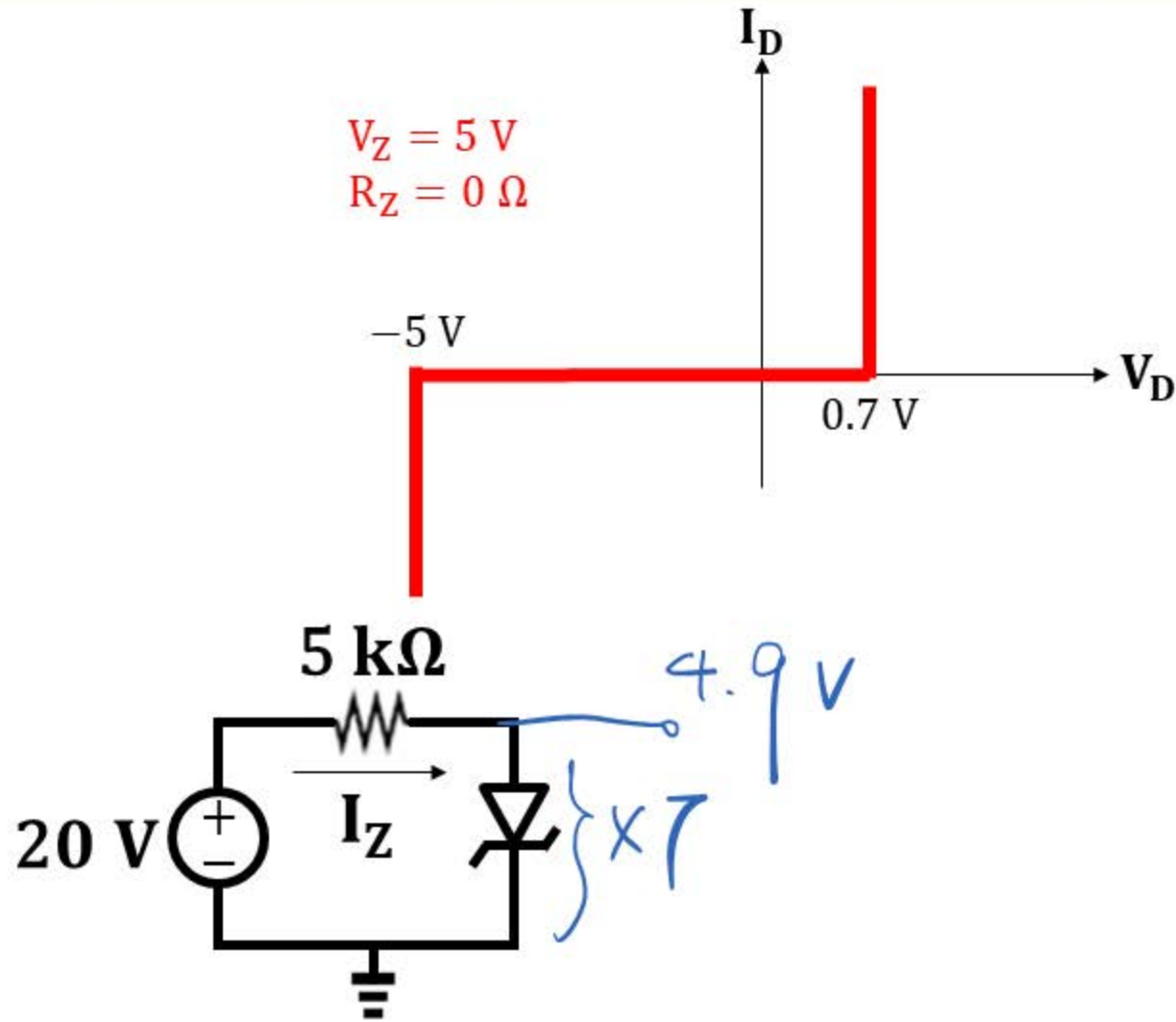
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What happens if $V_{IN} = 1\text{ V}$?

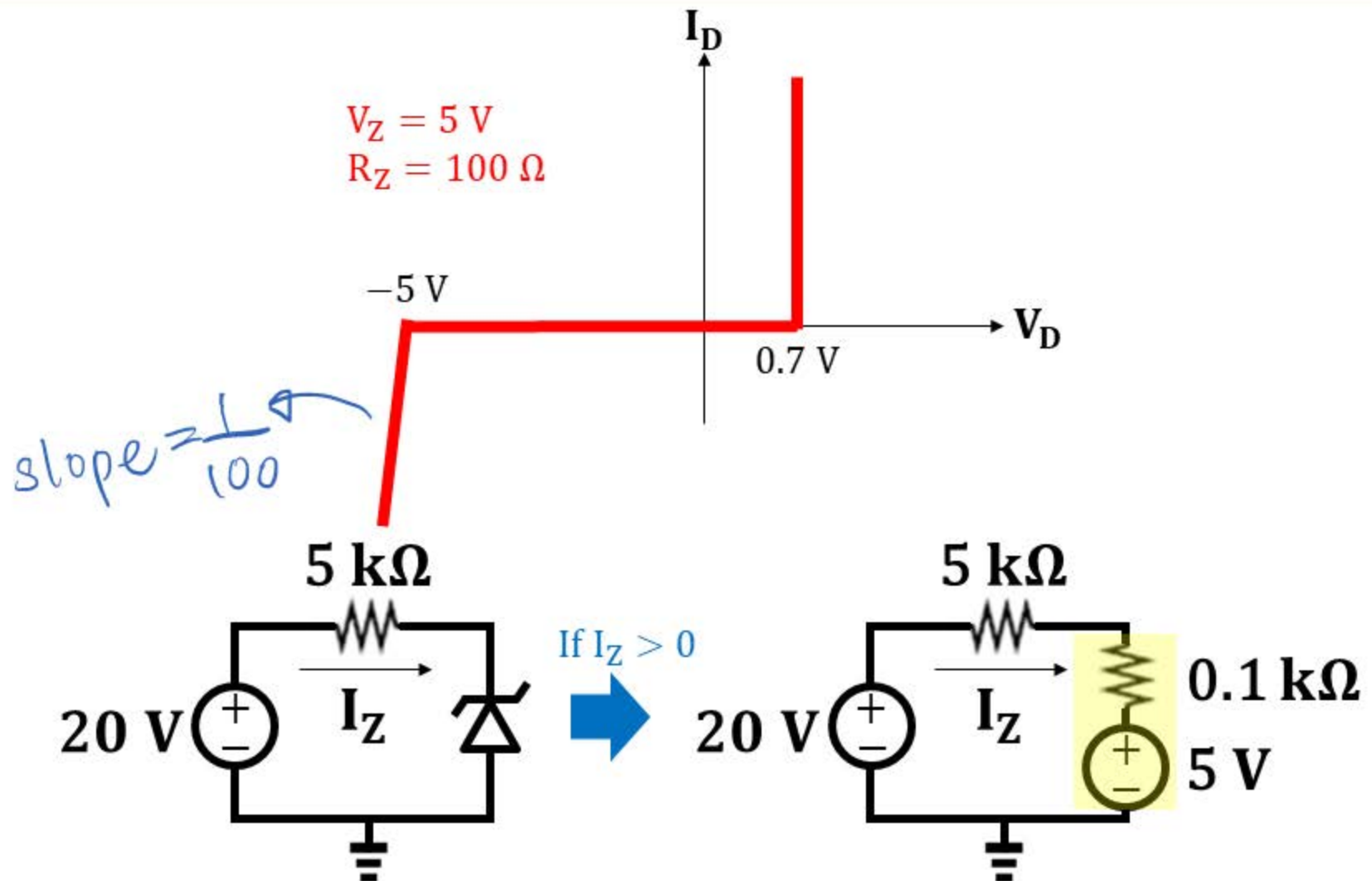
$$I_Z = \frac{20 - 5}{5000} = 3 \times 10^{-3}\text{ (A)}$$

Simplified Analysis for Zener Diode ($R_Z = 0$)



Simplified Analysis for Zener Diode ($R_Z \neq 0$)

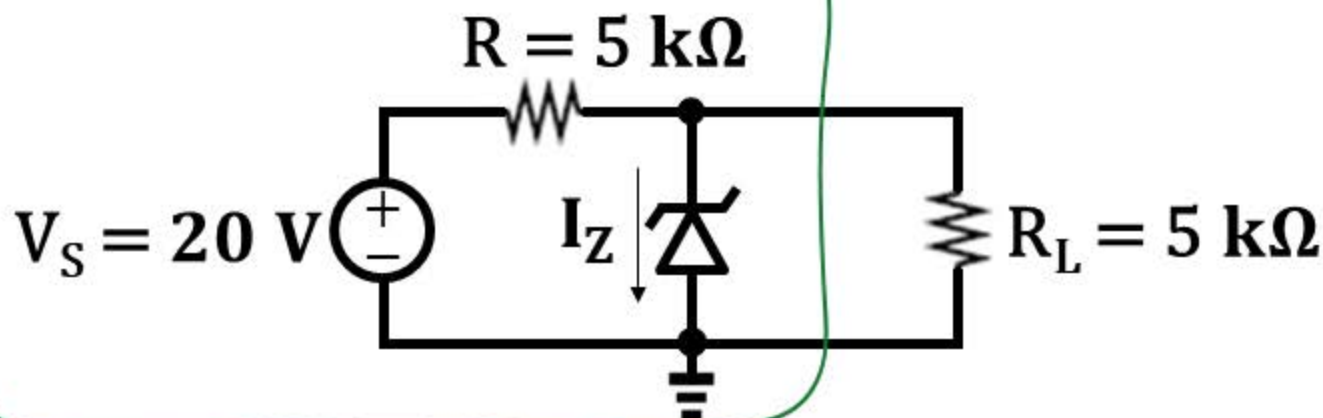
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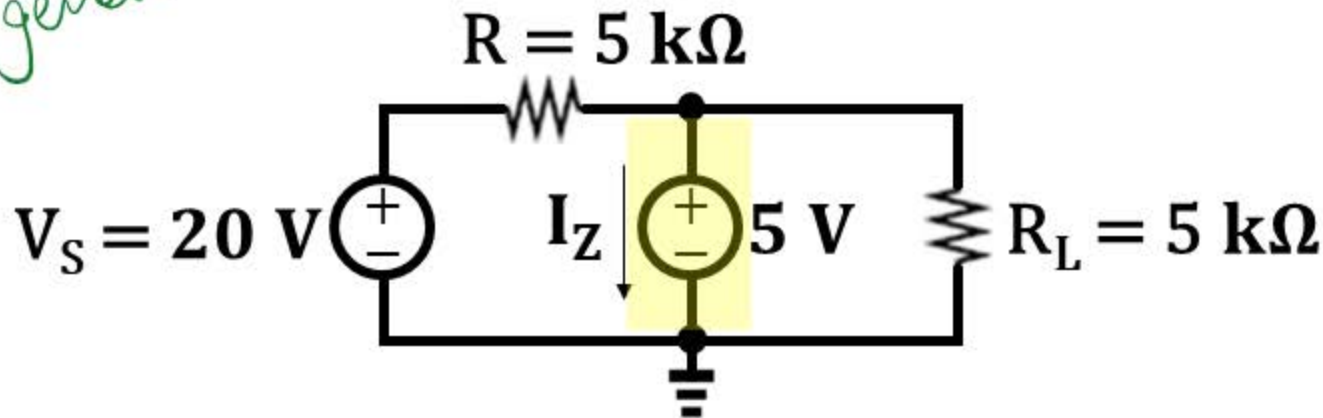
$$I_Z = \frac{20 - 5}{5100} = 2.94 \times 10^{-3} \text{ (A)}$$

Voltage Regulator Using Zener Diode ($R_Z = 0$) ²⁴

power supply
or DC 5V generator.



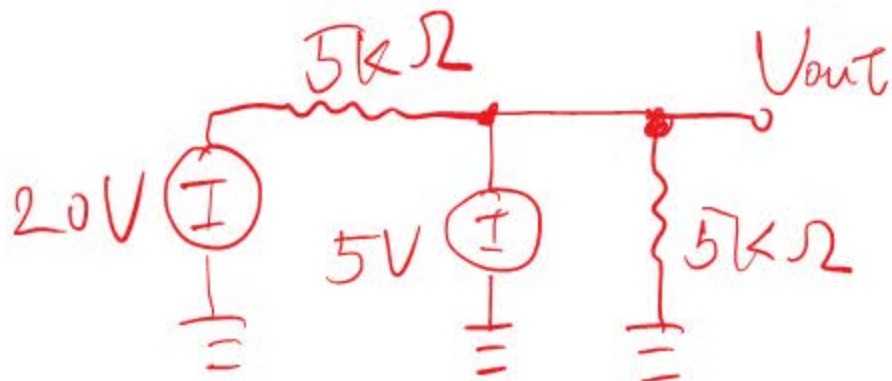
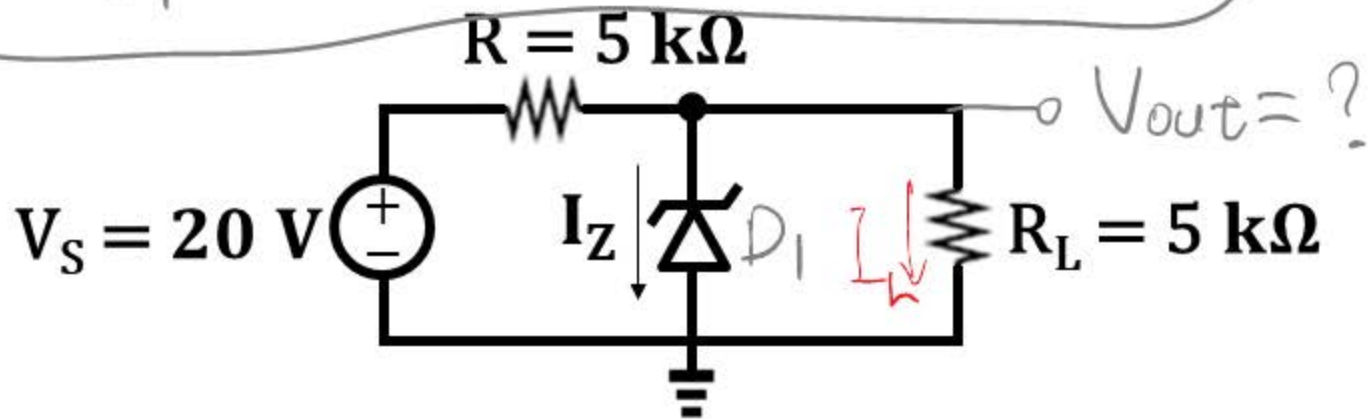
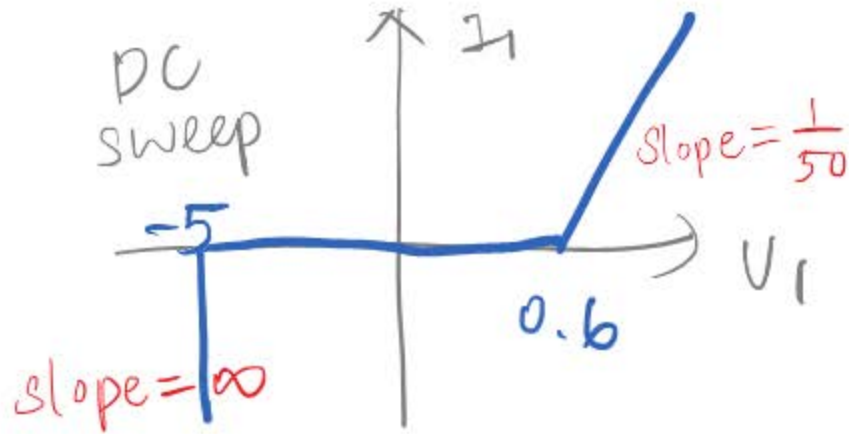
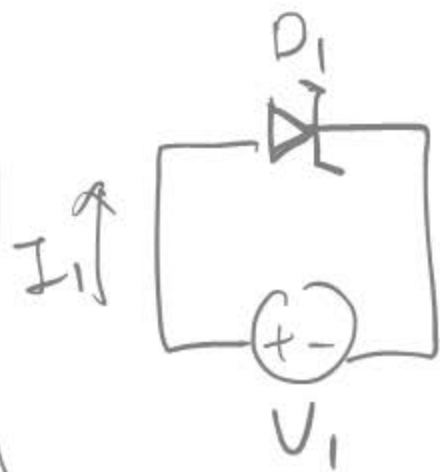
↓ If $I_Z > 0$



$$I_Z = \frac{20 - 5}{5\text{ k}} - \frac{5}{5\text{ k}} = 2\text{ mA} > 0$$

As long as the zener diode operates in reverse breakdown region ($I_Z > 0$), a constant voltage (5 V) appears across R_L .

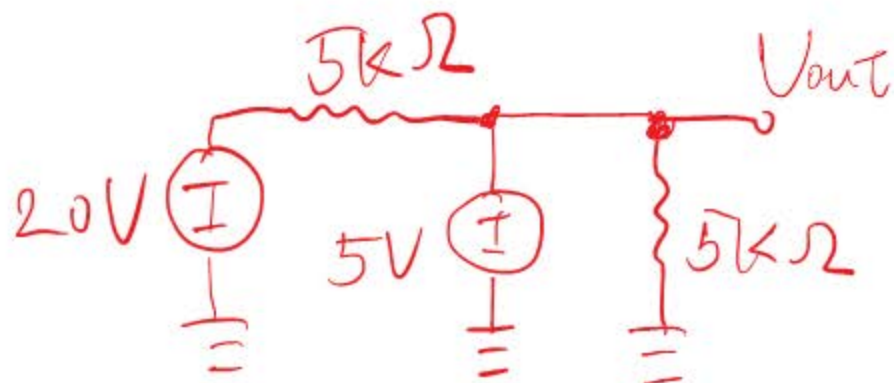
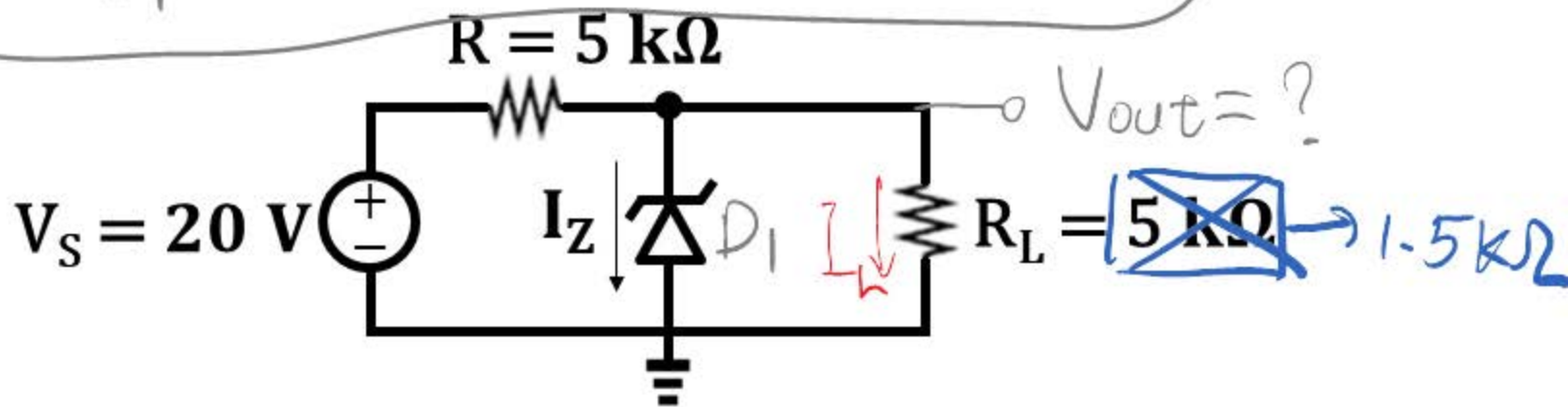
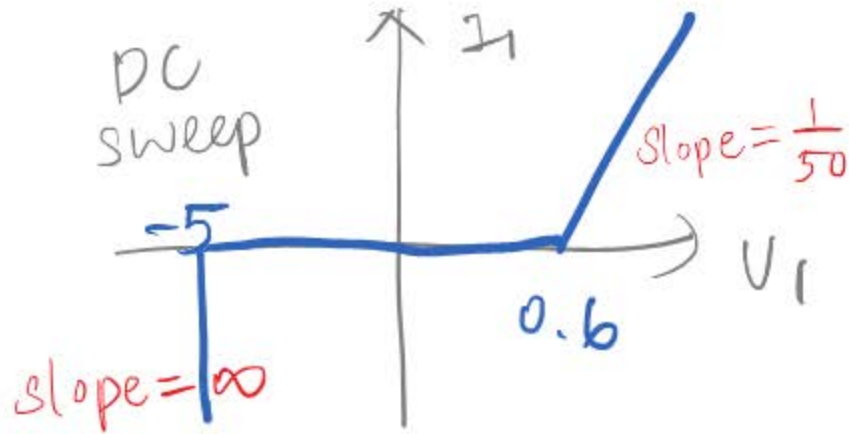
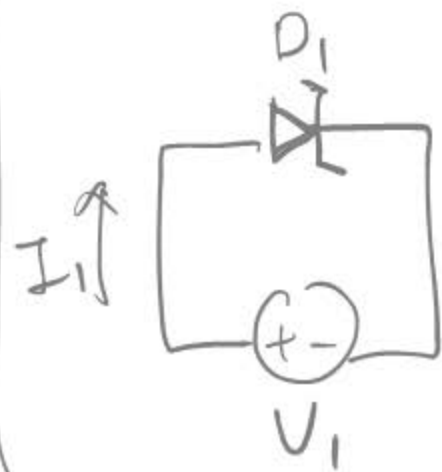
What is the smallest R_L for reverse breakdown to happen? Answer: $R_{L,\min} = 1.67\text{ k}\Omega$



$$V_{out} = 5 \text{ V}$$

$$\frac{20 - 5}{5 \text{ k}} - I_L = I_8$$

$$\frac{5}{5 \text{ k}} = I_L$$



$$20 \cdot \frac{1.5\text{k}}{1.5\text{k} + 5\text{k}} = V_{out}$$

$$\frac{5}{1.5\text{k}} = I_L > \frac{20-5}{5\text{k}}$$