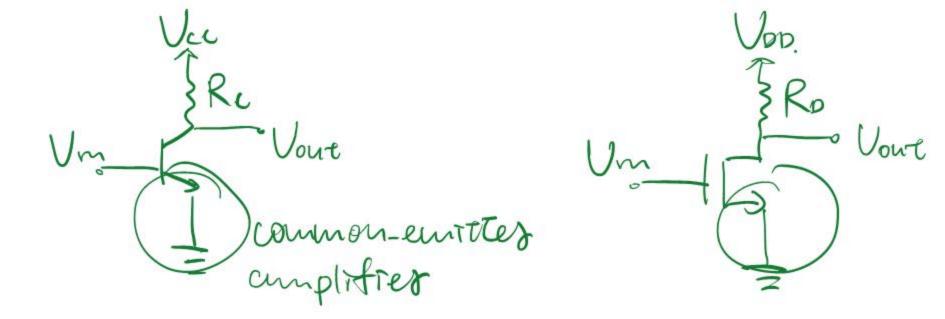
60T Although If Relarge high gam, input lange JOUT Although large.



## **FET Single Stage Amplifier**

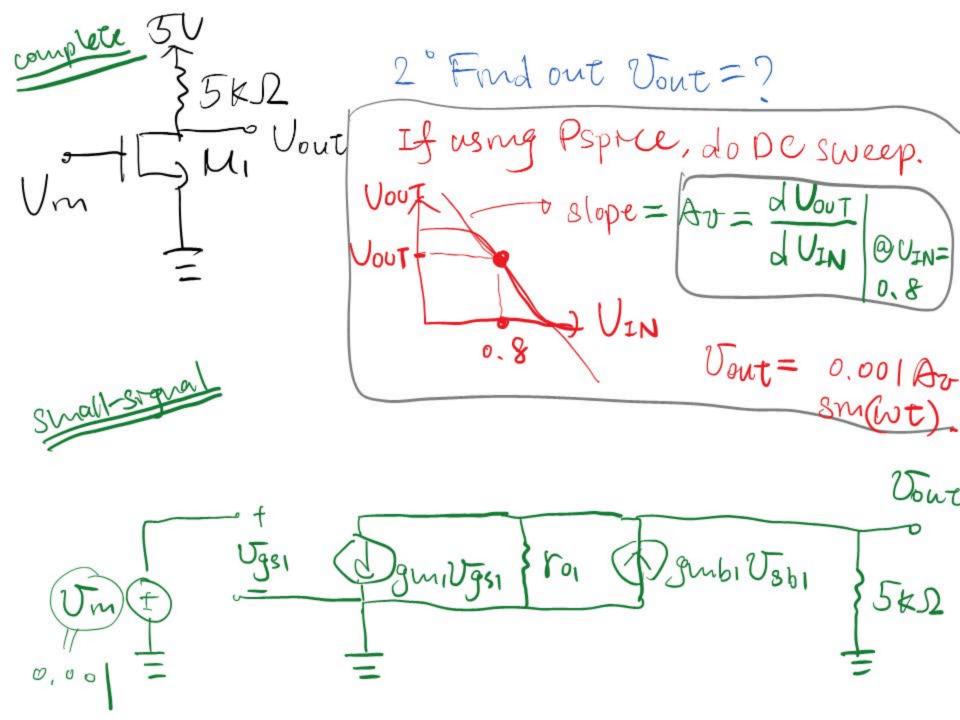
Ve311 Electronic Circuits (Fall 2020)

Dr. Chang-Ching Tu

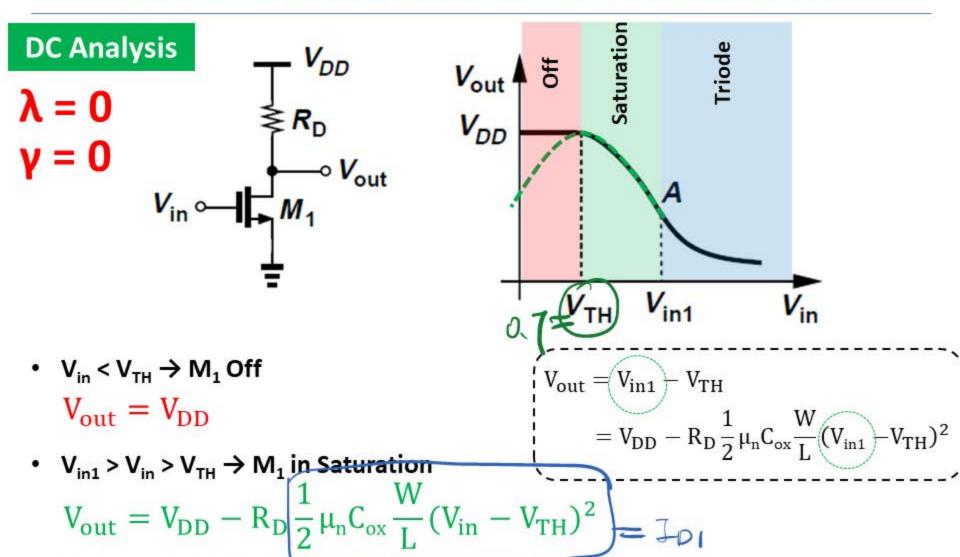


Common-Source with Resistive Load

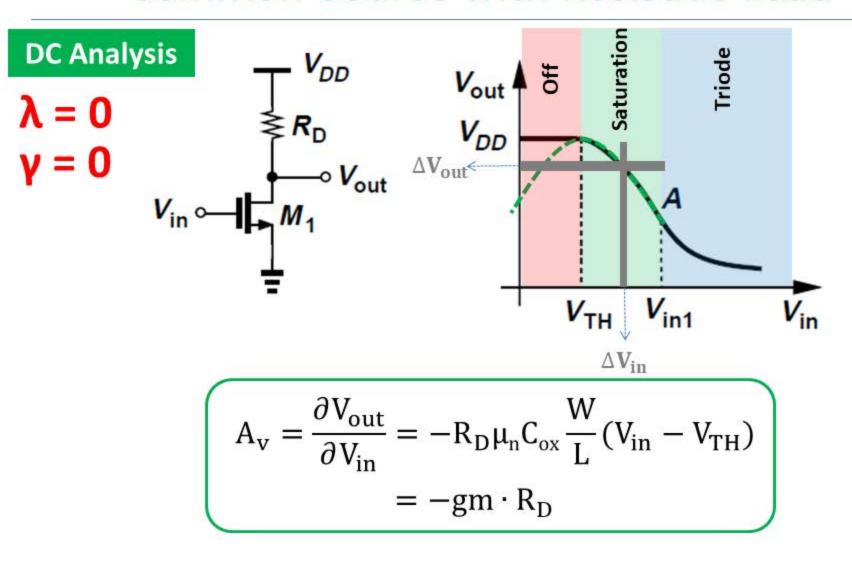
1° Fridont Vout =? then confirm whether Vos ≥ Vas- Uth VsB1 = 0 VOUT = VIN (TH) = 0.1 Mi has stre jum Mrawn VOUT = 5- (5K). IDI 入 キロ、ともの In= 2lm Cox (10mm) Vm=a8+a00|8m(2Twot) (0.8-0.7) (H7 VOUT) Vout = Vout + Vout = ?



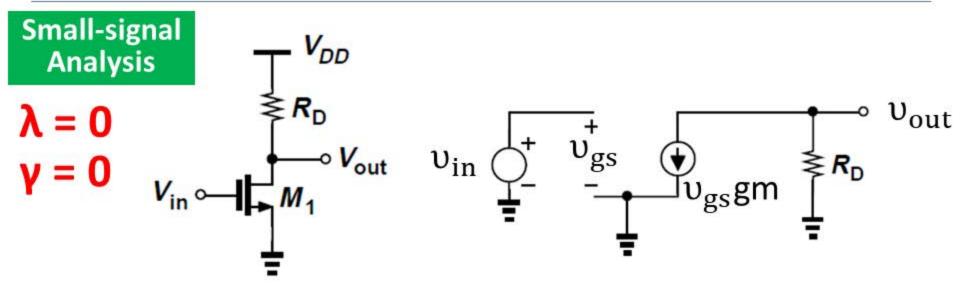
Small-stana gunita 0,00 Vour = - (m, Vm) (ho, 1/5k) Luncox (CO. 1) CHA VOUT) = -9mi(801/15K)



• 
$$V_{in} > V_{in1} \rightarrow M_1$$
 in Triode
$$V_{out} = V_{DD} - R_D \mu_n C_{ox} \frac{W}{L} [(V_{in} - V_{TH}) V_{out} - \frac{1}{2} V_{out}^2] = Lo_I$$

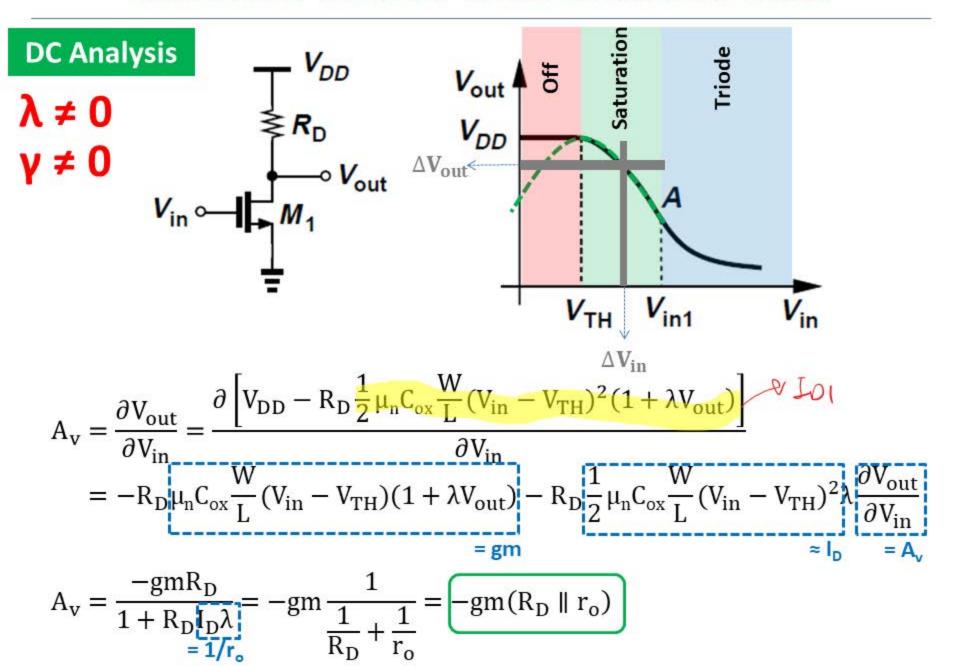


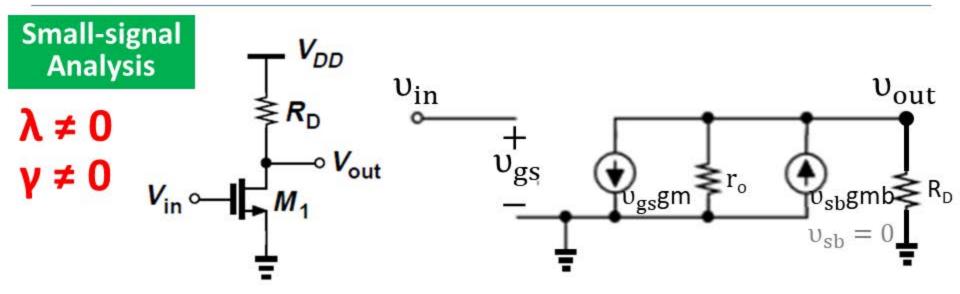
•  $V_{gs}$  increases by  $\Delta V_{in} \rightarrow I_{d}$  increases by  $\Delta V_{in} \cdot gm \rightarrow V_{out}$  decreases by  $\Delta V_{in} \cdot (gm \cdot R_D)$ 



$$A_{\rm v} = \frac{\upsilon_{\rm out}}{\upsilon_{\rm in}} = -gm \cdot R_{\rm D}$$

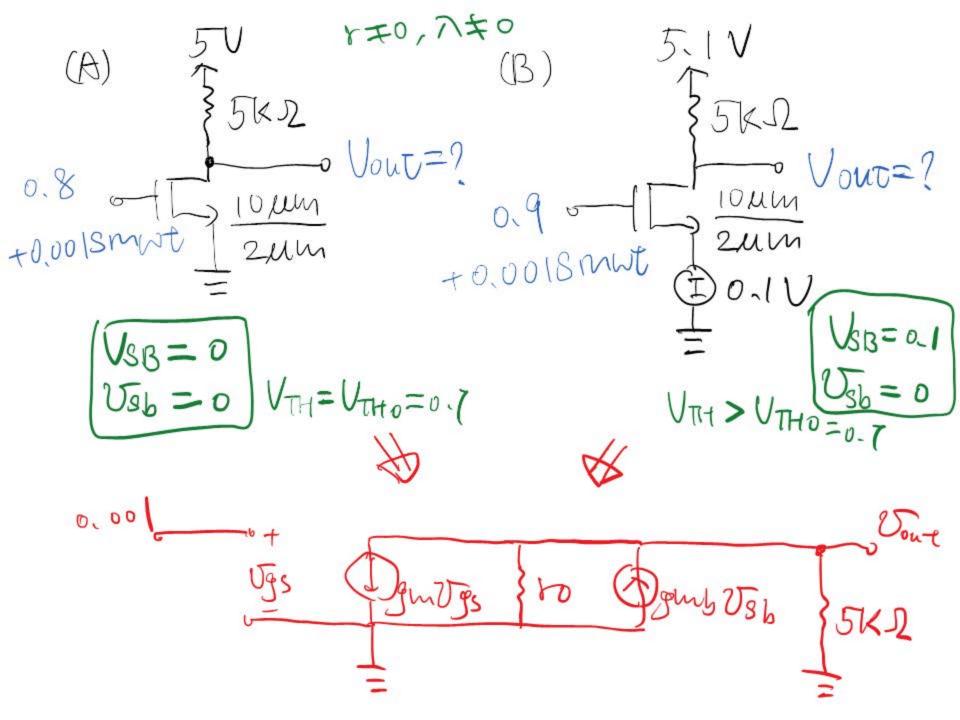
Small-signal analysis leads to the same result as DC analysis.

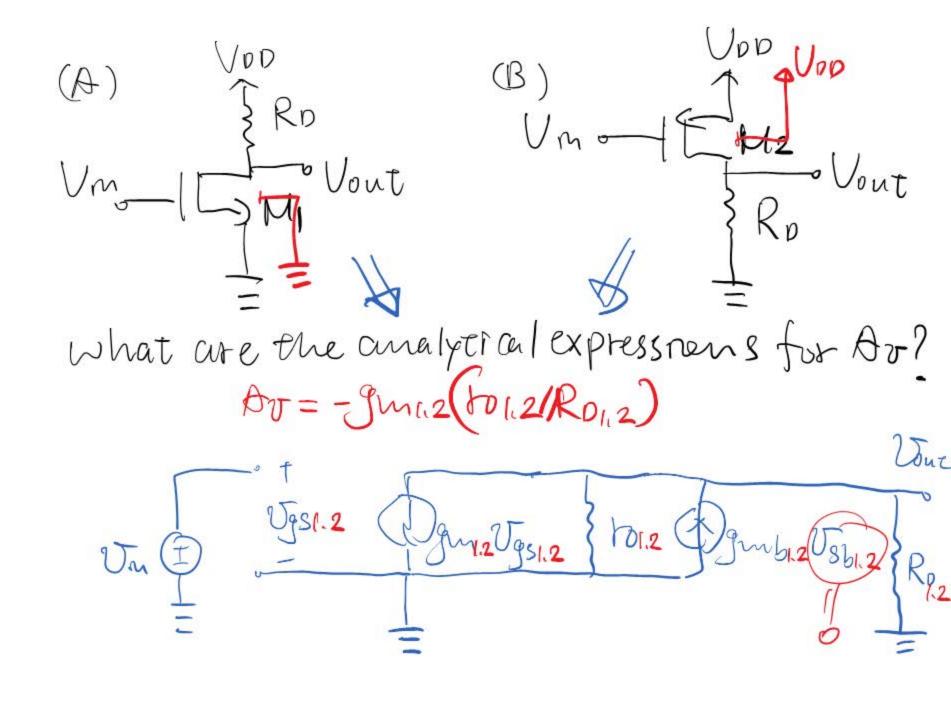




$$A_{v} = \frac{v_{out}}{v_{in}} = -gm \cdot (R_{D} \parallel r_{o})$$

- Small-signal analysis leads to the same result as DC analysis.
- gm is a function of  $V_{GS}$  and  $V_{DS}$ , while  $r_o$  is a function of  $I_D$ .  $\rightarrow$  **Nonlinearity**

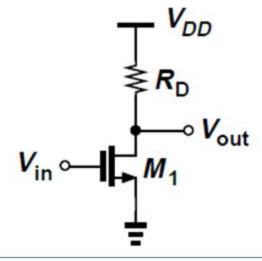


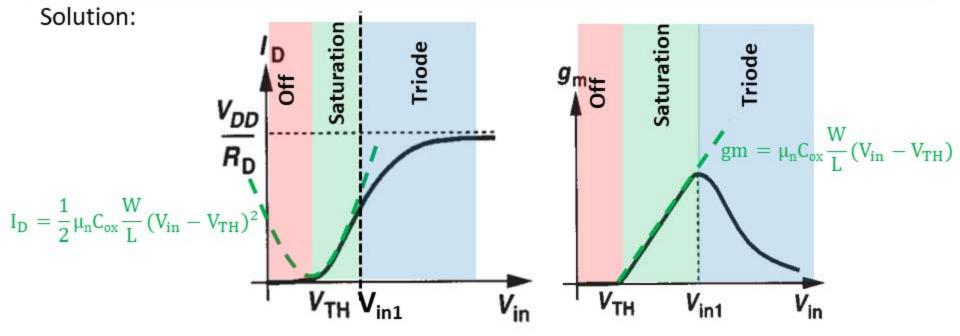


# **Example**

Sketch the drain current and transconductance of M<sub>1</sub> as a function of input

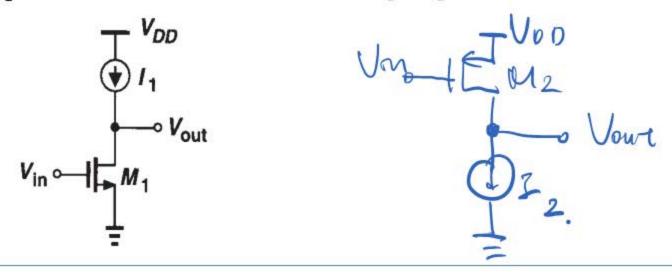
voltage. Assume  $\lambda = \gamma = 0$ .





# Example

Assuming M₁ in saturation, calculate its small-signal gain.



#### Solution:

· Small-signal Analysis:

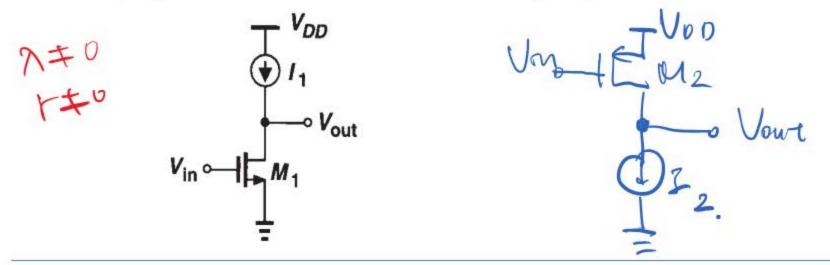
$$A_{v} = \frac{v_{out}}{v_{in}} = -gm_{1}r_{o1}$$

DC Analysis:

$$I_1 = \frac{1}{2} \mu_n C_{ox} \frac{W}{L} (V_{in} - V_{TH})^2 (1 + \lambda V_{DS})$$

# Example

Assuming M<sub>1</sub> in saturation, calculate its small-signal gain.



Solution: