

1. Description

1.1. Project

Project Name	RAILWAY_GATE_FINAL
Board Name	NUCLEO-F446RE
Generated with:	STM32CubeMX 6.12.0
Date	08/29/2025

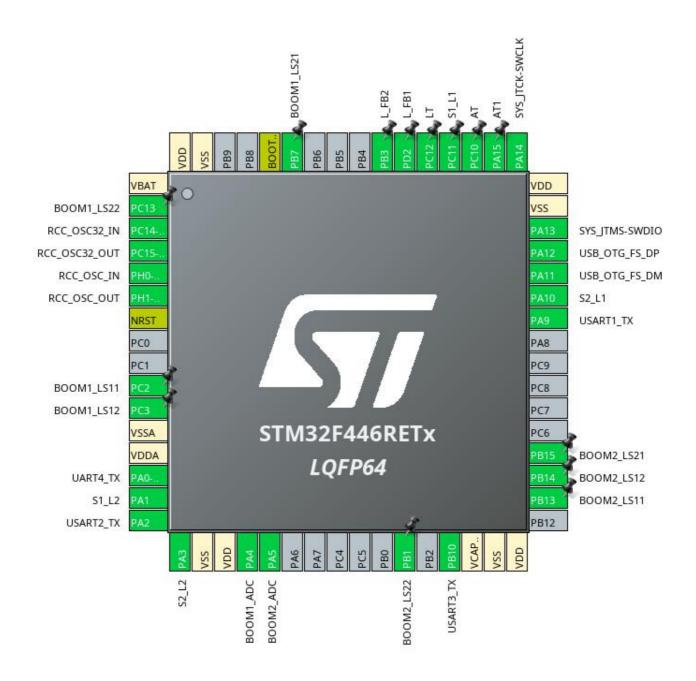
1.2. MCU

MCU Series	STM32F4
MCU Line	STM32F446
MCU name	STM32F446RETx
MCU Package	LQFP64
MCU Pin number	64

1.3. Core(s) information

Core(s)	Arm Cortex-M4	

2. Pinout Configuration



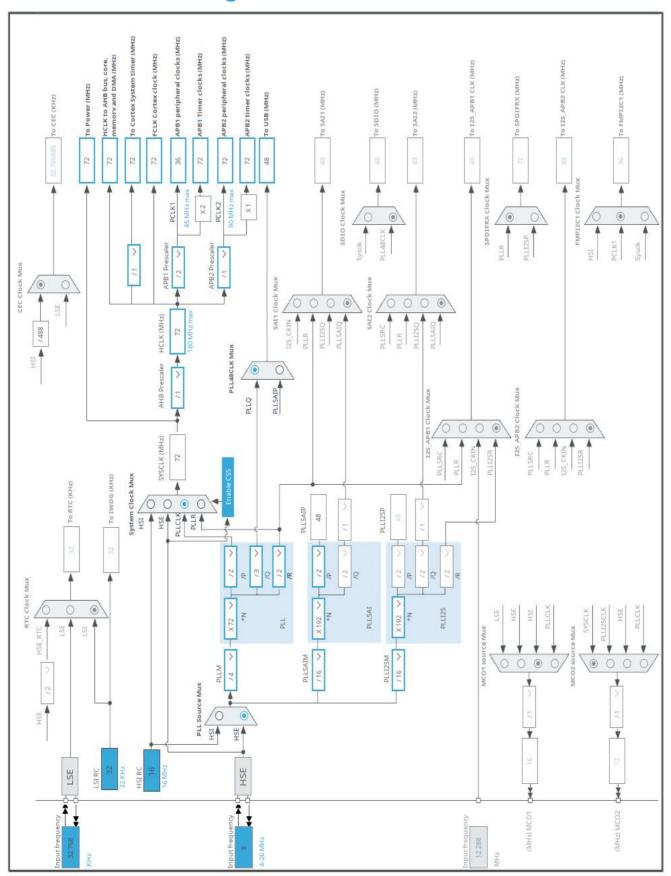
3. Pins Configuration

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Pin Number	Pin Name	Pin Type	Alternate	Label
LQFP64	(function after		Function(s)	
	reset)			
1	VBAT	Power		
2	PC13 *	I/O	GPIO_Input	BOOM1_LS22
3	PC14-OSC32_IN	I/O	RCC_OSC32_IN	
4	PC15-OSC32_OUT	I/O	RCC_OSC32_OUT	
5	PH0-OSC_IN	I/O	RCC_OSC_IN	
6	PH1-OSC_OUT	I/O	RCC_OSC_OUT	
7	NRST	Reset		
10	PC2 *	I/O	GPIO_Input	BOOM1_LS11
11	PC3 *	I/O	GPIO_Input	BOOM1_LS12
12	VSSA	Power		
13	VDDA	Power		
14	PA0-WKUP	I/O	UART4_TX	
15	PA1	I/O	UART4_RX	S1_L2
16	PA2	I/O	USART2_TX	
17	PA3	I/O	USART2_RX	S2_L2
18	VSS	Power		
19	VDD	Power		
20	PA4	I/O	ADC1_IN4	BOOM1_ADC
21	PA5	I/O	ADC1_IN5	BOOM2_ADC
27	PB1 *	I/O	GPIO_Input	BOOM2_LS22
29	PB10	I/O	USART3_TX	
30	VCAP_1	Power		
31	VSS	Power		
32	VDD	Power		
34	PB13 *	I/O	GPIO_Input	BOOM2_LS11
35	PB14 *	I/O	GPIO_Input	BOOM2_LS12
36	PB15 *	I/O	GPIO_Input	BOOM2_LS21
42	PA9	I/O	USART1_TX	
43	PA10	I/O	USART1_RX	S2_L1
44	PA11	I/O	USB_OTG_FS_DM	
45	PA12	I/O	USB_OTG_FS_DP	
46	PA13	I/O	SYS_JTMS-SWDIO	
47	VSS	Power		
48	VDD	Power		
49	PA14	I/O	SYS_JTCK-SWCLK	
50	PA15 *	I/O	GPIO_Output	AT1

Pin Number LQFP64	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
51	PC10 *	I/O	GPIO_Output	AT
52	PC11	I/O	USART3_RX	S1_L1
53	PC12 *	I/O	GPIO_Output	LT
54	PD2 *	I/O	GPIO_Input	L_FB1
55	PB3 *	I/O	GPIO_Input	L_FB2
59	PB7 *	I/O	GPIO_Input	BOOM1_LS21
60	воото	Boot		
63	VSS	Power		
64	VDD	Power		

^{*} The pin is affected with an I/O function

4. Clock Tree Configuration



1. Power Consumption Calculator report

1.1. Microcontroller Selection

Series	STM32F4
Line	STM32F446
MCU	STM32F446RETx
Datasheet	DS10693_Rev6

1.2. Parameter Selection

Temperature	25
Vdd	3.3

1.3. Battery Selection

Battery	Li-SOCL2(A3400)
Capacity	3400.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	100.0 mA
Max Pulse Current	200.0 mA
Cells in series	1
Cells in parallel	1

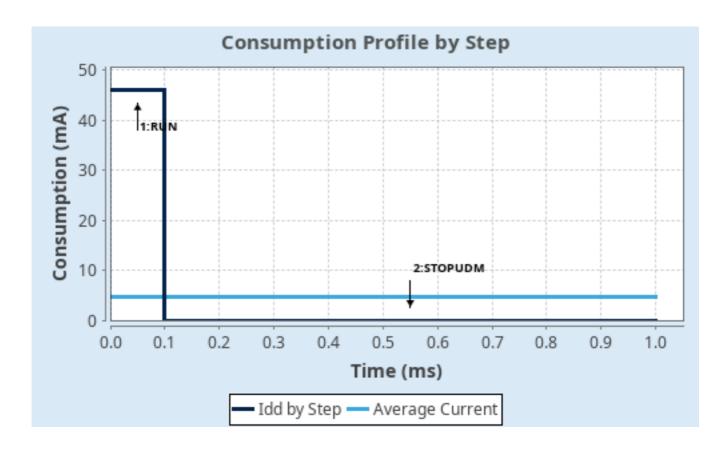
1.4. Sequence

Step	Step1	Step2
Mode	RUN	STOP UDM (Under Drive)
Vdd	3.3	3.3
Voltage Source	Battery	Battery
Range	Scale1-High	No Scale
Fetch Type	RAM/FLASH/REGON/ART/P REFETCH	n/a
CPU Frequency	180 MHz	0 Hz
Clock Configuration	HSE PLL	Regulator LP Flash-PwrDwn
Clock Source Frequency	4 MHz	0 Hz
Peripherals		
Additional Cons.	0 mA	0 mA
Average Current	46 mA	55 μA
Duration	0.1 ms	0.9 ms
DMIPS	225.0	0.0
Ta Max	98.02	104.99
Category	In DS Table	In DS Table

1.5. Results

Sequence Time	1 ms	Average Current	4.65 mA
Battery Life	1 month	Average DMIPS	225.0 DMIPS

1.6. Chart



2. Software Project

2.1. Project Settings

Name	Value
Project Name	RAILWAY_GATE_FINAL
Project Folder	/home/kailas/Desktop/LC_GATE/RAILWAY_GATE_FINAL
Toolchain / IDE	STM32CubeIDE
Firmware Package Name and Version	STM32Cube FW_F4 V1.28.2
Application Structure	Advanced
Generate Under Root	Yes
Do not generate the main()	No
Minimum Heap Size	0x200
Minimum Stack Size	0x400

2.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power	No
consumption)	
Enable Full Assert	No

2.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name		
1	SystemClock_Config	RCC		
2	MX_GPIO_Init	GPIO		
3	MX_TIM1_Init	TIM1		
4	MX_USART1_UART_Init USART1			
5	MX_USART2_UART_Init	USART2		
6	MX_USART3_UART_Init USART3			
7	MX_USB_DEVICE_Init USB_DEVICE			
8	MX_UART4_Init UART4			
9	MX_ADC1_Init	ADC1		

DAILWAY CATE FINAL Project
RAILWAY_GATE_FINAL Project Configuration Report
Configuration Report

3. Peripherals and Middlewares Configuration

3.1. ADC1 mode: IN4 mode: IN5

3.1.1. Parameter Settings:

ADCs_Common_Settings:

Mode Independent mode

ADC_Settings:

Clock Prescaler PCLK2 divided by 2

Resolution 12 bits (15 ADC Clock cycles)

Data Alignment

Scan Conversion Mode

Continuous Conversion Mode

Disabled

Discontinuous Conversion Mode

Disabled

DMA Continuous Requests

Disabled

End Of Conversion Selection EOC flag at the end of single channel conversion

ADC_Regular_ConversionMode:

Number Of Conversion 1

External Trigger Conversion Source Regular Conversion launched by software

External Trigger Conversion Edge None
Rank 1

Channel 4
Sampling Time 3 Cycles

ADC_Injected_ConversionMode:

Number Of Conversions 0

WatchDog:

Enable Analog WatchDog Mode false

3.2. RCC

High Speed Clock (HSE): Crystal/Ceramic Resonator Low Speed Clock (LSE): Crystal/Ceramic Resonator

3.2.1. Parameter Settings:

System Parameters:

VDD voltage (V) 3.3
Instruction Cache Enabled
Prefetch Buffer Enabled

Data Cache Enabled

Flash Latency(WS) 2 WS (3 CPU cycle)

RCC Parameters:

HSI Calibration Value 16

TIM Prescaler Selection Disabled
HSE Startup Timout Value (ms) 100
LSE Startup Timout Value (ms) 5000

Power Parameters:

Power Regulator Voltage Scale Power Regulator Voltage Scale 3

Power Over Drive Disabled

3.3. SYS

Debug: Serial Wire

Timebase Source: SysTick

3.4. TIM1

Trigger Source: ITR0

Clock Source: Internal Clock

3.4.1. Parameter Settings:

Counter Settings:

Prescaler (PSC - 16 bits value) 16467 *

Counter Mode Up
Counter Period (AutoReload Register - 16 bits value) 65535
Internal Clock Division (CKD) No Division

Repetition Counter (RCR - 8 bits value) 0
auto-reload preload Disable

Slave Mode Controller Slave mode disable

Trigger Output (TRGO) Parameters:

Master/Slave Mode (MSM bit) Disable (Trigger input effect not delayed)

Trigger Event Selection Reset (UG bit from TIMx_EGR)

3.5. UART4

Mode: Asynchronous

3.5.1. Parameter Settings:

Basic Parameters:

Baud Rate 115200

Word Length 8 Bits (including Parity)

Parity None Stop Bits 1

Advanced Parameters:

Data Direction Receive and Transmit

Over Sampling 16 Samples

3.6. USART1

Mode: Asynchronous

3.6.1. Parameter Settings:

Basic Parameters:

Baud Rate 115200

Word Length 8 Bits (including Parity)

Parity None Stop Bits 1

Advanced Parameters:

Data Direction Receive and Transmit

Over Sampling 16 Samples

3.7. **USART2**

Mode: Asynchronous

3.7.1. Parameter Settings:

Basic Parameters:

Baud Rate 115200

Word Length 8 Bits (including Parity)

Parity None Stop Bits 1

Advanced Parameters:

Data Direction Receive and Transmit

Over Sampling 16 Samples

3.8. **USART3**

Mode: Asynchronous

3.8.1. Parameter Settings:

Basic Parameters:

Baud Rate 115200

Word Length 8 Bits (including Parity)

Parity None Stop Bits 1

Advanced Parameters:

Data Direction Receive and Transmit

Over Sampling 16 Samples

3.9. USB_OTG_FS

Mode: Device_Only

3.9.1. Parameter Settings:

Speed Device Full Speed 12MBit/s

Low powerDisabledLink Power ManagementDisabledVBUS sensingDisabledSignal start of frameDisabled

3.10. USB_DEVICE

Class For FS IP: Communication Device Class (Virtual Port Com)

3.10.1. Parameter Settings:

Basic Parameters:

USBD_MAX_NUM_INTERFACES (Maximum number of supported interfaces) 1
USBD_MAX_NUM_CONFIGURATION (Maximum number of supported configuration) 1

USBD_MAX_STR_DESC_SIZ (Maximum size for the string descriptors) 512

USBD_SELF_POWERED (Enabled self power) Enabled

USBD_DEBUG_LEVEL (USBD Debug Level) 0: No debug message

USBD_LPM_ENABLED (Link Power Management) 1: Link Power Management supported

Class Parameters:

USB CDC Rx Buffer Size 2048

USB CDC Tx Buffer Size 2048

3.10.2. Device Descriptor:

Device Descriptor:

VID (Vendor IDentifier) 1155

LANGID_STRING (Language Identifier) English (United States)

MANUFACTURER_STRING (Manufacturer Identifier) STMicroelectronics

Device Descriptor FS:

PID (Product IDentifier) 22336

PRODUCT_STRING (Product Identifier) STM32 Virtual ComPort

CONFIGURATION_STRING (Configuration Identifier)

INTERFACE_STRING (Interface Identifier)

CDC Interface

CDC Interface

^{*} User modified value

4. System Configuration

4.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
ADC1	PA4	ADC1_IN4	Analog mode	No pull-up and no pull-down	n/a	BOOM1_ADC
	PA5	ADC1_IN5	Analog mode	No pull-up and no pull-down	n/a	BOOM2_ADC
RCC	PC14- OSC32_IN	RCC_OSC32_IN		n/a	n/a	_
	PC15- OSC32_OU T	RCC_OSC32_O UT	n/a	n/a	n/a	
	PH0- OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
	PH1- OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
SYS	PA13	SYS_JTMS- SWDIO	n/a	n/a	n/a	
	PA14	SYS_JTCK- SWCLK	n/a	n/a	n/a	
UART4	PA0-WKUP	UART4_TX	Alternate Function Push Pull	No pull-up and no pull-down	Very High	
	PA1	UART4_RX	Alternate Function Push Pull	No pull-up and no pull-down	Very High	\$1_L2
USART1	PA9	USART1_TX	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	
	PA10	USART1_RX	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	S2_L1
USART2	PA2	USART2_TX	Alternate Function Push Pull	No pull-up and no pull-down	Very High	
	PA3	USART2_RX	Alternate Function Push Pull	No pull-up and no pull-down	Very High	\$2_L2
USART3	PB10	USART3_TX	Alternate Function Push Pull	No pull-up and no pull-down	Very High	
	PC11	USART3_RX	Alternate Function Push Pull	No pull-up and no pull-down	Very High	S1_L1
USB_OTG_ FS	PA11	USB_OTG_FS_ DM	Alternate Function Push Pull	No pull-up and no pull-down	Very High	
	PA12	USB_OTG_FS_ DP	Alternate Function Push Pull	No pull-up and no pull-down	Very High	
GPIO	PC13	GPIO_Input	Input mode		n/a	BOOM1_LS22

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
					Орсси	
				Pull-up *		
	PC2	GPIO_Input	Input mode	Pull-up *	n/a	BOOM1_LS11
	PC3	GPIO_Input	Input mode	Pull-up *	n/a	BOOM1_LS12
	PB1	GPIO_Input	Input mode	Pull-up *	n/a	BOOM2_LS22
	PB13	GPIO_Input	Input mode	Pull-up *	n/a	BOOM2_LS11
	PB14	GPIO_Input	Input mode	Pull-up *	n/a	BOOM2_LS12
	PB15	GPIO_Input	Input mode	Pull-up *	n/a	BOOM2_LS21
	PA15	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	AT1
	PC10	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	AT
	PC12	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	LT
	PD2	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	L_FB1
	PB3	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	L_FB2
	PB7	GPIO_Input	Input mode	Pull-up *	n/a	BOOM1_LS21

4.2. DMA configuration

nothing configured in DMA service

4.3. NVIC configuration

4.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority		
Non maskable interrupt	true	0	0		
Hard fault interrupt	true	0	0		
Memory management fault	true	0	0		
Pre-fetch fault, memory access fault	true	0	0		
Undefined instruction or illegal state	true	0	0		
System service call via SWI instruction	true	0	0		
Debug monitor	true	0	0		
Pendable request for system service	true	0	0		
System tick timer	true	0	0		
TIM1 update interrupt and TIM10 global interrupt	true	0	0		
USB On The Go FS global interrupt	true	0	0		
PVD interrupt through EXTI line 16	unused				
Flash global interrupt	unused				
RCC global interrupt	unused				
ADC1, ADC2 and ADC3 interrupts	unused				
TIM1 break interrupt and TIM9 global interrupt	unused				
TIM1 trigger and commutation interrupts and TIM11 global interrupt	unused				
TIM1 capture compare interrupt	unused				
USART1 global interrupt	unused				
USART2 global interrupt	unused				
USART3 global interrupt	unused				
UART4 global interrupt	unused				
FPU global interrupt		unused			

4.3.2. NVIC Code generation

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false
Memory management fault	false	true	false
Pre-fetch fault, memory access fault	false	true	false
Undefined instruction or illegal state	false	true	false
System service call via SWI instruction	false	true	false
Debug monitor	false	true	false
Pendable request for system service	false	true	false

Enabled interrupt Table	Select for init	Generate IRQ	Call HAL handler	
	sequence ordering	handler		
System tick timer	false	true	true	
TIM1 update interrupt and TIM10 global interrupt	false	true	true	
USB On The Go FS global interrupt	false	true	true	

^{*} User modified value

5. System Views

5.1. Category view

5.1.1. Current



System Core	Analog	Timers	Connectivity	Multimedia	Computing
DMA	ADC1 ♥	тім1 ♥	UART4 ❖		
GPIO ⊘			USART1 ♥		
NVIC 🔗			USART2 ⊘		
RCC ⊘			USART3 ⊘		
sys 🤡			USB_FS ♥		

6. Docs & Resources

Type Link

BSDL files https://www.st.com/resource/en/bsdl_model/stm32f446_bsdl.zip https://www.st.com/resource/en/ibis_model/stm32f446_ibis.zip

System View https://www.st.com/resource/en/svd/stm32f4-svd.zip

Description

Presentations https://www.st.com/resource/en/product_presentation/stm32-

stm8_embedded_software_solutions.pdf

Presentations https://www.st.com/resource/en/product_presentation/stm32_eval-

tools_portfolio.pdf

Presentations https://www.st.com/resource/en/product_presentation/stm32_stm8_functi

onal-safety-packages.pdf

Presentations https://www.st.com/resource/en/product_presentation/stm32-

stm8_software_development_tools.pdf

Presentations https://www.st.com/resource/en/product_presentation/microcontrollers-

stm32-family-overview.pdf

Brochures https://www.st.com/resource/en/brochure/products-and-solutions-for-plcs-

and-smart-i-os.pdf

Flyers https://www.st.com/resource/en/flyer/flstm32nucleo.pdf

Flyers https://www.st.com/resource/en/flyer/flstm32trust.pdf

Product https://www.st.com/resource/en/certification_document/stm32_authenticat

Certifications ion_can.pdf

Security Bulletin https://www.st.com/resource/en/technical_note/tn1489-security-bulletin-

tn1489stpsirt-physical-attacks-on-stm32-and-stm32cube-firmware-

stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an1709-emc-design-

guide-for-stm8-stm32-and-legacy-mcus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an2606-stm32-

microcontroller-system-memory-boot-mode-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an2945-stm8s-and-

stm32-mcus-a-consistent-832bit-product-line-for-painless-migration-

- stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an3070-managing-the-driver-enable-signal-for-rs485-and-iolink-communications-with-the-stm32s-usart-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an3126-audio-and-waveform-generation-using-the-dac-in-stm32-products-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an3155-usart-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an3156-usb-dfu-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an3364-migration-and-compatibility-guidelines-for-stm32-microcontroller-applications-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an3997-audio-playback-and-recording-using-the-stm32f4discovery-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an3998-pdm-audio-software-decoding-on-stm32-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4031-using-the-stm32f2-stm32f4-and-stm32f7-series-dma-controller-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4221-i2c-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf
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- Application Notes https://www.st.com/resource/en/application_note/an4488-getting-started-with-stm32f4xxxx-mcu-hardware-development-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4640-peripherals-interconnections-on-stm32f4057xx-stm32f4157xx-stm32f42xxx-stm32f43xxx-stm32f446xx-and-stm32f469479xx-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4655-virtually-increasing-the-number-of-serial-communication-peripherals-in-stm32-applications-stmicroelectronics.pdf

- Application Notes https://www.st.com/resource/en/application_note/an4658-migration-of-applications-from-stm32f429439-lines-to-stm32f446-line-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4739-stm32cube-firmware-examples-for-stm32f4-series-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4750-handling-of-soft-errors-in-stm32-applications-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4776-generalpurpose-timer-cookbook-for-stm32-microcontrollers-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4803-highspeed-si-simulations-using-ibis-and-boardlevel-simulations-using-hyperlynx-si-on-stm32-mcus-and-mpus-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4850-stm32-mcusspreadspectrum-clock-generation-principles-properties-andimplementation-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4989-stm32-microcontroller-debug-toolbox-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4995-using-anelectromyogram-technique-to-detect-muscle-activitystmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an5027-interfacing-pdm-digital-microphones-using-stm32-mcus-and-mpus-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an5073-receiving-spdif-audio-stream-with-the-stm32f4f7h7-series-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4760-quadspiinterface-on-stm32-microcontrollers-and-microprocessors-stmicroelectronics.pdf
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- Application Notes https://www.st.com/resource/en/application_note/an5612-esd-protection-of-stm32-mcus-and-mpus-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4838-introduction-to-

- memory-protection-unit-management-on-stm32-mcusstmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4879-introduction-to-usb-hardware-and-pcb-guidelines-using-stm32-mcus-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an5225-introduction-to-usb-typec-power-delivery-for-stm32-mcus-and-mpus-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an5537-how-to-use-adcoversampling-techniques-to-improve-signaltonoise-ratio-on-stm32-mcusstmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an5036-guidelines-for-thermal-management-on-stm32-applications-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4230-introduction-to-random-number-generation-validation-using-the-nist-statistical-test-suite-for-stm32-mcus-and-mpus-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an2867-guidelines-for-oscillator-design-on-stm8afals-and-stm32-mcusmpus-stmicroelectronics.pdf
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- Application Notes https://www.st.com/resource/en/application_note/an4277-how-to-use-pwm-shutdown-for-motor-control-and-digital-power-conversion-on-stm32-mcus-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4759-introduction-to-using-the-hardware-realtime-clock-rtc-and-the-tamper-management-unit-tamp-with-stm32-mcus-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4908-getting-startedwith-usart-automatic-baud-rater-detection-for-stm32-mcusstmicroelectronics.pdf
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stmicroelectronics.pdf

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