

IO			
P1		P2	
PE0	1 2	PB9	PB7
PE2	3 4	PE5	PB5
PE4	5 6	PE3	PB3
PE6	5 6	PE1	PB1
PF0	7 8	PC13	PG13
PF2	9 10	PG11	PG10
PF4	11 12	PG9	PG8
PF6	13 14	PD5	PD7
PF8	15 16	PD4	PD3
PF10	17 18	PD2	PD1
PC1	19 20	PD0	PD0
PC3	21 22	PC11	PC10
PC5	23 24	PA0	PA14
PC7	25 26	PA2	PA12
PA5	27 28	PA4	PA8
PA7	29 30	PA6	PC8
PC9	31 32	PC7	PC6
PC11	33 34	PB0	PG7
PF11	35 36	PG6	PG5
PF13	37 38	PG4	PG3
PF15	39 40	PG2	PD15
PG1	41 42	PD14	PD13
PG3	43 44	PD12	PD11
PE10	45 46	PD10	PD9
PE12	47 48	PD8	PD7
PE14	49 50	PB14	PB15
PB10	51 52	PB12	PB13
	53 54		PB11

[illegible]

# USB Slave

The diagram illustrates the wiring for a USB Slave device. A USB connector (labeled USB1) is shown with the following connections:

- Pin 5 (shell):** Connected to GND.
- Pin 4 (GND):** Connected to GND.
- Pin 3 (D+):** Connected to a 10K resistor (R2), which is then connected to VCC3.
- Pin 2 (D-):** Connected to a 10K resistor (R1), which is then connected to VCC3.
- Pin 1 (VBUS):** Connected to VCC5.

The diagram is titled "USB Slave" in large red text.

TFTLCD				
LCD				
FSMC_NF4	1	2	FSMC_A6	
FSMC_NWE	3	4	FSMC_NOE	
RESET	5	6	FSMC_D0	
FSMC_D1	7	8	FSMC_D2	
FSMC_D3	9	10	FSMC_D4	
FSMC_D5	11	12	FSMC_D6	
FSMC_D7	13	14	FSMC_D8	
FSMC_D9	15	16	FSMC_D10	
FSMC_D11	17	18	FSMC_D12	
FSMC_D13	19	20	FSMC_D14	
FSMC_D15	21	22	GND	
LCD_BL	23	24	VCC3.3	
VCC3.3	25	26	GND	
GND	27	28	VCC3.3	
T_MISO	29	30	T_MOSI	
T_PEN	31	32		
T_CS	33	34	T_SCK	

[illegible][illegible]

## SPI FLASH

Diagram illustrating the SPI FLASH connection. The components and connections are as follows:

- FLASH\_CS** (Pin 1) is connected to **U2** pin 1 (**CS**).
- SPI\_MISO** (Pin 2) is connected to **U2** pin 2 (**SI(101)**).
- VCC3.3** (Pin 3) is connected to **U2** pin 3 (**VCC**) and **U2** pin 4 (**SO(101)**).
- GND** (Pin 4) is connected to **U2** pin 4 (**GND**).
- SPI\_MOSI** (Pin 5) is connected to **U2** pin 5 (**SI(100)**).
- SPI\_SCK** (Pin 6) is connected to **U2** pin 6 (**CLK**).
- VCC3.3** (Pin 7) is connected to **U2** pin 7 (**VCC**) and **U2** pin 8 (**RST(103)**).
- GND** (Pin 8) is connected to **U2** pin 8 (**GND**).
- A **104** ohm resistor is connected between **U2** pin 7 and **GND**.
- The chip is labeled **W25Q16**.

[illegible]

## BOOT

BOOT pin configuration circuit diagram showing connections to VCC3.3, GND, and resistors R20, R21, R22, and R23.

## RESET

RESET pin configuration circuit diagram showing connections to VCC3.3, GND, and resistors R19, R20, R21, and R22.